SPEC® CINT2006 Result
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Dell Inc.

PowerEdge M600 (Intel Xeon E5420, 2.50 GHz)

SPECint®2006 = 23.9

SPECint_base2006 = 20.9

Benchmark	Peak	Base
400.perlbench	21.4	17.2
401.bzip2	15.0	14.0
403.gcc	17.8	13.4
429.mcf	25.6	25.6
445.gobmk	18.4	16.9
456.hmmer	21.7	14.1
458.sjeng	17.9	17.0
462.libquantum		228
464.h264ref	29.3	24.5
471.omnetpp	15.7	14.0
473.astar	14.8	12.6
483.xalancbmk	21.5	

SPECint_base2006 = 20.9

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Hardware

CPU Name: Intel Xeon E5420
CPU Characteristics:
CPU MHz: 2500
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem: 1 x 80 GB 5400 RPM SATA
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cc_b_11.0.042
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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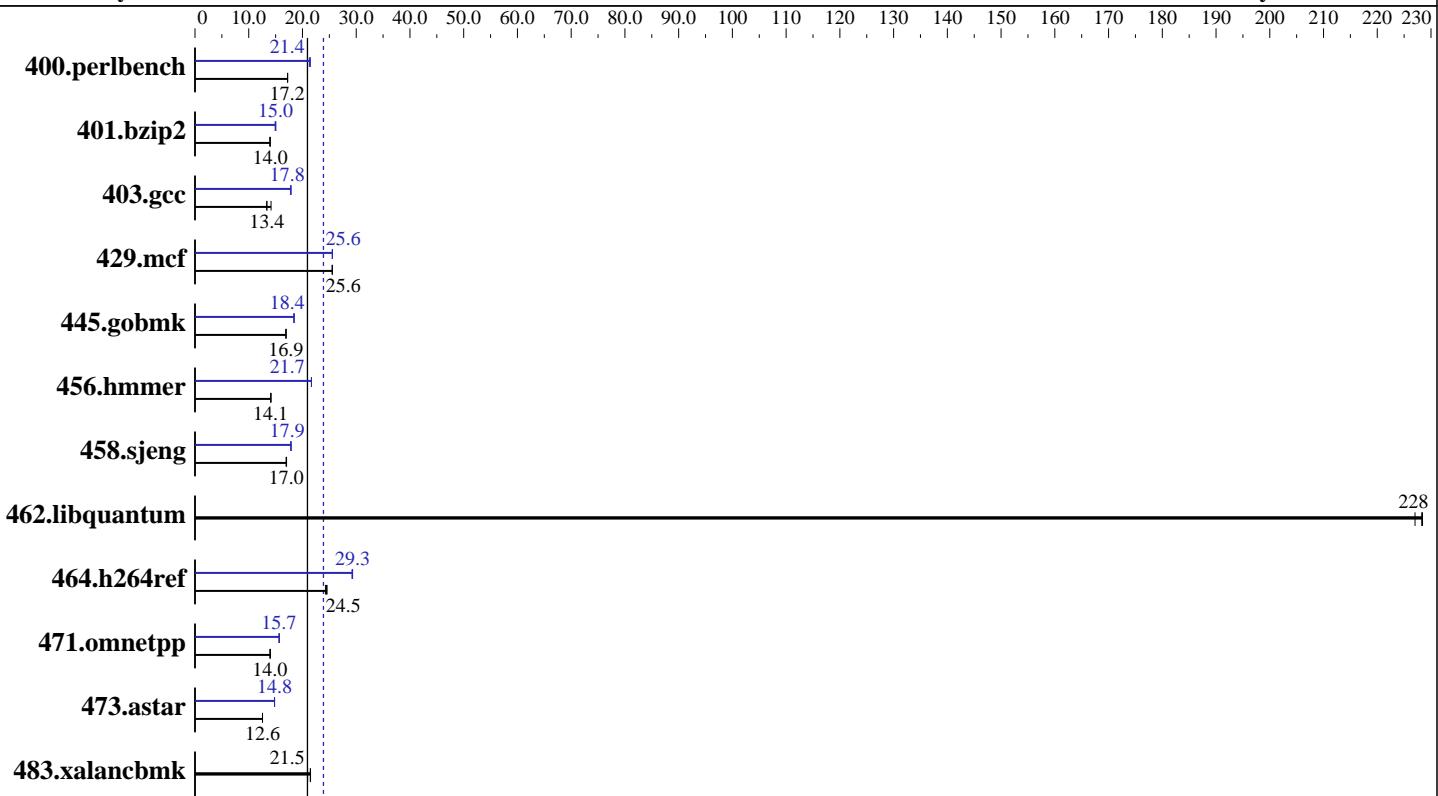
CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**567**	**17.2**	566	17.3	568	17.2	456	21.4	**456**	**21.4**	459	21.3
401.bzip2	693	13.9	690	14.0	**690**	**14.0**	642	15.0	647	14.9	**643**	**15.0**
403.gcc	603	13.3	**601**	**13.4**	569	14.2	**451**	**17.8**	452	17.8	451	17.9
429.mcf	**357**	**25.6**	357	25.5	357	25.6	356	25.6	357	25.5	**357**	**25.6**
445.gobmk	620	16.9	**620**	**16.9**	619	16.9	569	18.4	**569**	**18.4**	569	18.4
456.hmmer	**661**	**14.1**	661	14.1	661	14.1	430	21.7	**431**	**21.7**	431	21.7
458.sjeng	710	17.0	**711**	**17.0**	713	17.0	678	17.8	677	17.9	**677**	**17.9**
462.libquantum	90.7	228	91.3	227	**90.8**	**228**	90.7	228	91.3	227	**90.8**	**228**
464.h264ref	900	24.6	**905**	**24.5**	912	24.3	**755**	**29.3**	758	29.2	755	29.3
471.omnetpp	448	14.0	446	14.0	**447**	**14.0**	**399**	**15.7**	400	15.6	398	15.7
473.astar	**559**	**12.6**	559	12.6	559	12.6	474	14.8	**474**	**14.8**	474	14.8
483.xalancbmk	329	21.0	**322**	**21.5**	321	21.5	329	21.0	**322**	**21.5**	321	21.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
           -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
           -I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

```
456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
           -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
           -I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Continued on next page

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Peak Optimization Flags (Continued)

```
  400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                 -no-prec-div -static -ansi-alias -opt-prefetch

       401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                 -no-prec-div -static -auto-ilp32 -opt-prefetch
                 -ansi-alias

         403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
                 -opt-malloc-options=3

         429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

       445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
                 -no-prec-div -ansi-alias

      456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
                 -ansi-alias -auto-ilp32

       458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                 -no-prec-div -static -unroll4

   462.libquantum: basepeak = yes

      464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                 -no-prec-div -static -unroll2 -ansi-alias
```

C++ benchmarks:

```
     471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                 -no-prec-div -ansi-alias -opt-ra-region-strategy=block
                 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

        473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
                 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

    483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
         403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html

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You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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