



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = Not Run

ProLiant DL360 G5
(2.66 GHz, Intel Xeon E5430)

SPECint_rate_base2006 = 102

CPU2006 license: 3

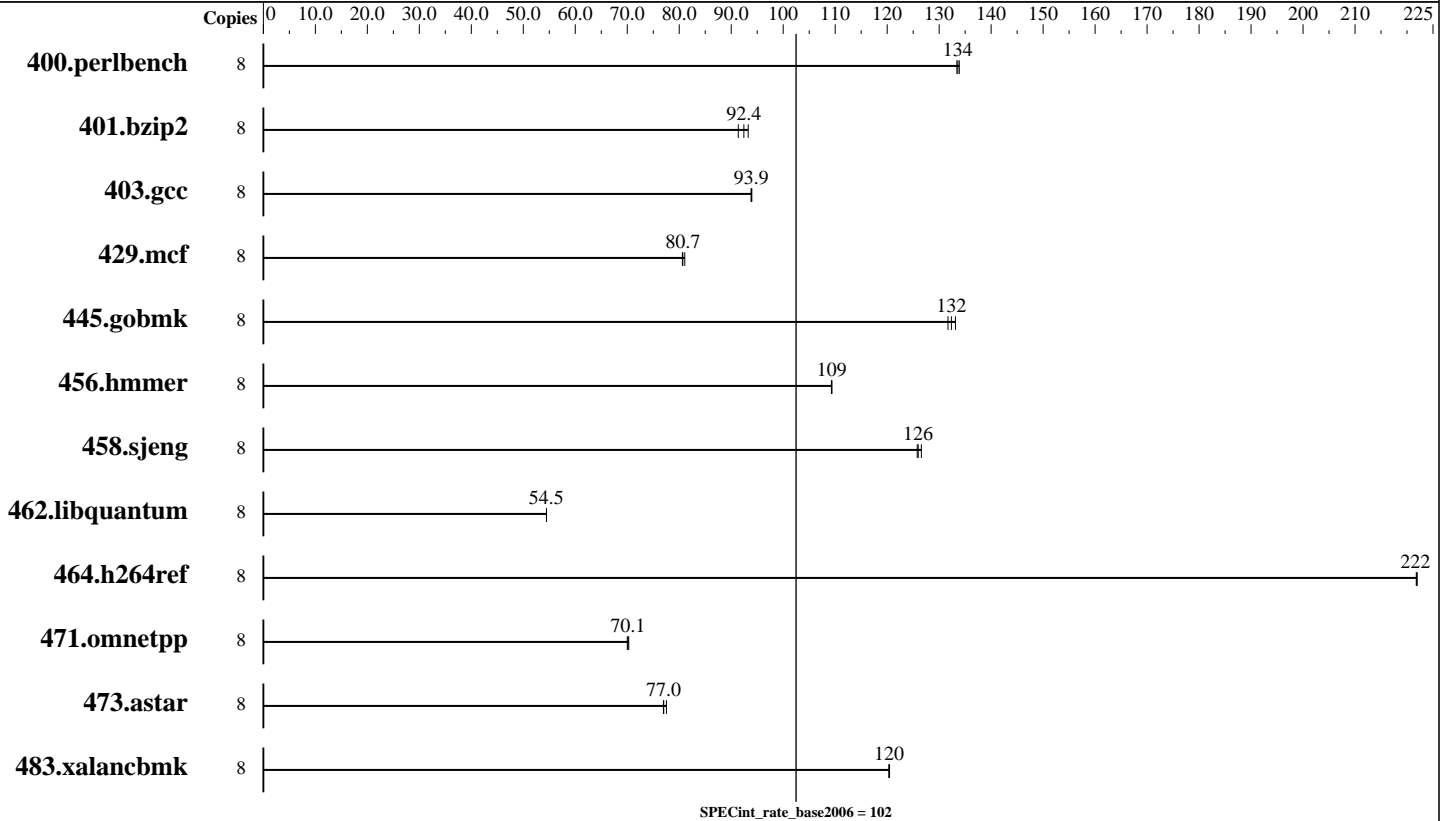
Test date: Sep-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2008

Tested by: Hewlett-Packard Company

Software Availability: Jun-2008



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 2.66 GHz, 2x6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20080602 Package ID: l_cc_p_10.1.017
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	584	134	<u>585</u>	<u>134</u>	586	133							
401.bzip2	8	845	91.4	<u>835</u>	<u>92.4</u>	828	93.3							
403.gcc	8	685	94.0	<u>686</u>	<u>93.9</u>	686	93.8							
429.mcf	8	900	81.1	<u>904</u>	<u>80.7</u>	905	80.6							
445.gobmk	8	630	133	637	132	<u>634</u>	<u>132</u>							
456.hammer	8	683	109	683	109	<u>683</u>	<u>109</u>							
458.sjeng	8	<u>768</u>	<u>126</u>	765	127	770	126							
462.libquantum	8	3043	54.5	<u>3044</u>	<u>54.5</u>	3044	54.5							
464.h264ref	8	798	222	798	222	<u>798</u>	<u>222</u>							
471.omnetpp	8	<u>713</u>	<u>70.1</u>	711	70.3	714	70.0							
473.astar	8	<u>729</u>	<u>77.0</u>	724	77.5	730	77.0							
483.xalanbmk	8	459	120	458	120	<u>458</u>	<u>120</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled
Hardware Prefetcher Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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