



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

### SPECint®\_rate2006 = 68.2

PRIMERGY RX200 S4, Intel Xeon E5430, 2.66 GHz

### SPECint\_rate\_base2006 = 57.3

CPU2006 license: 22

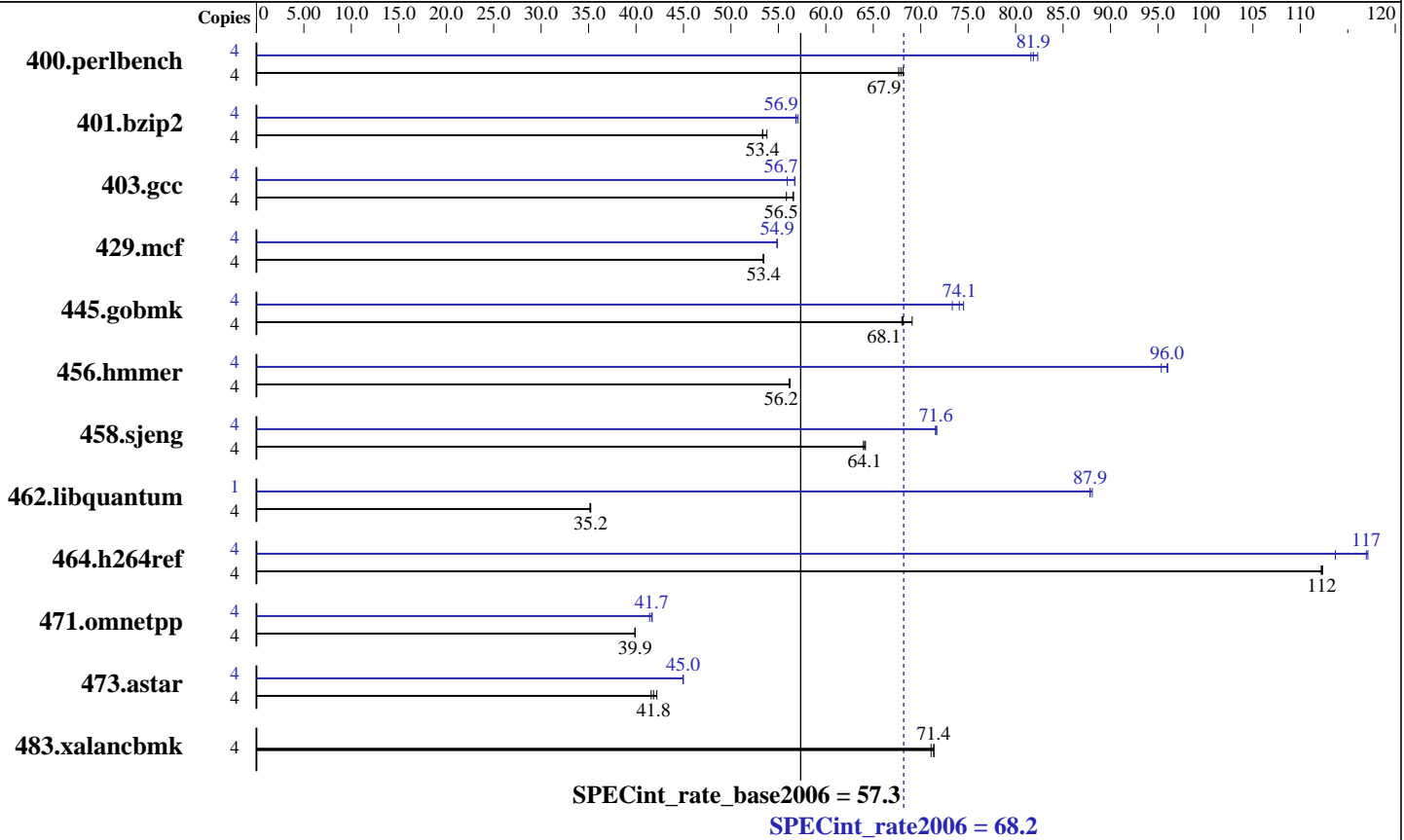
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Sep-2008

Hardware Availability: Dec-2007

Software Availability: May-2008



### Hardware

CPU Name: Intel Xeon E5430  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)  
 Disk Subsystem: 1x SAS, 73 GB, 15000 rpm  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) with SP2, kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap Library, Version 8.1  
 binutils-2.17.50.0.5-0.1.x86\_64



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	573	68.1	577	67.7	<u>575</u>	<u>67.9</u>	4	<u>477</u>	<u>81.9</u>	479	81.6	475	82.3
401.bzip2	4	<u>723</u>	<u>53.4</u>	718	53.8	724	53.3	4	<u>679</u>	<u>56.9</u>	676	57.1	679	56.9
403.gcc	4	569	56.6	<u>570</u>	<u>56.5</u>	577	55.8	4	<u>568</u>	<u>56.7</u>	576	55.9	568	56.7
429.mcf	4	683	53.4	<u>683</u>	<u>53.4</u>	682	53.5	4	<u>665</u>	<u>54.9</u>	665	54.9	665	54.9
445.gobmk	4	<u>616</u>	<u>68.1</u>	607	69.1	617	68.0	4	<u>567</u>	<u>74.1</u>	563	74.5	572	73.3
456.hmmmer	4	665	56.1	<u>664</u>	<u>56.2</u>	664	56.2	4	389	96.0	391	95.3	<u>389</u>	<u>96.0</u>
458.sjeng	4	<u>755</u>	<u>64.1</u>	754	64.2	757	64.0	4	<u>676</u>	<u>71.6</u>	676	71.6	675	71.7
462.libquantum	4	2359	35.1	2353	35.2	<u>2354</u>	<u>35.2</u>	1	236	87.8	235	88.1	<u>236</u>	<u>87.9</u>
464.h264ref	4	789	112	<u>788</u>	<u>112</u>	788	112	4	779	114	756	117	<u>757</u>	<u>117</u>
471.omnetpp	4	627	39.9	<u>626</u>	<u>39.9</u>	626	39.9	4	<u>600</u>	<u>41.7</u>	603	41.4	599	41.7
473.astar	4	<u>671</u>	<u>41.8</u>	665	42.2	675	41.6	4	624	45.0	<u>624</u>	<u>45.0</u>	625	44.9
483.xalancbmk	4	<u>387</u>	<u>71.4</u>	387	71.4	388	71.1	4	<u>387</u>	<u>71.4</u>	387	71.4	388	71.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

All binaries were built with 32-bit Intel compiler except:  
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores (default)

## Platform Notes

BIOS configuration:  
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable

## General Notes

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include

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## Base Compiler Invocation (Continued)

C++ benchmarks:

```
/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

```
401.bzip2: icc
```

```
456.hmmer: icc
```

C++ benchmarks:

```
/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
 -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
 -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
 -opt-streaming-stores always -vec-guard-write  
 -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
 -no-prec-div -ansi-alias -opt-ra-region-strategy=block  
 -Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
 -Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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