



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 286

Acer Altos R920 (Intel Xeon X7460)

SPECint_rate_base2006 = 263

CPU2006 license: 97

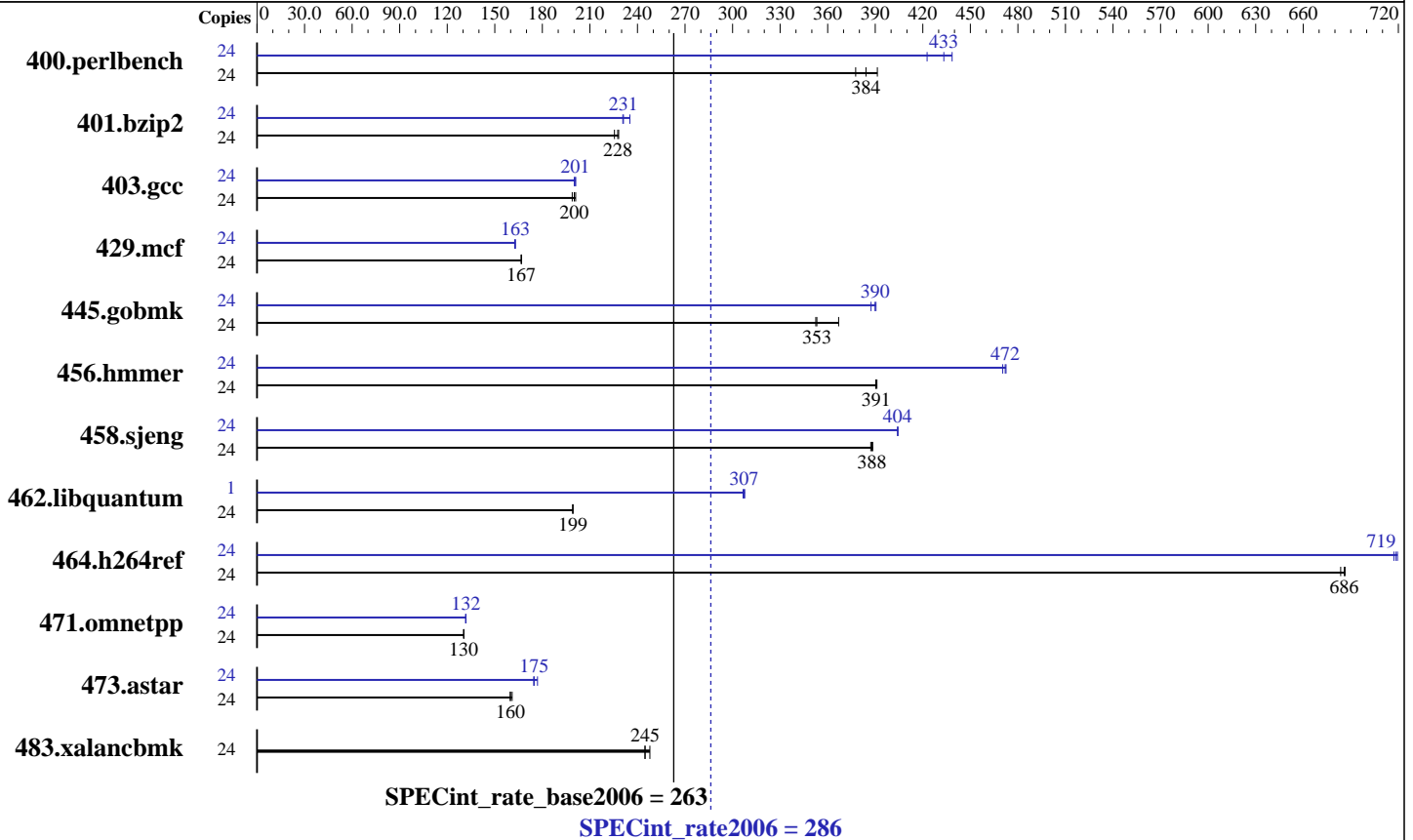
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Sep-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X7460
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip
 CPU(s) orderable: 1, 2, 4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 9 MB I+D on chip per chip, 3 MB shared / 2 cores
 L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16 x 2048MB ECC FB-DIMM DDR2-667 CL5)
 Disk Subsystem: 1 x 73GB 10000PRM SAS HDD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	610	384	621	378	599	391	24	555	423	541	433	535	439
401.bzip2	24	1027	225	1015	228	1018	228	24	1004	231	1002	231	985	235
403.gcc	24	965	200	972	199	961	201	24	961	201	965	200	963	201
429.mcf	24	1313	167	1313	167	1311	167	24	1343	163	1344	163	1346	163
445.gobmk	24	713	353	686	367	714	352	24	645	390	646	390	650	387
456.hmmmer	24	573	391	573	391	574	390	24	474	472	474	473	476	470
458.sjeng	24	750	387	749	388	748	388	24	719	404	718	404	718	404
462.libquantum	24	2497	199	2495	199	2496	199	1	67.3	308	67.6	307	67.4	307
464.h264ref	24	777	684	774	686	774	686	24	741	717	739	719	738	719
471.omnetpp	24	1150	130	1150	130	1150	130	24	1138	132	1140	132	1139	132
473.astar	24	1047	161	1054	160	1055	160	24	952	177	965	175	963	175
483.xalancbmk	24	668	248	676	245	677	245	24	668	248	676	245	677	245

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode
 taskset was used to bind processes to cores except for 462.libquantum peak
 OMP_NUM_THREADS set to number of processors
 KMP_AFFINITY set to "physical,0"
 KMP_STACKSIZE set to 64M
 "ulimit -s unlimited" was set for stacksize unlimited

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.10.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.10.xml>

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For other inquiries, please contact webmaster@spec.org.

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