



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint®2006 = 25.0

Intel Server System S7000FC4UR (Intel Xeon X7460)

SPECint_base2006 = 21.7

CPU2006 license: 13

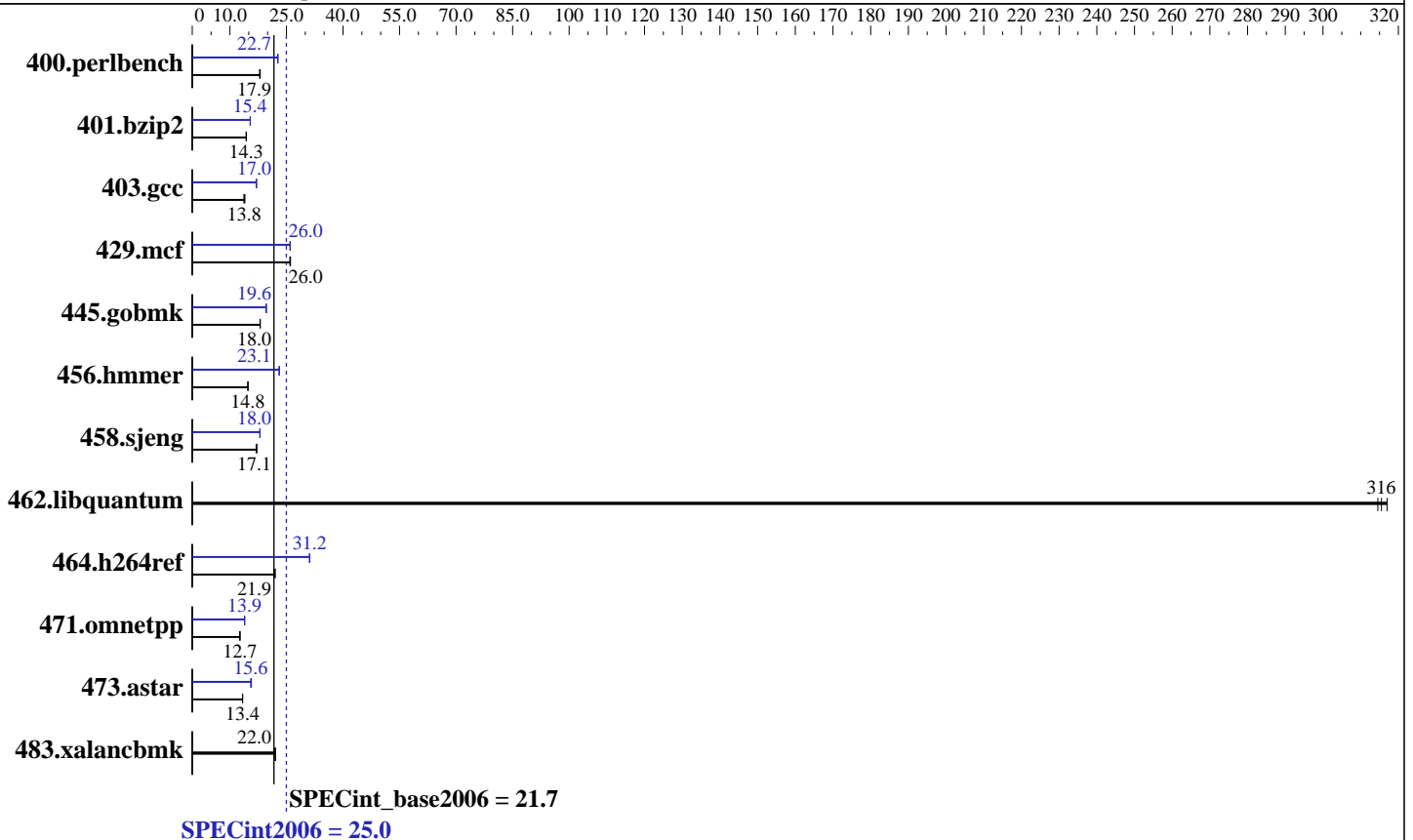
Test date: Sep-2008

Test sponsor: Intel Corporation

Hardware Availability: Sep-2008

Tested by: Intel Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X7460
 CPU Characteristics:
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 9 MB I+D on chip per chip, 3 MB shared / 2 cores
 L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16x2 GB Micron DDR2 5300F, CL5-5-5, 2 rank, ECC)
 Disk Subsystem: 73 GB Seagate SAS, 10K RPM
 Other Hardware: None

Software

Operating System: SuSe Linux SLES10 SP2
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 25.0

Intel Server System S7000FC4UR (Intel Xeon X7460)

SPECint_base2006 = 21.7

CPU2006 license: 13

Test date: Sep-2008

Test sponsor: Intel Corporation

Hardware Availability: Sep-2008

Tested by: Intel Corporation

Software Availability: Nov-2008

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	545	17.9	542	18.0	545	17.9	429	22.8	431	22.7	431	22.7
401.bzip2	675	14.3	671	14.4	674	14.3	627	15.4	626	15.4	628	15.4
403.gcc	585	13.8	585	13.8	576	14.0	472	17.0	472	17.1	474	17.0
429.mcf	352	25.9	350	26.1	350	26.0	350	26.0	351	26.0	351	26.0
445.gobmk	582	18.0	582	18.0	582	18.0	535	19.6	534	19.6	534	19.6
456.hmmmer	631	14.8	631	14.8	632	14.8	404	23.1	405	23.0	403	23.1
458.sjeng	711	17.0	706	17.1	709	17.1	674	18.0	674	18.0	674	17.9
462.libquantum	65.4	317	65.7	316	65.9	315	65.4	317	65.7	316	65.9	315
464.h264ref	1018	21.7	1009	21.9	1005	22.0	710	31.2	712	31.1	710	31.2
471.omnetpp	494	12.7	496	12.6	493	12.7	450	13.9	450	13.9	450	13.9
473.astar	526	13.3	526	13.4	525	13.4	451	15.6	451	15.6	447	15.7
483.xalancbmk	313	22.0	313	22.0	313	22.0	313	22.0	313	22.0	313	22.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetcher: Enabled
High Bandwidth Option: Disabled

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 25.0

Intel Server System S7000FC4UR (Intel Xeon X7460)

SPECint_base2006 = 21.7

CPU2006 license: 13

Test date: Sep-2008

Test sponsor: Intel Corporation

Hardware Availability: Sep-2008

Tested by: Intel Corporation

Software Availability: Nov-2008

Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

```
456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

C++ benchmarks:

icpc

Peak Portability Flags

```
400.perlbenc: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 25.0

Intel Server System S7000FC4UR (Intel Xeon X7460)

SPECint_base2006 = 21.7

CPU2006 license: 13

Test date: Sep-2008

Test sponsor: Intel Corporation

Hardware Availability: Sep-2008

Tested by: Intel Corporation

Software Availability: Nov-2008

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.06.html>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 25.0

Intel Server System S7000FC4UR (Intel Xeon X7460)

SPECint_base2006 = 21.7

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Sep-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.06.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 20:57:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 October 2008.