



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp[®]_rate2006 = 48.5

ProLiant DL380 G5
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 45.1

CPU2006 license: 3

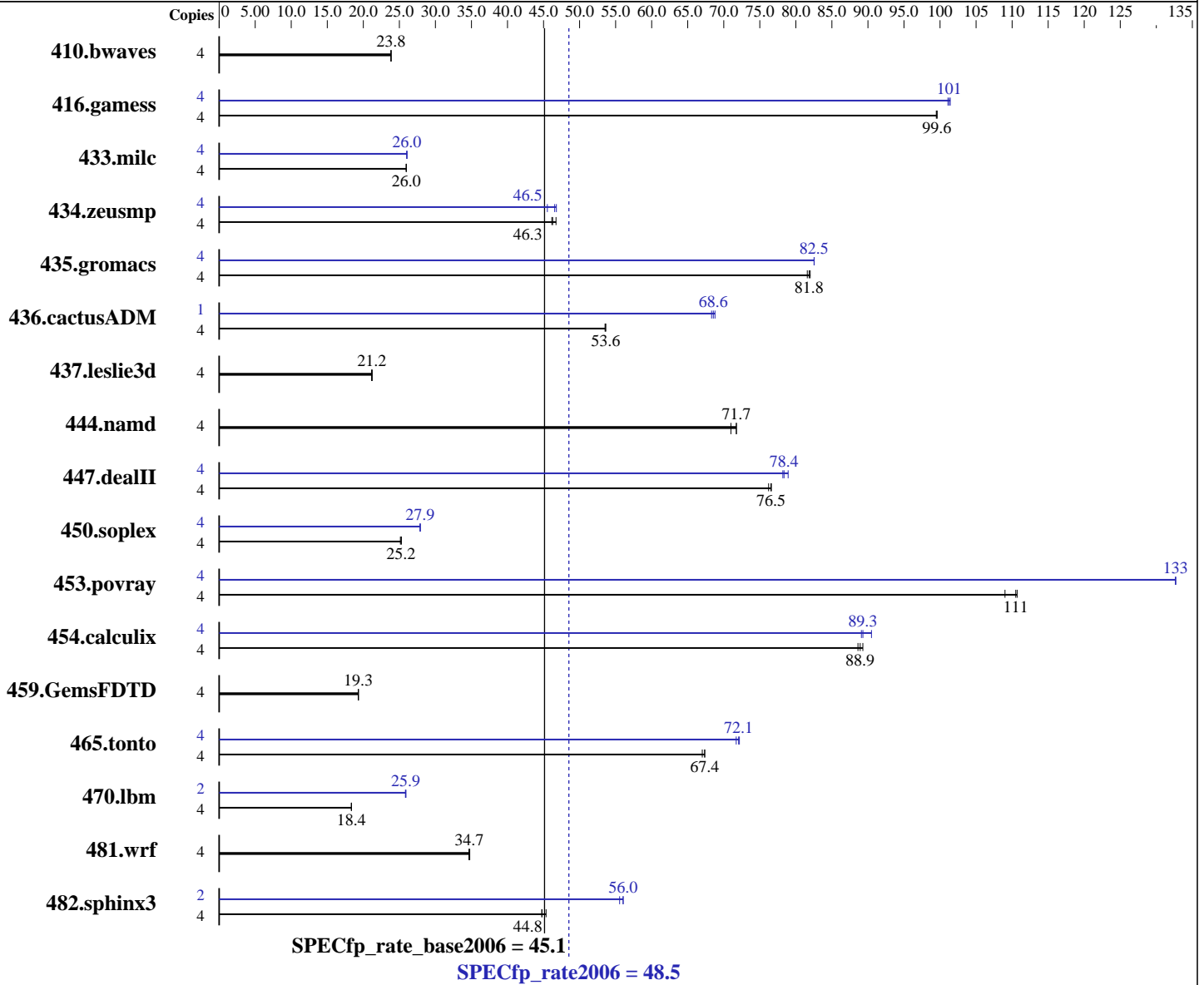
Test sponsor: Hewlett-Packard Company

Test date: Sep-2008

Hardware Availability: Sep-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5470
 CPU Characteristics: 3.33 GHz, 2x6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smpp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 48.5

ProLiant DL380 G5
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 45.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 1x72 GB 10 K SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 8.1
Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2281	23.8	2279	23.8	<u>2280</u>	<u>23.8</u>	4	2281	23.8	2279	23.8	<u>2280</u>	<u>23.8</u>
416.gamess	4	787	99.5	786	99.6	<u>787</u>	<u>99.6</u>	4	<u>773</u>	<u>101</u>	772	101	775	101
433.milc	4	1415	25.9	<u>1414</u>	<u>26.0</u>	1414	26.0	4	<u>1410</u>	<u>26.0</u>	1410	26.0	1410	26.0
434.zeusmp	4	779	46.7	<u>787</u>	<u>46.3</u>	789	46.2	4	778	46.8	<u>782</u>	<u>46.5</u>	800	45.5
435.gromacs	4	<u>349</u>	<u>81.8</u>	350	81.6	348	82.0	4	346	82.5	346	82.5	<u>346</u>	<u>82.5</u>
436.cactusADM	4	893	53.5	892	53.6	<u>892</u>	<u>53.6</u>	1	175	68.3	<u>174</u>	<u>68.6</u>	174	68.8
437.leslie3d	4	1772	21.2	1778	21.2	<u>1774</u>	<u>21.2</u>	4	1772	21.2	1778	21.2	<u>1774</u>	<u>21.2</u>
444.namd	4	<u>447</u>	<u>71.7</u>	447	71.8	452	71.0	4	<u>447</u>	<u>71.7</u>	447	71.8	452	71.0
447.dealII	4	597	76.6	600	76.2	<u>598</u>	<u>76.5</u>	4	580	78.9	585	78.2	<u>584</u>	<u>78.4</u>
450.soplex	4	<u>1322</u>	<u>25.2</u>	1327	25.1	1320	25.3	4	1197	27.9	1196	27.9	<u>1196</u>	<u>27.9</u>
453.povray	4	<u>193</u>	<u>111</u>	195	109	192	111	4	<u>160</u>	<u>133</u>	160	133	160	133
454.calculix	4	<u>371</u>	<u>88.9</u>	372	88.6	370	89.3	4	370	89.1	365	90.5	<u>370</u>	<u>89.3</u>
459.GemsFDTD	4	2199	19.3	<u>2198</u>	<u>19.3</u>	2196	19.3	4	2199	19.3	<u>2198</u>	<u>19.3</u>	2196	19.3
465.tonto	4	587	67.0	<u>584</u>	<u>67.4</u>	584	67.4	4	545	72.2	549	71.7	<u>546</u>	<u>72.1</u>
470.lbm	4	2997	18.3	<u>2995</u>	<u>18.4</u>	2994	18.4	2	1061	25.9	1064	25.8	<u>1061</u>	<u>25.9</u>
481.wrf	4	1285	34.8	<u>1288</u>	<u>34.7</u>	1289	34.7	4	1285	34.8	<u>1288</u>	<u>34.7</u>	1289	34.7
482.sphinx3	4	1719	45.4	<u>1741</u>	<u>44.8</u>	1742	44.8	2	<u>696</u>	<u>56.0</u>	695	56.0	702	55.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 436.cactusADM peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 48.5

ProLiant DL380 G5
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 45.1

CPU2006 license: 3

Test date: Sep-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008

Platform Notes

BIOS configuration:

Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled
Hardware Prefetcher Disabled

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 48.5

ProLiant DL380 G5
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 45.1

CPU2006 license: 3

Test date: Sep-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008

Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380 G5
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate2006 = 48.5

SPECfp_rate_base2006 = 45.1

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Sep-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380 G5
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate2006 = 48.5

SPECfp_rate_base2006 = 45.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Peak Optimization Flags (Continued)

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.20090713.html>
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.20090713.xml>
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 20:57:32 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 October 2008.