



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL580 G5
(2.66 GHz, Intel Xeon X7460)

SPECfp[®]_rate2006 = 81.4

SPECfp_rate_base2006 = 73.9

CPU2006 license: 3

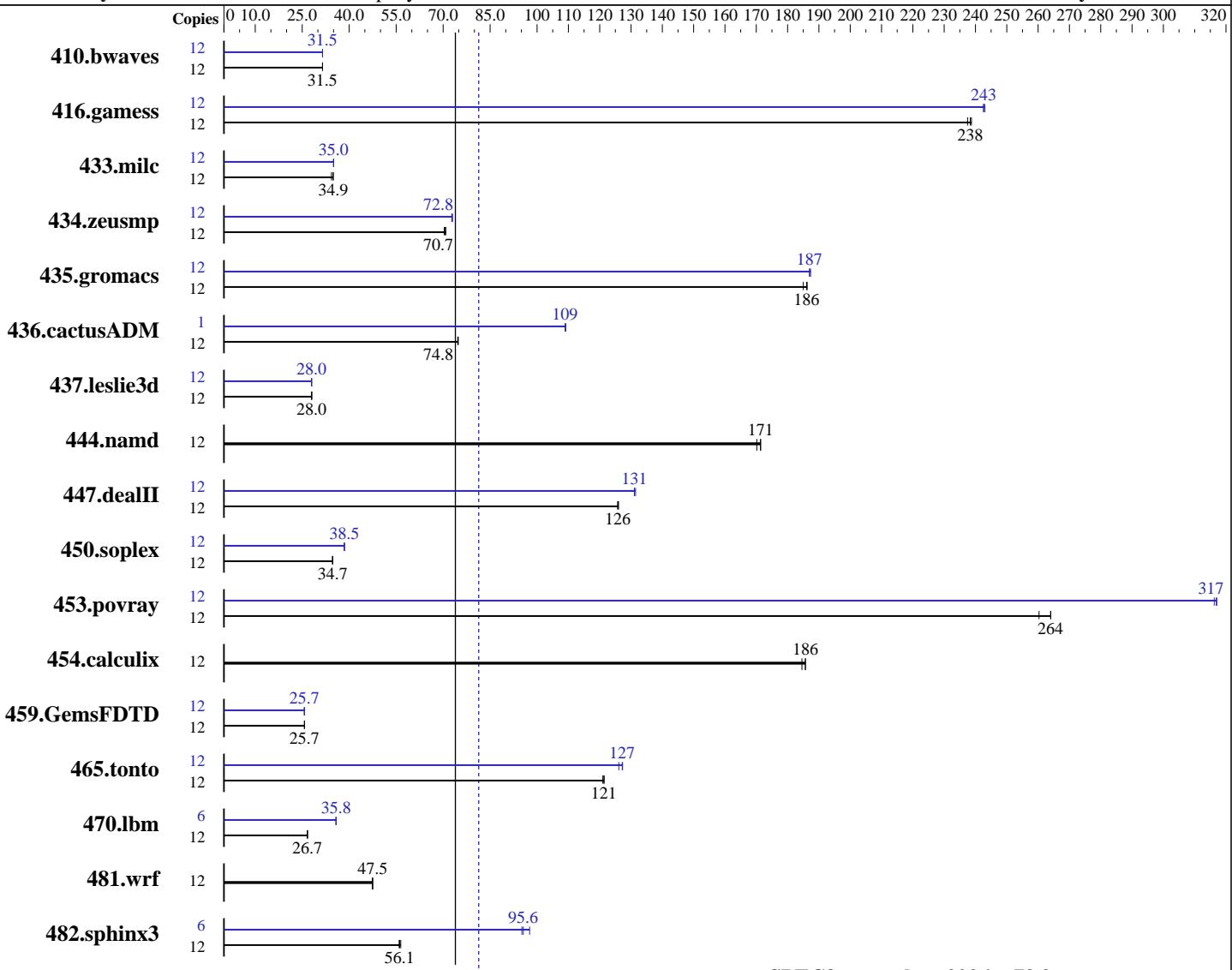
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Sep-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



SPECfp_rate_base2006 = 73.9

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Hardware

CPU Name: Intel Xeon X7460
CPU Characteristics: 2.66 GHz, 16 MB shared, 1333 MHz system bus
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 9 MB I+D on chip per chip, 3 MB shared / 2 cores

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 16 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (16x4 GB PC2-5300F CL5)
Disk Subsystem: 1x146 GB 10 K SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	5180	31.5	5180	31.5	5180	31.5	12	5180	31.5	5180	31.5	5179	31.5
416.gamess	12	989	237	984	239	986	238	12	969	243	968	243	967	243
433.milc	12	3157	34.9	3157	34.9	3208	34.3	12	3146	35.0	3146	35.0	3146	35.0
434.zeusmp	12	1542	70.8	1552	70.3	1545	70.7	12	1499	72.8	1501	72.8	1496	73.0
435.gromacs	12	460	186	460	186	463	185	12	458	187	457	187	458	187
436.cactusADM	12	1917	74.8	1921	74.7	1918	74.8	1	110	109	109	109	110	109
437.leslie3d	12	4025	28.0	4026	28.0	4028	28.0	12	4028	28.0	4029	28.0	4029	28.0
444.namd	12	566	170	562	171	562	171	12	566	170	562	171	562	171
447.dealII	12	1091	126	1092	126	1089	126	12	1047	131	1044	131	1047	131
450.soplex	12	2884	34.7	2883	34.7	2883	34.7	12	2602	38.5	2602	38.5	2600	38.5
453.povray	12	245	260	242	264	242	264	12	201	317	202	316	201	317
454.calculix	12	533	186	533	186	536	185	12	533	186	533	186	536	185
459.GemsFDTD	12	4952	25.7	4950	25.7	4949	25.7	12	4957	25.7	4955	25.7	4956	25.7
465.tonto	12	973	121	976	121	973	121	12	928	127	927	127	936	126
470.lbm	12	6185	26.7	6184	26.7	6184	26.7	6	2301	35.8	2302	35.8	2303	35.8
481.wrf	12	2825	47.4	2824	47.5	2823	47.5	12	2825	47.4	2824	47.5	2823	47.5
482.sphinx3	12	4142	56.5	4168	56.1	4180	55.9	6	1198	97.6	1229	95.1	1223	95.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

taskset was used to bind processes to cores except for 436.cactusADM peak

For peak modules using 1/2 the number of available cores, copies were each assigned to a single L2 cache using mysubmit.pl script. See the flags description file for mysubmit.pl details.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: /opt/intel/Compiler/11.0/042/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xsse4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xsse4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.20090713.html>
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.20090713.xml>
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

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