SPEC® CINT2006 Result
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IBM Corporation

$SPECint^®\_rate2006 =$ Not Run

IBM System x3550 (Intel Xeon X5470)
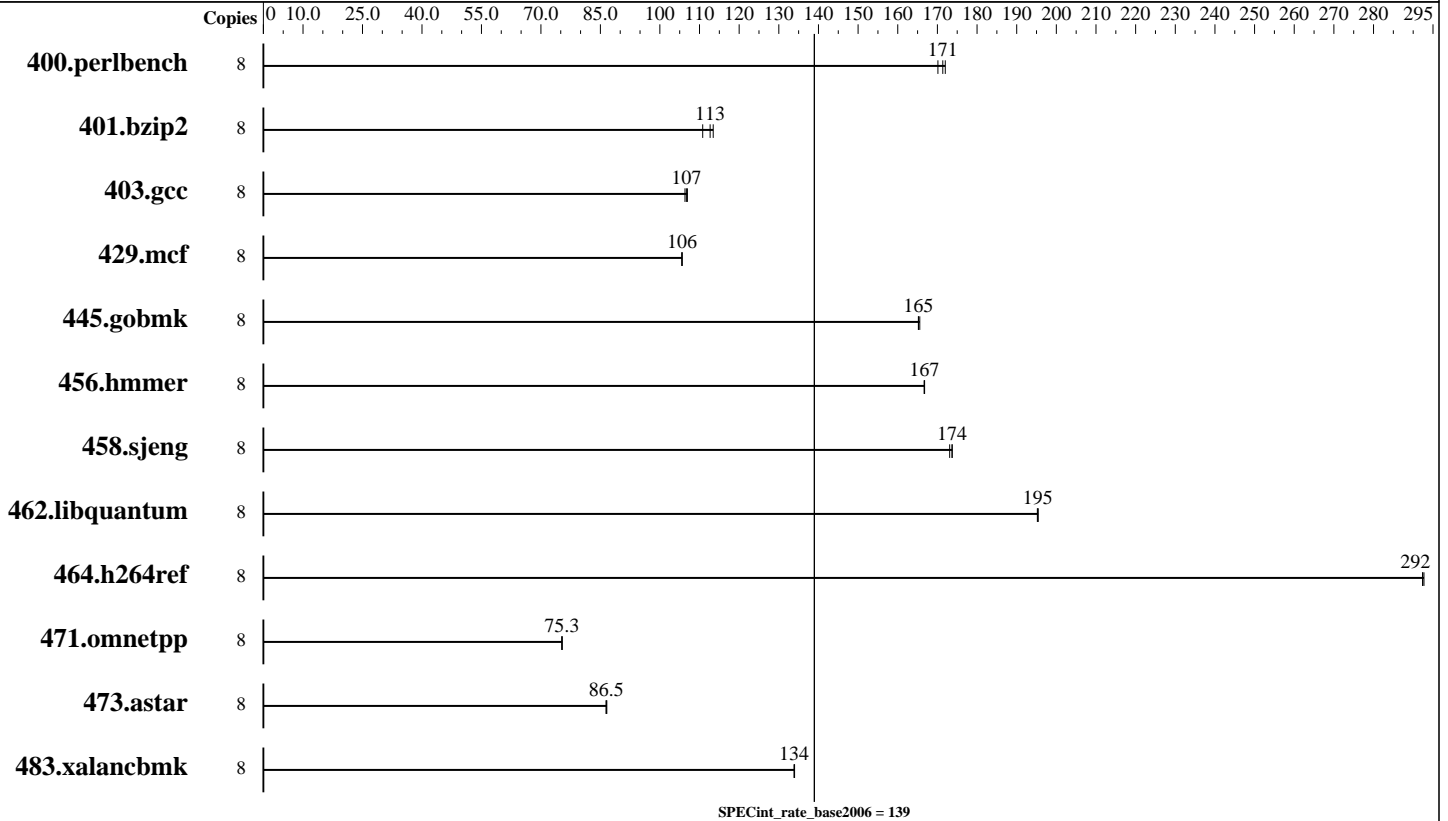
SPECint_rate_base2006 = 139

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Sep-2008
Hardware Availability: Nov-2008
Software Availability: Nov-2008

Copies 0 10.0 25.0 40.0 55.0 70.0 85.0 100 110 120 130 140 150 160 170 180 190 200 210 220 230 240 250 260 270 280 295

Benchmark	Copies	Result
400.perlbench	8	171
401.bzip2	8	113
403.gcc	8	107
429.mcf	8	106
445.gobmk	8	165
456.hmmer	8	167
458.sjeng	8	174
462.libquantum	8	195
464.h264ref	8	292
471.omnetpp	8	75.3
473.astar	8	86.5
483.xalancbmk	8	134

SPECint_rate_base2006 = 139

Hardware

CPU Name:	Intel Xeon X5470
CPU Characteristics:	1333MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem:	1 x 36 GB SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	459	170	**456**	**171**	454	172							
401.bzip2	8	**685**	**113**	697	111	680	113							
403.gcc	8	**604**	**107**	602	107	606	106							
429.mcf	8	691	106	691	106	**691**	**106**							
445.gobmk	8	**508**	**165**	508	165	507	166							
456.hmmer	8	**448**	**167**	448	167	448	167							
458.sjeng	8	**557**	**174**	559	173	557	174							
462.libquantum	8	**849**	**195**	848	195	849	195							
464.h264ref	8	605	293	606	292	**605**	**292**							
471.omnetpp	8	663	75.4	664	75.3	**664**	**75.3**							
473.astar	8	**649**	**86.5**	650	86.4	648	86.6							
483.xalancbmk	8	412	134	412	134	**412**	**134**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
    taskset was used to bind processes to cores except for 462.libquantum peak

General Notes

     All benchmarks compiled in 32-bit mode
     Hardware Prefetch Disabled, Adjacent Sector Prefetch Disabled
     'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Base Compiler Invocation

C benchmarks:
     icc

C++ benchmarks:
     icpc

Base Portability Flags

     400.perlbench: -DSPEC_CPU_LINUX_IA32
     462.libquantum: -DSPEC_CPU_LINUX
     483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.html
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.04.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.xml
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.04.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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