



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation
Lenovo Thinkpad T400

SPECint_rate2006 = 35.6

SPECint_rate_base2006 = 32.9

CPU2006 license: 13

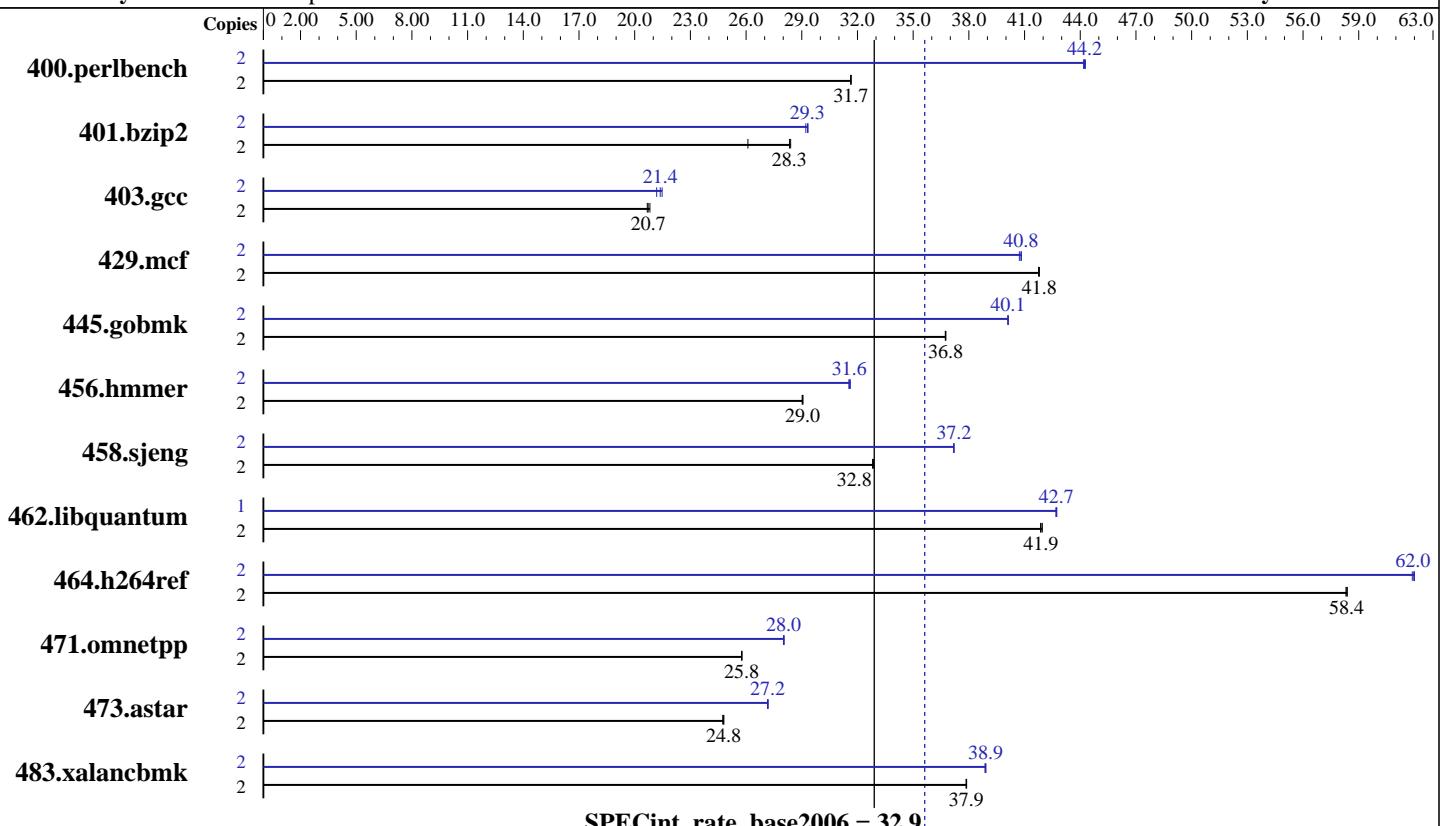
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo T9600
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Micron PC3-8500 DDR3-1066 CL7)
 Disk Subsystem: Hitachi Travelstar HTS722020K9SA00 SATA 200GB 7200RPM
 Other Hardware: None

Software

Operating System: Windows XP Professional SP2
 Compiler: Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	617	31.7	618	31.6	617	31.7	2	442	44.2	442	44.2	441	44.3
401.bzip2	2	740	26.1	680	28.4	681	28.3	2	659	29.3	658	29.3	661	29.2
403.gcc	2	779	20.7	773	20.8	777	20.7	2	750	21.5	760	21.2	753	21.4
429.mcf	2	437	41.8	436	41.8	437	41.8	2	448	40.7	447	40.8	447	40.8
445.gobmk	2	571	36.7	571	36.8	571	36.8	2	523	40.1	523	40.1	523	40.1
456.hammer	2	643	29.0	642	29.0	642	29.1	2	590	31.6	592	31.5	591	31.6
458.sjeng	2	737	32.8	737	32.8	737	32.8	2	651	37.2	651	37.2	651	37.2
462.libquantum	2	990	41.9	988	42.0	989	41.9	1	485	42.7	485	42.7	485	42.7
464.h264ref	2	758	58.4	758	58.4	759	58.3	2	714	62.0	715	61.9	714	62.0
471.omnetpp	2	485	25.8	485	25.8	485	25.8	2	446	28.0	446	28.0	446	28.0
473.astar	2	567	24.7	566	24.8	566	24.8	2	517	27.2	517	27.2	516	27.2
483.xalancbmk	2	364	37.9	364	37.9	365	37.9	2	355	38.9	355	38.9	355	38.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries were built on Windows Vista32

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

Base Compiler Invocation

C benchmarks:

 icl -Qvc8 -Qc99

C++ benchmarks:

 icl -Qvc8

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

 -fast -Qvec-guard-write /F512000000

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias  
-Qprefetch /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE
```

```
401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch  
/F512000000
```

```
403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
```

```
429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch  
/F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE
```

```
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo  
-Qprec-div- -Qansi-alias /F512000000
```

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Peak Optimization Flags (Continued)

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.xml>



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