



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®\_rate2006 = 76.8

PowerEdge M600 (Intel Xeon L5240, 3.00GHz)

SPECint\_rate\_base2006 = 64.9

CPU2006 license: 55

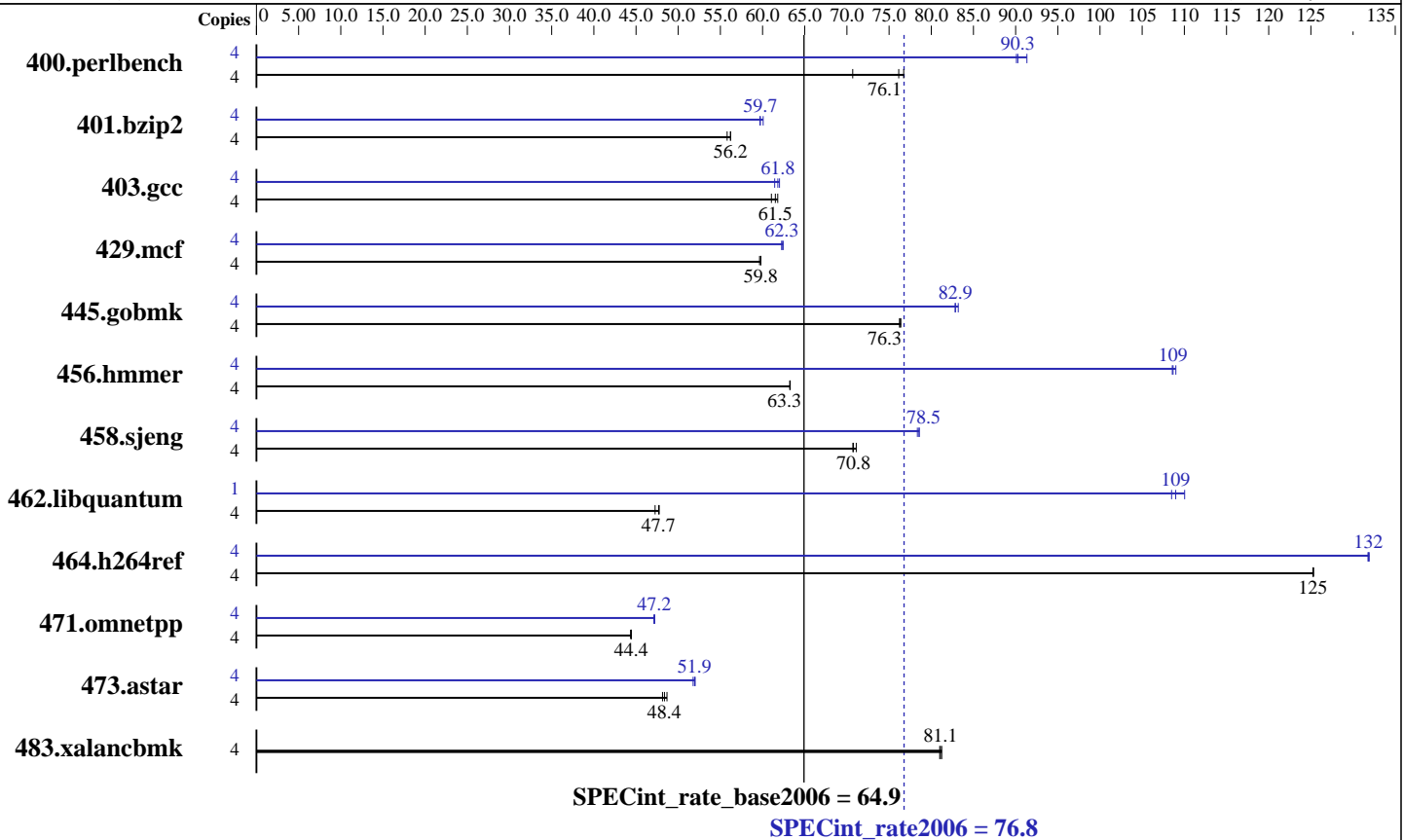
Test date: Aug-2008

Test sponsor: Dell Inc.

Hardware Availability: May-2008

Tested by: Dell Inc.

Software Availability: May-2008



## Hardware

CPU Name: Intel Xeon L5240  
 CPU Characteristics: 3000  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB 667 MHz ECC CL5 FB-DIMM)  
 Disk Subsystem: 1 x 80 GB 5400 RPM SATA  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16-60.0.21-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20080312 Package ID: l\_cc\_p\_10.1.015  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	553	70.7	<u>513</u>	<u>76.1</u>	509	76.7	4	434	90.1	428	91.3	<u>433</u>	<u>90.3</u>
401.bzip2	4	687	56.2	<u>687</u>	<u>56.2</u>	692	55.8	4	647	59.7	643	60.1	<u>646</u>	<u>59.7</u>
403.gcc	4	521	61.8	<u>523</u>	<u>61.5</u>	527	61.0	4	519	62.0	<u>521</u>	<u>61.8</u>	524	61.4
429.mcf	4	610	59.8	612	59.6	<u>610</u>	<u>59.8</u>	4	<u>586</u>	<u>62.3</u>	584	62.4	586	62.3
445.gobmk	4	549	76.4	<u>550</u>	<u>76.3</u>	550	76.2	4	<u>506</u>	<u>82.9</u>	507	82.8	504	83.2
456.hmmer	4	590	63.2	590	63.3	<u>590</u>	<u>63.3</u>	4	342	109	344	109	<u>343</u>	<u>109</u>
458.sjeng	4	<u>684</u>	<u>70.8</u>	681	71.1	685	70.7	4	616	78.6	618	78.4	<u>616</u>	<u>78.5</u>
462.libquantum	4	1754	47.3	1736	47.7	<u>1738</u>	<u>47.7</u>	1	<u>190</u>	<u>109</u>	191	108	188	110
464.h264ref	4	707	125	706	125	<u>707</u>	<u>125</u>	4	672	132	671	132	<u>671</u>	<u>132</u>
471.omnetpp	4	564	44.4	<u>563</u>	<u>44.4</u>	562	44.5	4	531	47.1	<u>530</u>	<u>47.2</u>	529	47.2
473.astar	4	583	48.1	577	48.7	<u>581</u>	<u>48.4</u>	4	540	52.0	<u>541</u>	<u>51.9</u>	542	51.8
483.xalancbmk	4	341	81.0	340	81.2	<u>340</u>	<u>81.1</u>	4	341	81.0	340	81.2	<u>340</u>	<u>81.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## Platform Notes

BIOS Settings:  
Adjacent Cache Line Prefetch = Disabled (Default Enabled)

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode  
taskset was used to bind processes to cores except for 462.libquantum peak  
OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 64M



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## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-alloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.015/bin/icc  
-L/opt/intel/cce/10.1.015/lib  
-I/opt/intel/cce/10.1.015/include

456.hmmer: /opt/intel/cce/10.1.015/bin/icc  
-L/opt/intel/cce/10.1.015/lib  
-I/opt/intel/cce/10.1.015/include

C++ benchmarks:  
icpc



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
               -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
           -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
                -opt-streaming-stores always -vec-guard-write
                -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
             -no-prec-div -ansi-alias -opt-ra-region-strategy=block
             -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
           -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
           -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

```



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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