



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 52.1

PowerEdge M600 (Intel Xeon E5205, 1.86GHz)

SPECint_rate_base2006 = 44.4

CPU2006 license: 55

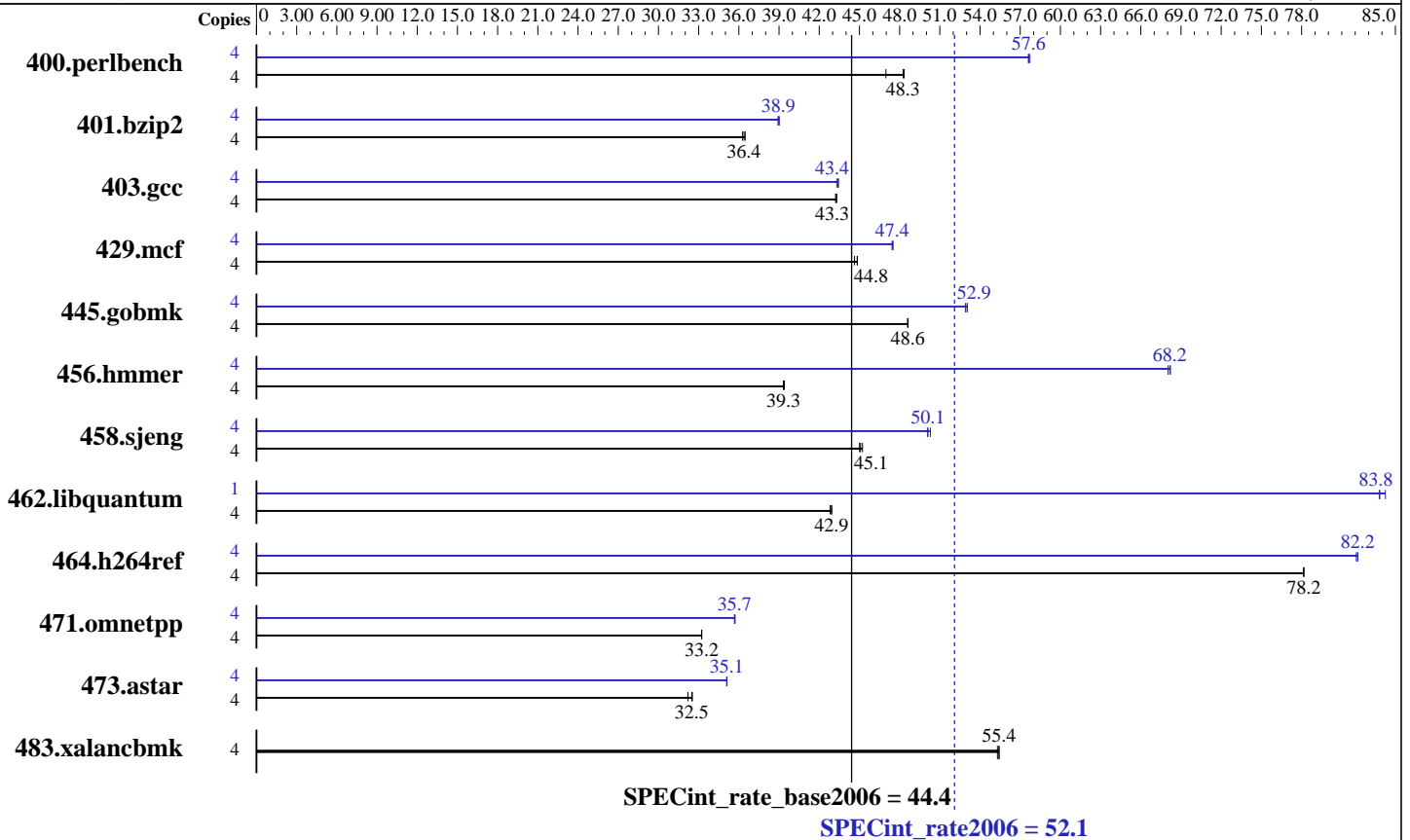
Test date: Aug-2008

Test sponsor: Dell Inc.

Hardware Availability: Feb-2008

Tested by: Dell Inc.

Software Availability: May-2008



Hardware

CPU Name: Intel Xeon E5205
 CPU Characteristics: 1866
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 80 GB 5400 RPM SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20080312 Package ID: l_cc_p_10.1.015
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	832	47.0	808	48.3	810	48.3	4	677	57.7	678	57.6	678	57.6
401.bzip2	4	1061	36.4	1058	36.5	1064	36.3	4	991	38.9	989	39.0	991	38.9
403.gcc	4	745	43.2	744	43.3	744	43.3	4	743	43.3	741	43.4	742	43.4
429.mcf	4	814	44.8	814	44.8	817	44.6	4	769	47.4	769	47.4	768	47.5
445.gobmk	4	863	48.6	863	48.6	863	48.6	4	792	52.9	793	52.9	791	53.1
456.hmmer	4	947	39.4	949	39.3	949	39.3	4	547	68.2	549	68.0	548	68.2
458.sjeng	4	1074	45.1	1075	45.0	1070	45.2	4	962	50.3	966	50.1	966	50.1
462.libquantum	4	1931	42.9	1931	42.9	1936	42.8	1	247	83.8	247	83.8	246	84.3
464.h264ref	4	1132	78.2	1132	78.2	1133	78.1	4	1077	82.2	1078	82.1	1077	82.2
471.omnetpp	4	752	33.2	752	33.2	752	33.2	4	700	35.7	701	35.7	701	35.7
473.astar	4	863	32.5	872	32.2	864	32.5	4	800	35.1	800	35.1	800	35.1
483.xalancbmk	4	498	55.4	499	55.3	498	55.4	4	498	55.4	499	55.3	498	55.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

BIOS Settings:

Adjacent Cache Line Prefetch = Disabled (Default Enabled)

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

taskset was used to bind processes to cores except for 462.libquantum peak

OMP_NUM_THREADS set to number of processors

KMP_AFFINITY set to "physical,0"

KMP_STACKSIZE set to 64M



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.015/bin/icc
-L/opt/intel/cce/10.1.015/lib
-I/opt/intel/cce/10.1.015/include

456.hmmer: /opt/intel/cce/10.1.015/bin/icc
-L/opt/intel/cce/10.1.015/lib
-I/opt/intel/cce/10.1.015/include

C++ benchmarks:
icpc



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
               -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
           -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
                -opt-streaming-stores always -vec-guard-write
                -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
             -no-prec-div -ansi-alias -opt-ra-region-strategy=block
             -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
           -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
           -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

```



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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