



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = Not Run

PowerEdge 2900 III (Intel Xeon E5405, 2.00 GHz)

SPECint\_rate\_base2006 = 44.9

CPU2006 license: 55

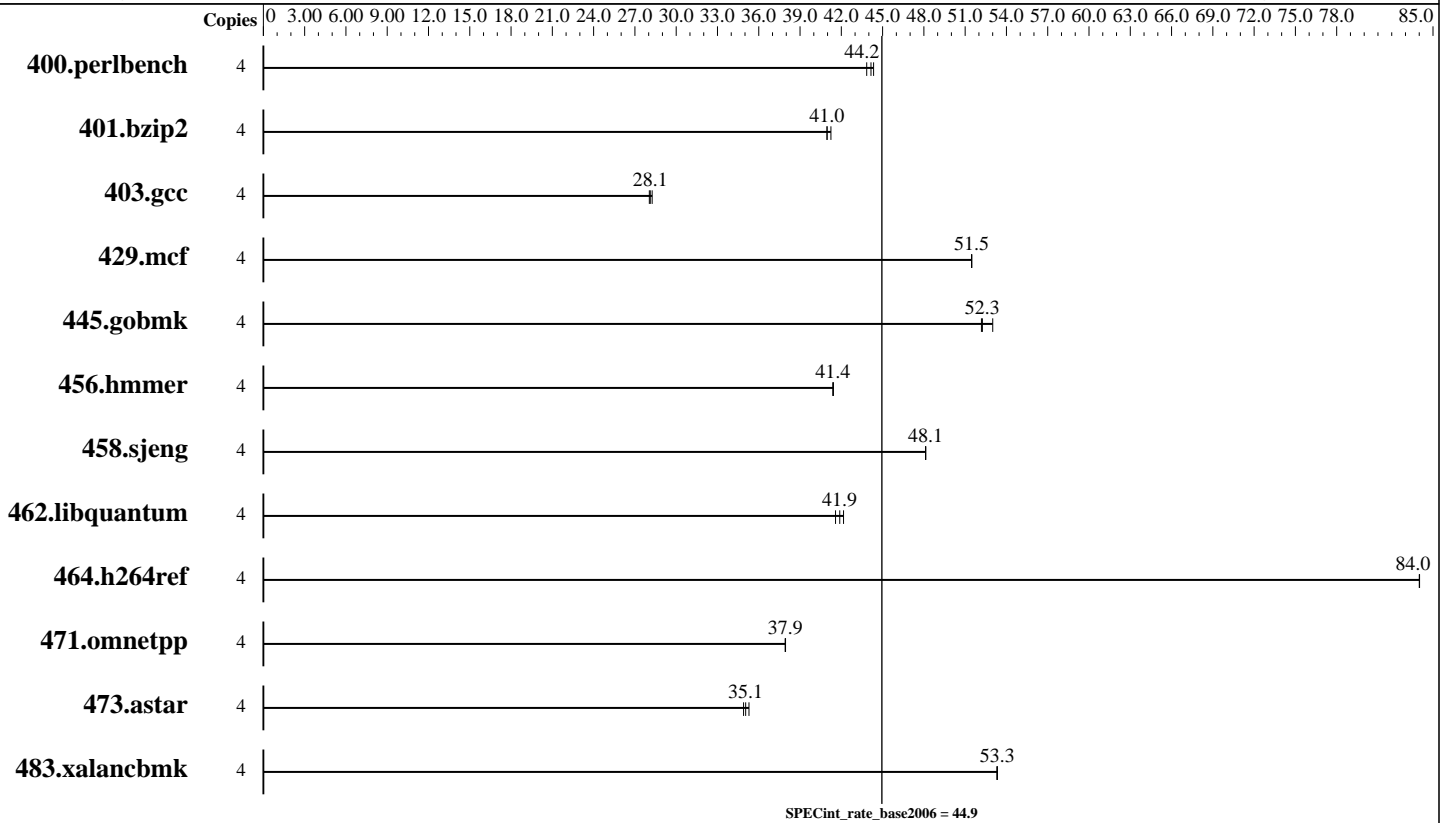
Test date: Aug-2008

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Jun-2007



## Hardware

CPU Name: Intel Xeon E5405  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)  
 Disk Subsystem: 2 x 73 GB GB SAS 15000 RPM (RAID-0)  
 Other Hardware: None

## Software

Operating System: Microsoft Windows Server 2003 R2 Enterprise x64 Edition  
 Compiler: Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w\_cc\_p\_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap 8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	881	44.3	<b>885</b>	<b>44.2</b>	891	43.8							
401.bzip2	4	942	41.0	936	41.2	<b>942</b>	<b>41.0</b>							
403.gcc	4	1140	28.3	<b>1145</b>	<b>28.1</b>	1148	28.0							
429.mcf	4	709	51.5	<b>709</b>	<b>51.5</b>	709	51.5							
445.gobmk	4	792	53.0	<b>803</b>	<b>52.3</b>	804	52.2							
456.hammer	4	902	41.4	901	41.4	<b>901</b>	<b>41.4</b>							
458.sjeng	4	1006	48.1	<b>1006</b>	<b>48.1</b>	1006	48.1							
462.libquantum	4	<b>1979</b>	<b>41.9</b>	1993	41.6	1966	42.2							
464.h264ref	4	1054	84.0	1054	84.0	<b>1054</b>	<b>84.0</b>							
471.omnetpp	4	<b>659</b>	<b>37.9</b>	659	37.9	659	37.9							
473.astar	4	805	34.9	796	35.3	<b>801</b>	<b>35.1</b>							
483.xalanbmk	4	517	53.3	<b>518</b>	<b>53.3</b>	518	53.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

BIOS Settings:

Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

Hardware Prefetcher = Disabled (Default = Enabled)

## General Notes

Binaries were built on Windows Vista

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

## Base Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32

464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

483.xalanbmk: -Qoption,cpp,--no\_wchar\_t\_keyword



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## Base Optimization Flags

C benchmarks:

```
-fast -Qvec-guard-write /F512000000
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.00.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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