



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint®2006 = 26.3

IBM System x3200 M2 (Intel Xeon X3370)

SPECint_base2006 = 22.0

CPU2006 license: 11

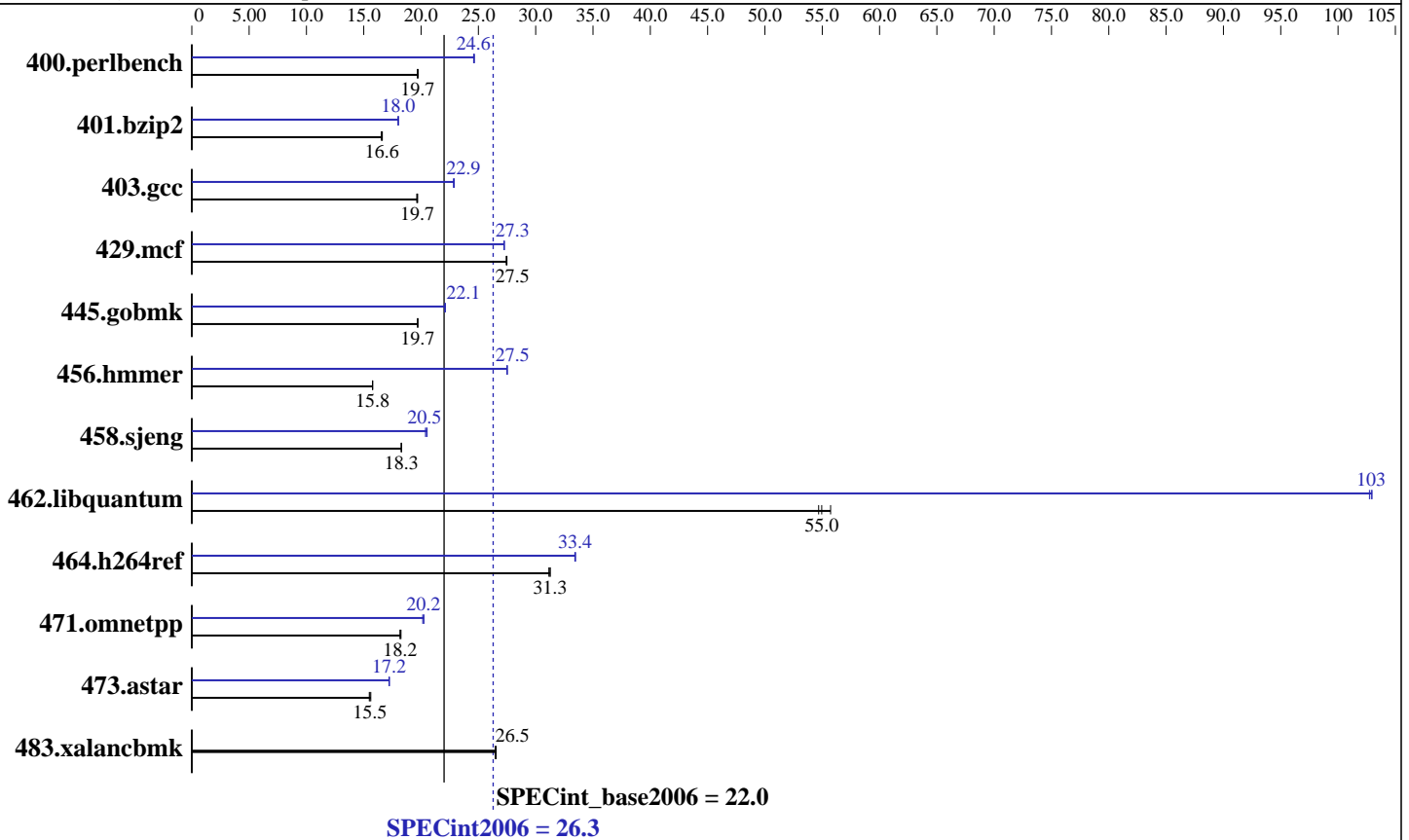
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jul-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X3370
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR2-6400E ECC)
 Disk Subsystem: 1 x 160 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 26.3

IBM System x3200 M2 (Intel Xeon X3370)

SPECint_base2006 = 22.0

CPU2006 license: 11

Test date: Jul-2008

Test sponsor: IBM Corporation

Hardware Availability: Sep-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	496	19.7	495	19.7	497	19.7	396	24.7	397	24.6	397	24.6
401.bzip2	581	16.6	581	16.6	584	16.5	536	18.0	536	18.0	536	18.0
403.gcc	409	19.7	409	19.7	410	19.6	352	22.9	352	22.9	353	22.8
429.mcf	332	27.5	333	27.4	332	27.5	335	27.2	335	27.3	335	27.3
445.gobmk	532	19.7	532	19.7	532	19.7	474	22.1	475	22.1	475	22.1
456.hmmmer	592	15.8	591	15.8	592	15.8	339	27.5	339	27.5	339	27.5
458.sjeng	662	18.3	661	18.3	662	18.3	594	20.4	589	20.5	591	20.5
462.libquantum	379	54.7	372	55.7	377	55.0	201	103	202	103	201	103
464.h264ref	710	31.1	708	31.3	708	31.3	661	33.5	662	33.4	662	33.4
471.omnetpp	342	18.2	344	18.2	344	18.2	309	20.2	310	20.1	309	20.2
473.astar	452	15.5	453	15.5	450	15.6	408	17.2	407	17.2	408	17.2
483.xalancbmk	261	26.5	260	26.6	260	26.5	261	26.5	260	26.6	260	26.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Enabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 26.3

IBM System x3200 M2 (Intel Xeon X3370)

SPECint_base2006 = 22.0

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jul-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 26.3

IBM System x3200 M2 (Intel Xeon X3370)

SPECint_base2006 = 22.0

CPU2006 license: 11

Test date: Jul-2008

Test sponsor: IBM Corporation

Hardware Availability: Sep-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation SPECint2006 = 26.3

IBM System x3200 M2 (Intel Xeon X3370) SPECint_base2006 = 22.0

CPU2006 license: 11	Test date: Jul-2008
Test sponsor: IBM Corporation	Hardware Availability: Sep-2008
Tested by: IBM Corporation	Software Availability: Nov-2007

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 19:14:24 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 September 2008.