SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

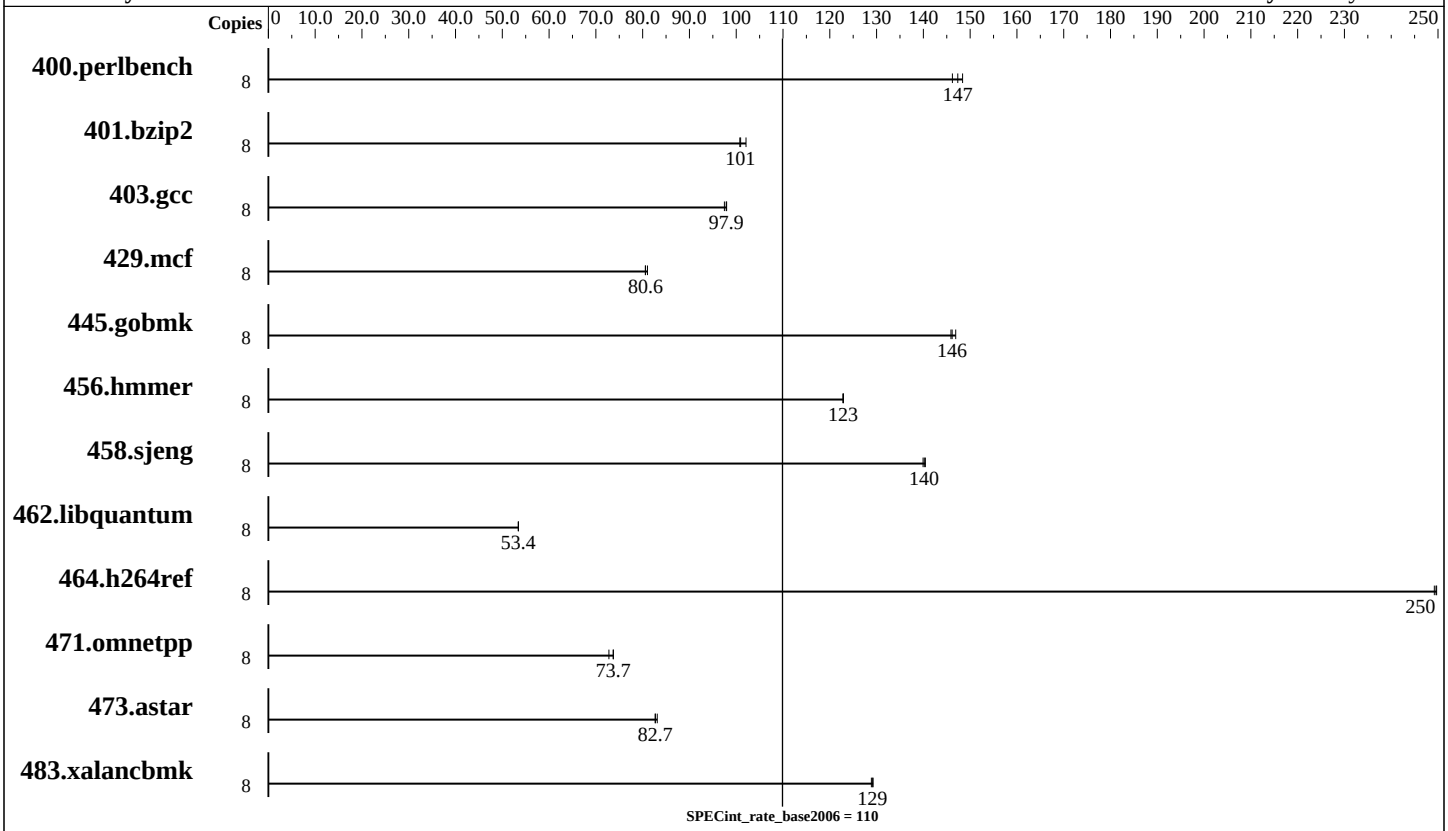
PowerEdge 2900 III (Intel Xeon E5450, 3.00 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 110

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2008
Hardware Availability: Nov-2007
Software Availability: May-2008



Hardware

CPU Name:	Intel Xeon E5450
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)
Disk Subsystem:	2 x 73 GB GB SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2 Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20080312 Package ID: l_cc_p_10.1.015
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 2900 III (Intel Xeon E5450, 3.00 GHz)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 110

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2008
Hardware Availability: Nov-2007
Software Availability: May-2008

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	535	146	**530**	**147**	527	148							
401.bzip2	8	766	101	**765**	**101**	756	102							
403.gcc	8	657	97.9	**658**	**97.9**	661	97.5							
429.mcf	8	906	80.6	900	81.0	**905**	**80.6**							
445.gobmk	8	571	147	**574**	**146**	575	146							
456.hmmer	8	608	123	**608**	**123**	607	123							
458.sjeng	8	689	140	692	140	**690**	**140**							
462.libquantum	8	**3102**	**53.4**	3102	53.4	3103	53.4							
464.h264ref	8	709	250	710	249	**709**	**250**							
471.omnetpp	8	687	72.8	678	73.7	**678**	**73.7**							
473.astar	8	679	82.7	**679**	**82.7**	676	83.1							
483.xalancbmk	8	**428**	**129**	427	129	428	129							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)
Hardware Prefetcher = Disabled (Default = Enabled)
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 19:25:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 August 2008.