



SPEC[®] CINT2006 Result

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NEC Corporation

Express5800/110Ri-1
(Intel Xeon E3110)

SPECint[®]_rate2006 = 43.1

SPECint_rate_base2006 = 36.9

CPU2006 license: 9006

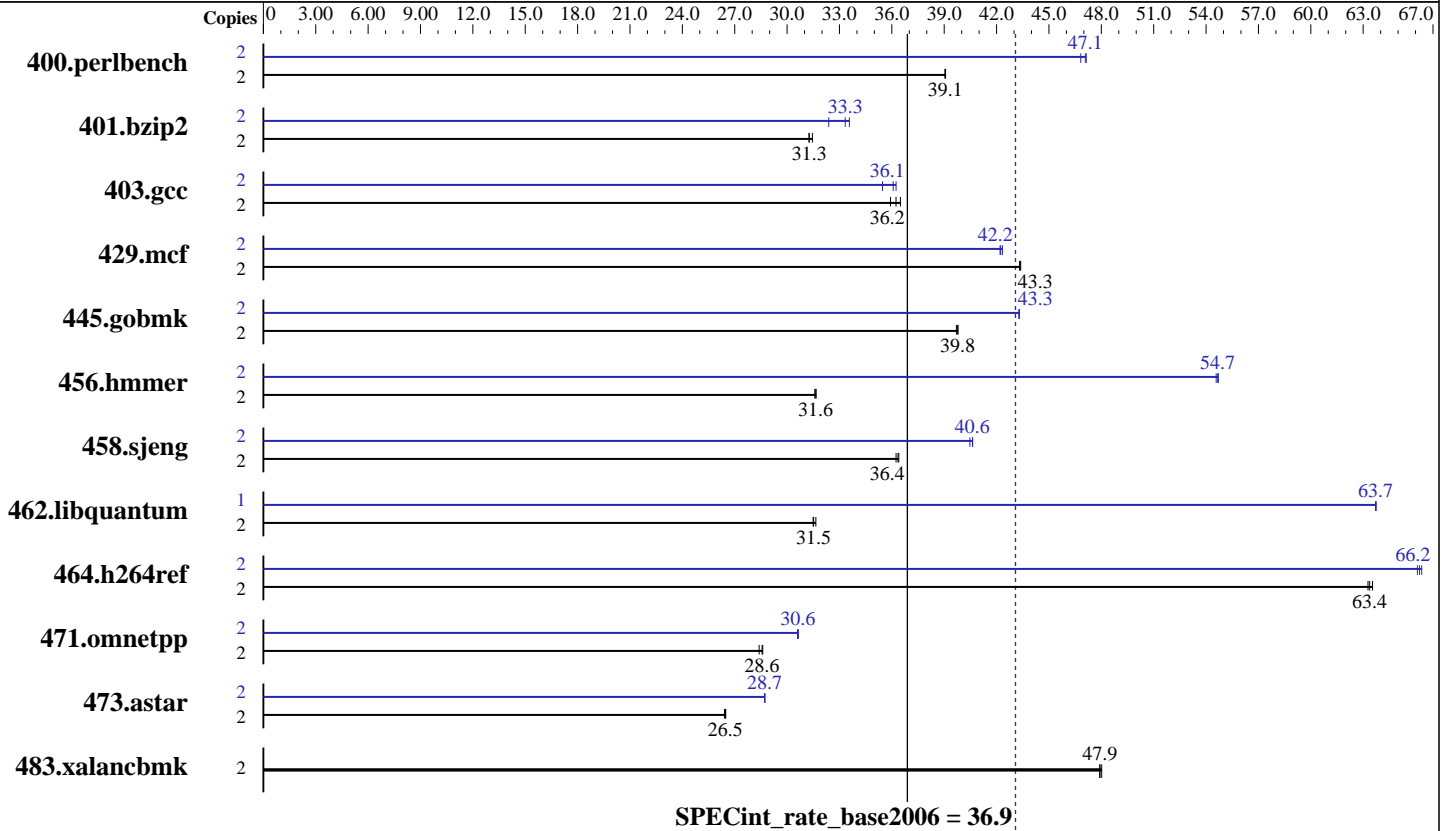
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2008

Hardware Availability: Apr-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E3110
 CPU Characteristics: 3.00 GHz, 6 MB L2, 1333 MHz bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)
 Disk Subsystem: 1x80.0 GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	501	39.0	500	39.1	500	39.1	2	415	47.1	417	46.8	415	47.1
401.bzip2	2	614	31.5	618	31.3	617	31.3	2	575	33.6	579	33.3	596	32.4
403.gcc	2	441	36.5	448	35.9	444	36.2	2	444	36.2	446	36.1	454	35.5
429.mcf	2	421	43.3	420	43.4	421	43.3	2	432	42.2	431	42.3	432	42.2
445.gobmk	2	527	39.8	528	39.7	527	39.8	2	485	43.3	485	43.3	485	43.3
456.hmmer	2	590	31.6	591	31.6	589	31.7	2	342	54.6	341	54.7	341	54.7
458.sjeng	2	668	36.2	665	36.4	666	36.4	2	596	40.6	596	40.6	598	40.5
462.libquantum	2	1315	31.5	1309	31.6	1316	31.5	1	325	63.7	325	63.7	325	63.7
464.h264ref	2	697	63.5	698	63.4	699	63.3	2	668	66.2	667	66.4	670	66.1
471.omnetpp	2	438	28.6	437	28.6	440	28.4	2	408	30.6	408	30.6	409	30.6
473.astar	2	530	26.5	530	26.5	532	26.4	2	489	28.7	489	28.7	489	28.7
483.xalancbmk	2	287	48.0	288	47.9	288	47.9	2	287	48.0	288	47.9	288	47.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetch: Disabled
Intel SpeedStep Technology: Enabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.xml>

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 19:24:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 August 2008.