



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 15.4

IBM BladeCenter LS22 (AMD Opteron 2356)

SPECint_base2006 = 13.2

CPU2006 license: 11

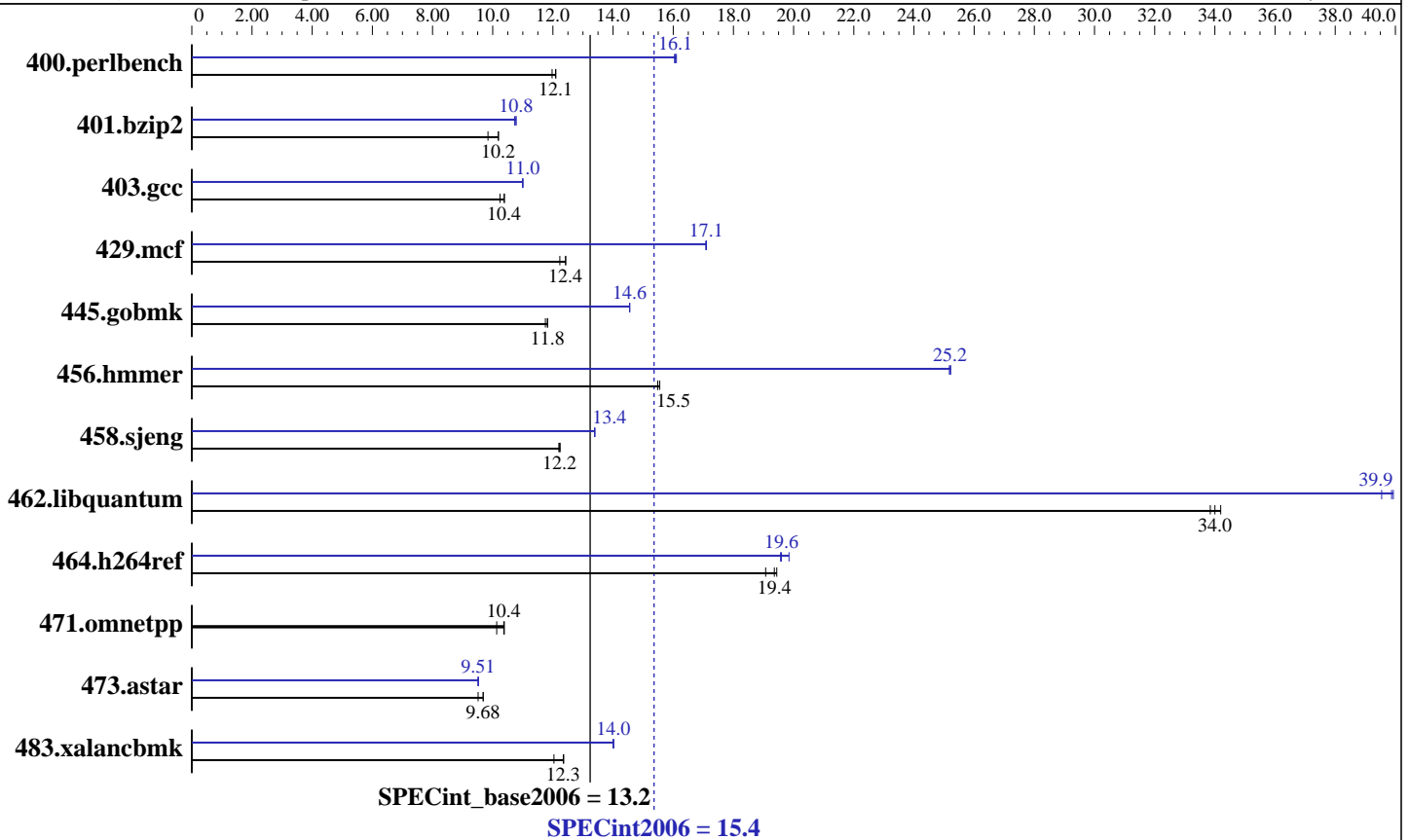
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jun-2008

Hardware Availability: Sep-2008

Software Availability: May-2008



Hardware

CPU Name: AMD Opteron 2356
 CPU Characteristics:
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (8 x 4 GB DDR2-6400 ECC)
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: PGI Server Complete Version 7.2 PathScale Compiler Suite Version 3.1
 Auto Parallel: No
 File System: ext2
 System State: Runlevel 3 (Full multiuser with network)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap 8.0 32-bit Library for Linux



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	808	12.1	808	12.1	816	12.0	609	16.0	607	16.1	608	16.1
401.bzip2	947	10.2	947	10.2	980	9.84	896	10.8	895	10.8	900	10.7
403.gcc	775	10.4	775	10.4	786	10.2	732	11.0	732	11.0	732	11.0
429.mcf	734	12.4	734	12.4	746	12.2	533	17.1	534	17.1	533	17.1
445.gobmk	887	11.8	888	11.8	893	11.8	721	14.6	721	14.6	721	14.5
456.hammer	603	15.5	602	15.5	600	15.6	370	25.2	371	25.2	370	25.2
458.sjeng	992	12.2	989	12.2	990	12.2	903	13.4	904	13.4	904	13.4
462.libquantum	606	34.2	609	34.0	612	33.8	519	39.9	520	39.9	524	39.5
464.h264ref	1138	19.4	1160	19.1	1143	19.4	1129	19.6	1131	19.6	1115	19.8
471.omnetpp	603	10.4	617	10.1	602	10.4	603	10.4	617	10.1	602	10.4
473.astar	725	9.68	738	9.51	725	9.68	737	9.53	738	9.51	738	9.51
483.xalancbmk	559	12.3	573	12.0	558	12.4	493	14.0	493	14.0	492	14.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'numactl' was used to bind copies to the cores
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2457600' was used to set environment locked pages in memory limit
Environment variable PGI_HUGE_PAGES set to 896
Set vm/nr_hugepages=7168 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
Processor Performance States Disabled in BIOS
Memory ChipKill Disabled in BIOS
```

Base Compiler Invocation

C benchmarks:

pgcc

C++ benchmarks:

pgcpp

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -Mipa=fast -Mipa=inline -Mfprelaxed -Msmartalloc=huge:150
-tp barcelona-64 -Bstatic_pgi

C++ benchmarks:

-fastsse -Mipa=fast -Mipa=inline -Mfprelaxed -Msmartalloc=huge:150
--zc_eh -tp barcelona -Bstatic_pgi

Base Other Flags

C benchmarks:

-w -Mipa=jobs:4

C++ benchmarks:

-w -Mipa=jobs:4

Peak Compiler Invocation

C benchmarks (except as noted below):

pgcc

400.perlbench: pathcc

403.gcc: pathcc

445.gobmk: pathcc

C++ benchmarks (except as noted below):

pathCC

471.omnetpp: pgcpp



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
 -WOPT:if_conv=0 -CG:local_sched_alg=1

401.bzip2: -Mpfi(pass 1) -Mpfo(pass 2) -fast -O4
 -Msmartalloc=huge:150 -Mnounroll -tp barcelona-64
 -Bstatic_pgi

403.gcc: -march=barcelona -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -m32 -O3 -OPT:Ofast

429.mcf: -fastsse -Mipa=fast -Mipa=inline:1 -Msmartalloc=huge:150
 -tp barcelona -Bstatic_pgi

445.gobmk: -march=barcelona -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict -LNO:opt=0
 -CG:p2align=on

456.hmmer: -fastsse -Munroll=n:8 -Msmartalloc=huge:150 -Mfprelaxed
 -Mvect=partial -Msafeptr -Mipa=const -Mipa=ptr -Mipa=arg
 -Mipa=inline -tp barcelona-64 -Bstatic_pgi

458.sjeng: -Mpfi(pass 1) -Mipa=fast(pass 2) -Mipa=inline:1(pass 2)
 -Mipa=noarg(pass 2) -Mpfo(pass 2) -fastsse
 -Msmartalloc=huge:150 -Mfprelaxed -tp barcelona-64
 -Bstatic_pgi

462.libquantum: -fastsse -Mfprelaxed -Msmartalloc=huge:150 -Munroll=m:8
 -Mipa=fast -Mipa=inline -Mipa=noarg -tp barcelona-64
 -Bstatic_pgi

464.h264ref: -Mpfi=indirect(pass 1) -Mipa=fast(pass 2)
 -Mipa=inline(pass 2) -Mpfo=indirect(pass 2) -fastsse
 -Msmartalloc=huge:150 -Mfprelaxed -tp barcelona-64
 -Bstatic_pgi

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -march=barcelona -Ofast -TENV:frame_pointer=off
-WOPT:if_conv=0 -GRA:optimize_boundary=on -IPA:plimit=525
-m32 -lsmartheap

483.xalancbmk: -march=barcelona -Ofast -m32 -OPT:unroll_times_max=8
-CG:push_pop_int_saved_regs=off -CG:ptr_load_use=0
-lsmartheap

Peak Other Flags

C benchmarks (except as noted below):

-w -Mipa=jobs:4(pass 2)

400.perlbench: No flags used

401.bzip2: -w

403.gcc: No flags used

445.gobmk: No flags used

C++ benchmarks (except as noted below):

-L/root/work/cpu2006/amd123GH.libs/32

471.omnetpp: -w -Mipa=jobs:4

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/amd123GH-flags.20090713.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/amd123GH-flags.20090713.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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