SPEC® CINT2006 Result
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Hewlett-Packard Company

ProLiant DL320 G5p
(2.83 GHz, Intel Xeon X3360)

SPECint®_rate2006 =   Not Run

SPECint_rate_base2006 = 63.6

CPU2006 license: 3
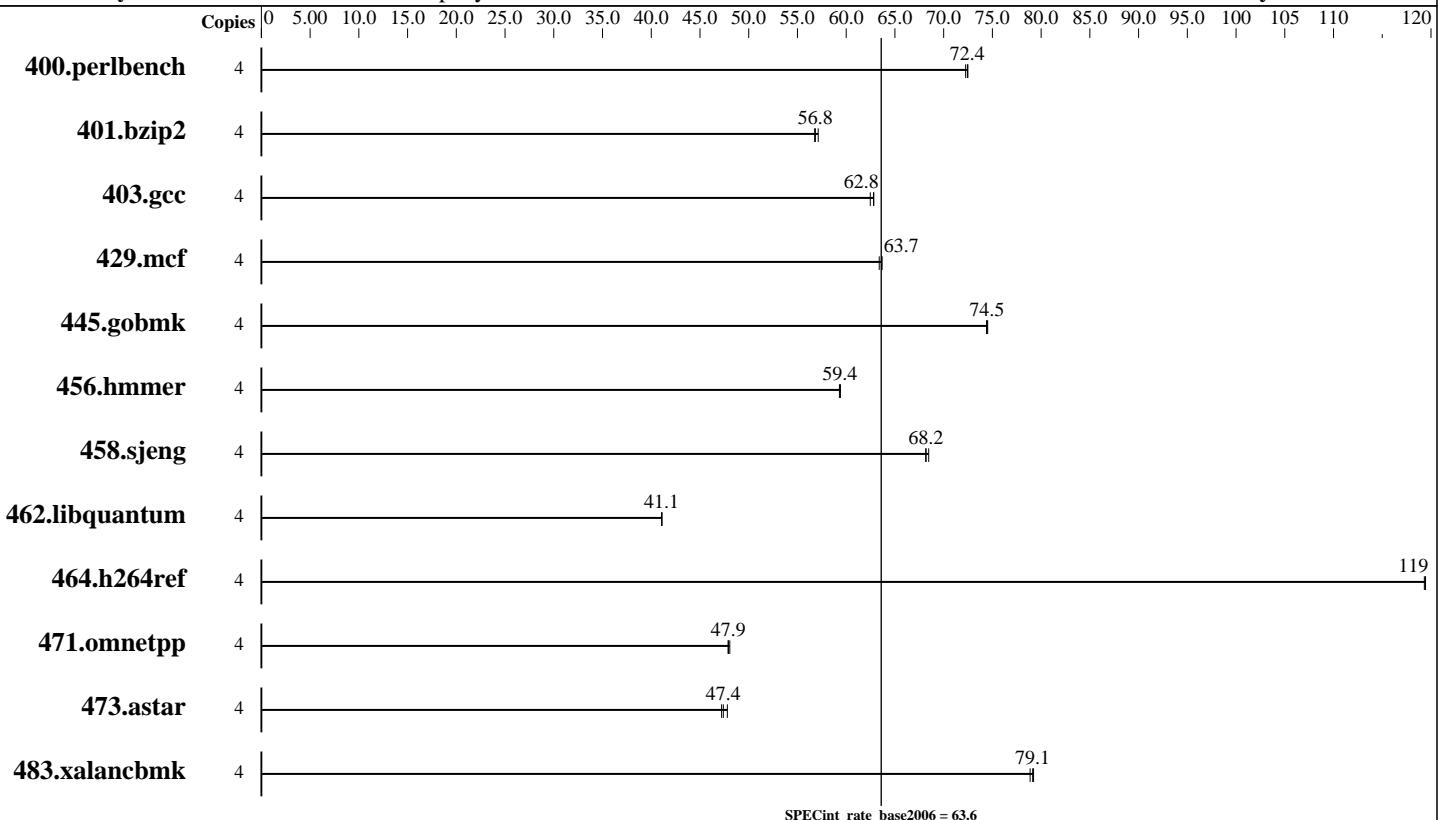
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: May-2008

Hardware Availability: May-2008

Software Availability: Nov-2007

Copies		
400.perlbench	4	72.4
401.bzip2	4	56.8
403.gcc	4	62.8
429.mcf	4	63.7
445.gobmk	4	74.5
456.hmmer	4	59.4
458.sjeng	4	68.2
462.libquantum	4	41.1
464.h264ref	4	119
471.omnetpp	4	47.9
473.astar	4	47.4
483.xalancbmk	4	79.1

SPECint_rate_base2006 = 63.6

Hardware

CPU Name: Intel Xeon X3360

CPU Characteristics: 2.83 GHz, 2x6 MB L2 shared, 1333 MHz system bus

CPU MHz: 2833

FPU: Integrated

CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip

CPU(s) orderable: 1 chip

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

L3 Cache: None

Other Cache: None

Memory: 8 GB (4x2 GB PC2-6400E CL5)

Disk Subsystem: 1 x 80 GB 7.2 K SATA

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp

Compiler: Intel C++ Compiler for applications running on Build 20070913 Package ID: l_cc_p_10.1.008

Auto Parallel: No

File System: ext2

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: Not Applicable

Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50

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Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	4	539	72.5	**540**	**72.4**	541	72.3							
401.bzip2	4	680	56.8	**679**	**56.8**	676	57.1							
403.gcc	4	**513**	**62.8**	512	62.8	515	62.5							
429.mcf	4	**573**	**63.7**	575	63.4	573	63.7							
445.gobmk	4	563	74.5	**564**	**74.5**	564	74.4							
456.hmmer	4	628	59.4	629	59.3	**629**	**59.4**							
458.sjeng	4	**710**	**68.2**	707	68.5	710	68.2							
462.libquantum	4	2017	41.1	**2018**	**41.1**	2018	41.1							
464.h264ref	4	741	119	742	119	**741**	**119**							
471.omnetpp	4	**522**	**47.9**	522	47.9	520	48.1							
473.astar	4	**592**	**47.4**	587	47.8	595	47.2							
483.xalancbmk	4	348	79.2	**349**	**79.1**	350	78.9							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
    Power Regulator set to Static High Performance Mode
    Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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