



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint®_rate2006 = 71.9

Servidor Itautec MX201 (Intel Xeon X5450)

SPECint_rate_base2006 = 62.5

CPU2006 license: 9001

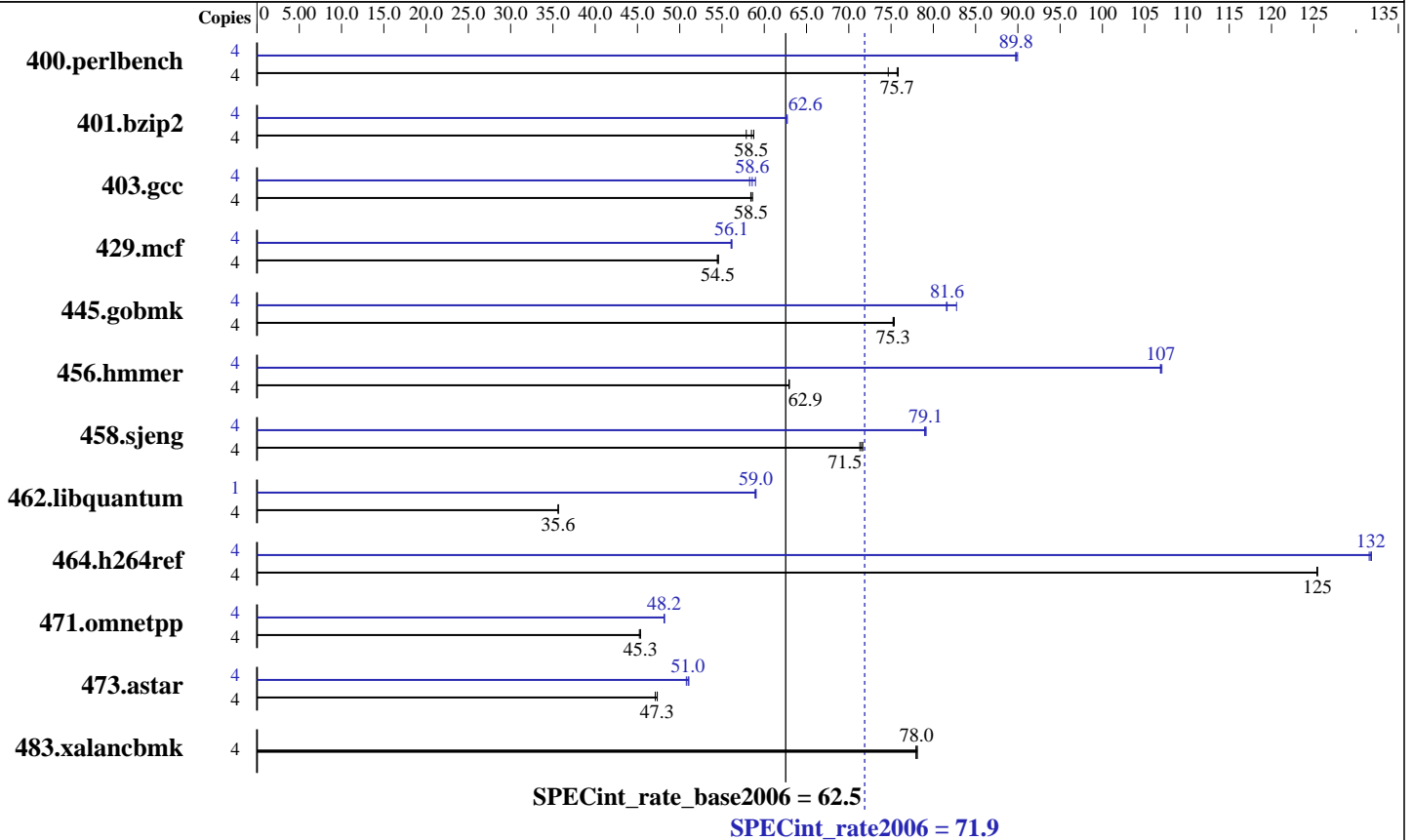
Test sponsor: Itautec

Tested by: Itautec

Test date: May-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon X5450
 CPU Characteristics:
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 71.9

Servidor Itaotec MX201 (Intel Xeon X5450)

SPECint_rate_base2006 = 62.5

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: May-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	523	74.7	<u>516</u>	<u>75.7</u>	515	75.8	4	434	90.0	435	89.7	<u>435</u>	<u>89.8</u>
401.bzip2	4	667	57.9	657	58.8	<u>660</u>	<u>58.5</u>	4	615	62.7	<u>616</u>	<u>62.6</u>	617	62.6
403.gcc	4	549	58.6	551	58.4	<u>551</u>	<u>58.5</u>	4	546	59.0	<u>550</u>	<u>58.6</u>	553	58.3
429.mcf	4	<u>669</u>	<u>54.5</u>	668	54.6	670	54.4	4	<u>650</u>	<u>56.1</u>	649	56.2	651	56.1
445.gobmk	4	557	75.4	<u>558</u>	<u>75.3</u>	558	75.2	4	507	82.7	<u>514</u>	<u>81.6</u>	515	81.6
456.hammer	4	593	63.0	593	62.9	<u>593</u>	<u>62.9</u>	4	<u>349</u>	<u>107</u>	349	107	349	107
458.sjeng	4	679	71.3	676	71.6	<u>677</u>	<u>71.5</u>	4	612	79.1	613	79.0	<u>612</u>	<u>79.1</u>
462.libquantum	4	<u>2326</u>	<u>35.6</u>	2325	35.6	2326	35.6	1	352	58.9	351	59.0	<u>351</u>	<u>59.0</u>
464.h264ref	4	<u>706</u>	<u>125</u>	706	125	706	125	4	672	132	<u>672</u>	<u>132</u>	673	132
471.omnetpp	4	<u>551</u>	<u>45.3</u>	553	45.2	551	45.4	4	<u>519</u>	<u>48.2</u>	519	48.2	519	48.2
473.astar	4	593	47.3	596	47.1	<u>593</u>	<u>47.3</u>	4	553	50.8	<u>551</u>	<u>51.0</u>	550	51.1
483.xalancbmk	4	353	78.1	<u>354</u>	<u>78.0</u>	354	77.9	4	353	78.1	<u>354</u>	<u>78.0</u>	354	77.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processor, except for 462.libquantum at peak.

Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

General Notes

This result was measured on the Servidor Itaotec MX201.
The Servidor Itaotec MX201 and the Servidor Itaotec MX221 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 71.9

Servidor Itaotec MX201 (Intel Xeon X5450)

SPECint_rate_base2006 = 62.5

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: May-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 71.9

Servidor Itaotec MX201 (Intel Xeon X5450)

SPECint_rate_base2006 = 62.5

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: May-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
403.gcc: -fast -inline-calloc -opt-malloc-options=3
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 71.9

Servidor Itautec MX201 (Intel Xeon X5450)

SPECint_rate_base2006 = 62.5

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: May-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 17:04:27 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 May 2008.