



# SPEC® CINT2006 Result

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## NEC Corporation

Express5800/120Gd  
(Intel Xeon E5205)

SPECint®\_rate2006 = 49.7

SPECint\_rate\_base2006 = 41.5

CPU2006 license: 9006

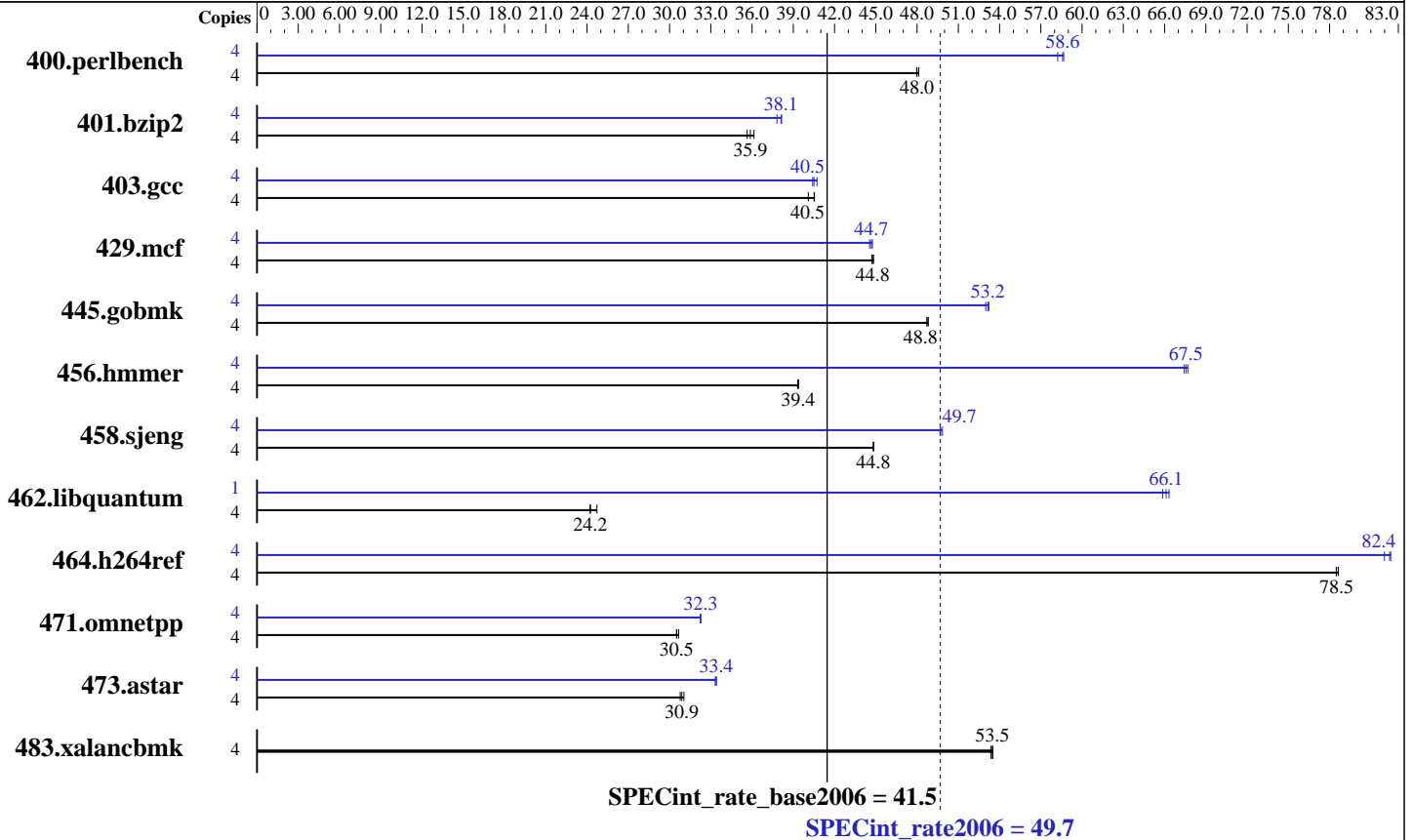
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: May-2008

Hardware Availability: Apr-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5205  
 CPU Characteristics: 1.86 GHz, 6 MB L2, 1066 MHz bus  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1x250 GB SATAII, 7200RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: L\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	812	48.1	<b>814</b>	<b>48.0</b>	815	48.0	4	<b>667</b>	<b>58.6</b>	666	58.7	671	58.2
401.bzip2	4	1083	35.6	1068	36.1	<b>1076</b>	<b>35.9</b>	4	1021	37.8	1012	38.2	<b>1013</b>	<b>38.1</b>
403.gcc	4	794	40.5	803	40.1	<b>795</b>	<b>40.5</b>	4	791	40.7	797	40.4	<b>795</b>	<b>40.5</b>
429.mcf	4	816	44.7	<b>815</b>	<b>44.8</b>	814	44.8	4	<b>817</b>	<b>44.7</b>	815	44.8	819	44.5
445.gobmk	4	<b>860</b>	<b>48.8</b>	862	48.7	860	48.8	4	788	53.2	<b>789</b>	<b>53.2</b>	792	53.0
456.hmmer	4	949	39.3	<b>948</b>	<b>39.4</b>	948	39.4	4	<b>552</b>	<b>67.5</b>	551	67.7	554	67.4
458.sjeng	4	1080	44.8	1079	44.9	<b>1080</b>	<b>44.8</b>	4	971	49.9	974	49.7	<b>974</b>	<b>49.7</b>
462.libquantum	4	3353	24.7	<b>3419</b>	<b>24.2</b>	3421	24.2	1	<b>313</b>	<b>66.1</b>	312	66.3	315	65.9
464.h264ref	4	1128	78.5	1126	78.6	<b>1128</b>	<b>78.5</b>	4	1080	82.0	<b>1075</b>	<b>82.4</b>	1074	82.4
471.omnetpp	4	815	30.7	<b>819</b>	<b>30.5</b>	819	30.5	4	<b>775</b>	<b>32.3</b>	775	32.3	775	32.3
473.astar	4	904	31.1	<b>909</b>	<b>30.9</b>	912	30.8	4	<b>841</b>	<b>33.4</b>	841	33.4	843	33.3
483.xalancbmk	4	<b>516</b>	<b>53.5</b>	516	53.5	517	53.4	4	<b>516</b>	<b>53.5</b>	516	53.5	517	53.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

Bios settings:  
Hardware Prefetcher: Enabled  
Adjacent Cache Line Prefetch: Enabled  
Intel SpeedStep Technology: Disabled

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2, 456.hmmer, for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include  
456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include  
C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
403.gcc: -fast -inline-calloc -opt-malloc-options=3  
429.mcf: -fast -prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap  
483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090714.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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