SPEC® CINT2006 Result

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Dell Inc.

Dell Precision T3400 (Intel QX9650, 3.00 GHz)

$SPECint^®\_rate2006 =$ 70.9

$SPECint\_rate\_base2006 =$ 64.2
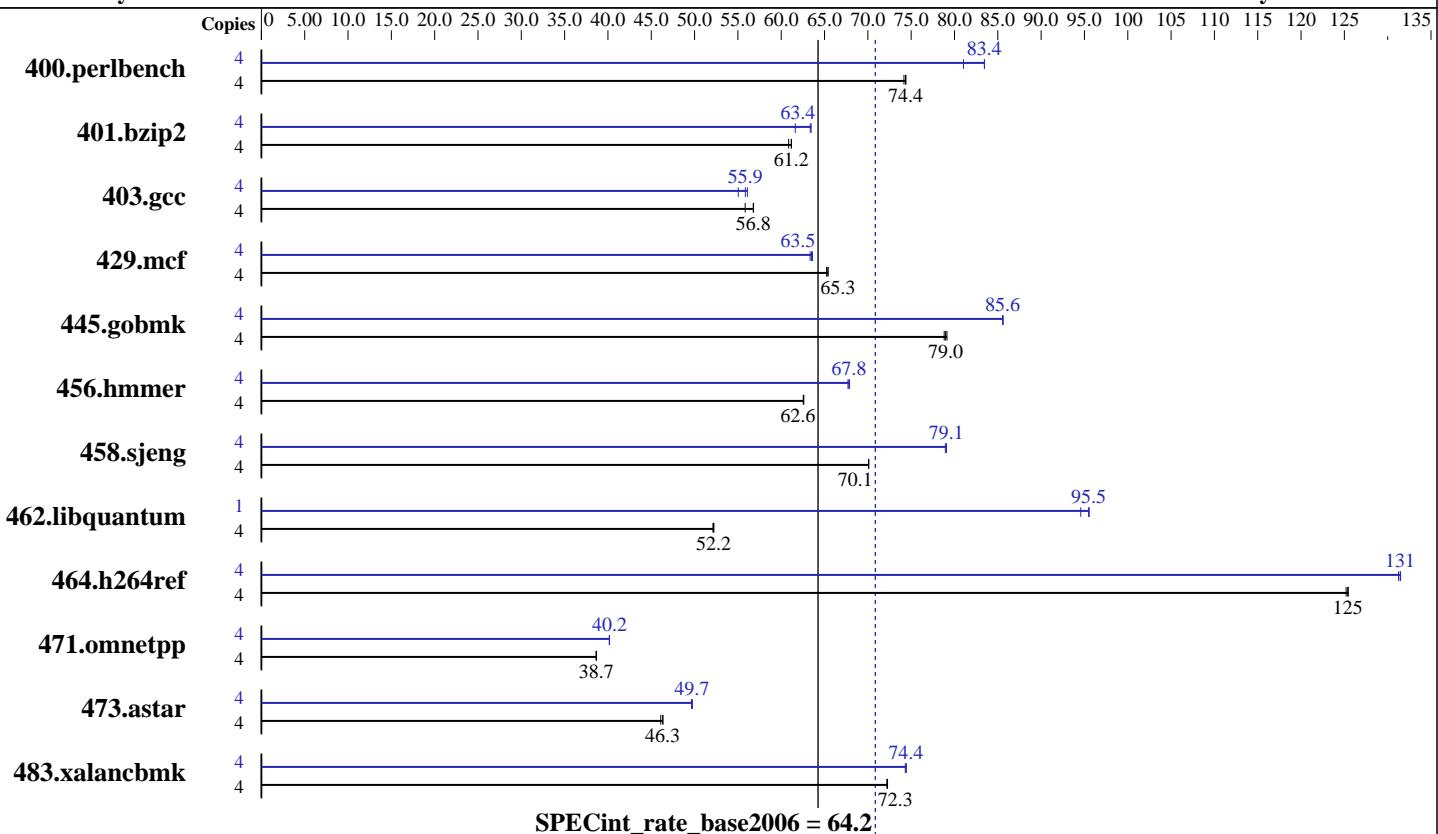
CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Apr-2008
Hardware Availability: Jan-2008
Software Availability: Mar-2008

Copies		
400.perlbench	4	83.4
	4	74.4
401.bzip2	4	63.4
	4	61.2
403.gcc	4	55.9
	4	56.8
429.mcf	4	63.5
	4	65.3
445.gobmk	4	85.6
	4	79.0
456.hmmer	4	67.8
	4	62.6
458.sjeng	4	79.1
	4	70.1
462.libquantum	1	95.5
	4	52.2
464.h264ref	4	131
	4	125
471.omnetpp	4	40.2
	4	38.7
473.astar	4	49.7
	4	46.3
483.xalancbmk	4	74.4
	4	72.3

SPECint_rate_base2006 = 64.2

SPECint_rate2006 = 70.9

Hardware

CPU Name:	Intel Core 2 Extreme QX9650
CPU Characteristics:	1333 MHz Bus Speed
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB 800 MHz ECC CL6 DDR2)
Disk Subsystem:	1 x 160 GB SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate (64-bit)
Compiler:	Intel C++ Compiler for IA-32, Version 10.1 Build 20080312 Package ID: w_cc_p_10.1.021 Microsoft Visual Studio 2005 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**526**	**74.4**	527	74.2	525	74.4	4	482	81.0	**468**	**83.4**	468	83.5
401.bzip2	4	634	60.9	631	61.2	**631**	**61.2**	4	626	61.6	608	63.5	**609**	**63.4**
403.gcc	4	567	56.8	**567**	**56.8**	577	55.8	4	585	55.0	**576**	**55.9**	574	56.1
429.mcf	4	559	65.2	558	65.4	**559**	**65.3**	4	576	63.3	**574**	**63.5**	573	63.6
445.gobmk	4	532	78.8	**531**	**79.0**	530	79.1	4	**490**	**85.6**	490	85.6	490	85.6
456.hmmer	4	597	62.5	596	62.6	**596**	**62.6**	4	551	67.7	**550**	**67.8**	550	67.9
458.sjeng	4	691	70.1	690	70.1	**690**	**70.1**	4	**612**	**79.1**	612	79.1	613	78.9
462.libquantum	4	1590	52.1	**1588**	**52.2**	1587	52.2	1	**217**	**95.5**	217	95.5	219	94.6
464.h264ref	4	707	125	706	125	**706**	**125**	4	674	131	**673**	**131**	673	131
471.omnetpp	4	646	38.7	**647**	**38.7**	647	38.6	4	**622**	**40.2**	622	40.2	623	40.2
473.astar	4	609	46.1	606	46.3	**606**	**46.3**	4	566	49.6	**565**	**49.7**	564	49.7
483.xalancbmk	4	**382**	**72.3**	382	72.3	382	72.2	4	371	74.3	**371**	**74.4**	371	74.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qstd=c99
```

C++ benchmarks:
```
icl
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:
```
-fast -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
        -link /FORCE:MULTIPLE
```

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Base Other Flags

C benchmarks:

>   403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
>   `icl -Qstd=c99`

C++ benchmarks:
>   `icl`

Peak Portability Flags

>   403.gcc: `-DSPEC_CPU_WIN32`
>   464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
>   483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

>   400.perlbench: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias`
>   `-Qprefetch /F512000000`
>
>   401.bzip2: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch`
>   `/F512000000`
>
>   403.gcc: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000`
>
>   429.mcf: `Same as 401.bzip2`
>
>   445.gobmk: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo`
>   `-Qprec-div- -Qansi-alias /F512000000`
>
>   456.hmmer: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2`
>   `-Qansi-alias -Qopt-multi-version-aggressive /F512000000`
>
>   458.sjeng: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4`
>   `/F512000000`
>
>   462.libquantum: `-fast -Qunroll4 -Ob0 -Qprefetch`
>   `-Qopt-streaming-stores:always -Qparallel`
>   `-Qpar-runtime-control /F512000000 shlw32mt.lib`

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Peak Optimization Flags (Continued)

```
   464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
                -Qansi-alias /F512000000
```

C++ benchmarks:

```
   471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
                -Qopt-ra-region-strategy=block -Qcxx_features /F512000000
                shlw32m.lib              -link /FORCE:MULTIPLE

     473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
                -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
                shlw32m.lib                -link /FORCE:MULTIPLE

 483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
                -Qcxx_features /F512000000 shlw32m.lib
                        -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
     403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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