



SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 24.6

Dell Precision T3400 (Intel QX9650, 3.00 GHz)

SPECint_base2006 = 21.6

CPU2006 license: 55

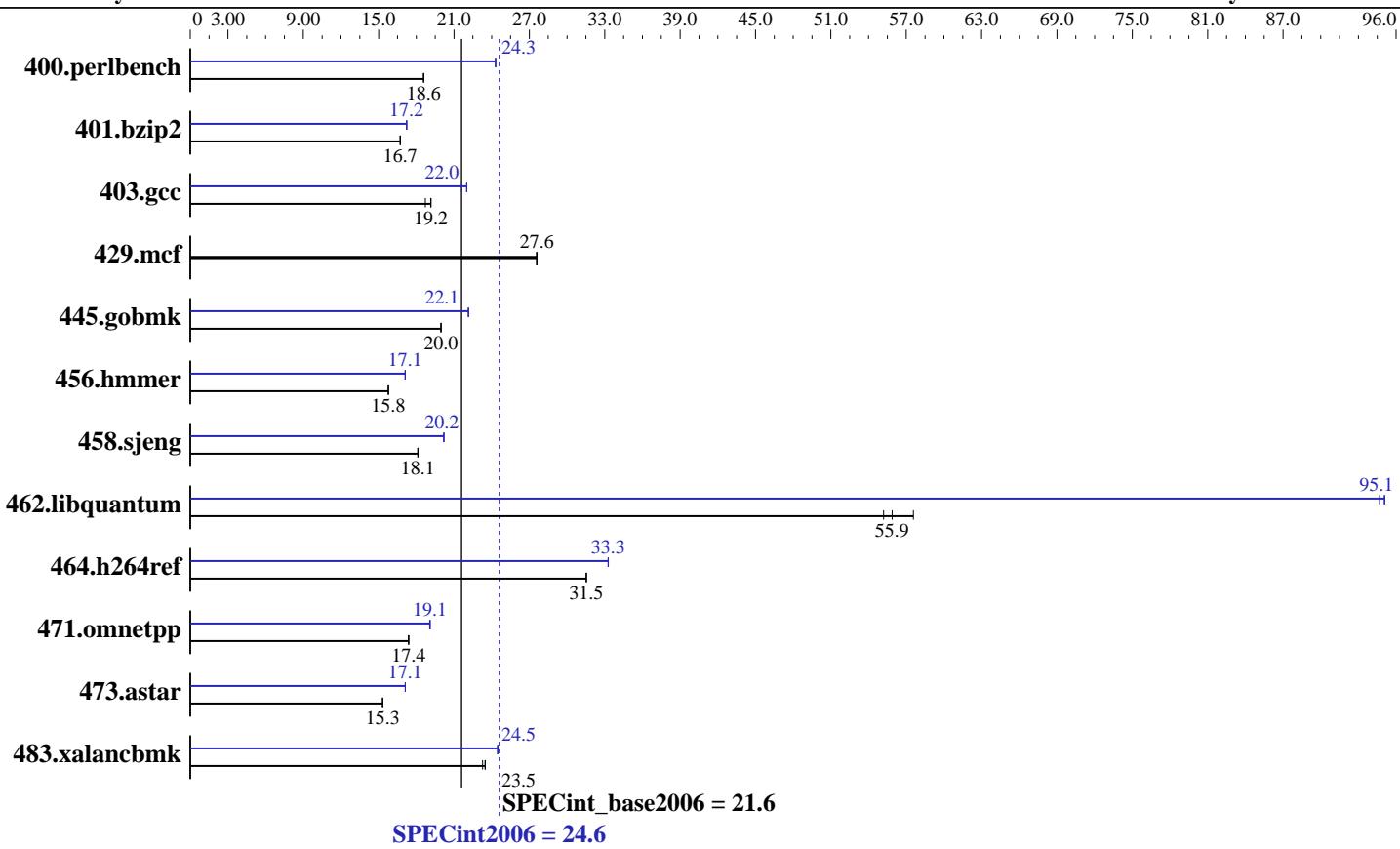
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Apr-2008

Hardware Availability: Jan-2008

Software Availability: Mar-2008



Hardware

CPU Name: Intel Core 2 Extreme QX9650
CPU Characteristics: 1333 MHz Bus Speed
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB 800 MHz ECC CL6 DDR2)
Disk Subsystem: 1 x 160 GB SATA 7200 RPM
Other Hardware: None

Software

Operating System: Windows Vista Ultimate (64-bit)
Compiler: Intel C++ Compiler for IA-32, Version 10.1
Build 20080312 Package ID: w_cc_p_10.1.021
Microsoft Visual Studio 2005 SP1
Auto Parallel: Yes
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	526	18.6	526	18.6	526	18.6	402	24.3	402	24.3	402	24.3
401.bzip2	577	16.7	577	16.7	578	16.7	560	17.2	560	17.2	560	17.2
403.gcc	420	19.2	420	19.2	430	18.7	366	22.0	366	22.0	373	21.6
429.mcf	331	27.6	331	27.6	330	27.6	331	27.6	331	27.6	330	27.6
445.gobmk	525	20.0	525	20.0	525	20.0	474	22.1	474	22.2	474	22.1
456.hammer	591	15.8	592	15.8	592	15.8	545	17.1	545	17.1	545	17.1
458.sjeng	667	18.1	668	18.1	668	18.1	599	20.2	599	20.2	599	20.2
462.libquantum	371	55.9	360	57.6	375	55.2	218	95.1	218	95.1	219	94.7
464.h264ref	702	31.5	702	31.5	702	31.5	665	33.3	665	33.3	665	33.3
471.omnetpp	360	17.4	359	17.4	359	17.4	327	19.1	328	19.1	327	19.1
473.astar	458	15.3	458	15.3	459	15.3	410	17.1	410	17.1	410	17.1
483.xalancbmk	294	23.5	294	23.5	296	23.3	282	24.5	282	24.5	282	24.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qstd=c99

C++ benchmarks:

icl

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

483.xalancbmk: -Qoption,cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib

C++ benchmarks:

-fast -Qcxx_features /F51200000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qstd=c99

C++ benchmarks:

icl

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

483.xalancbmk: -Qoption_cpp, --no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hummer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 libguide40.lib

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Peak Optimization Flags (Continued)

462.libquantum: -fast -Qunroll14 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll12
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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