SPEC® CINT2006 Result
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Acer Incorporated

Acer Altos G330 Mk2 (Intel Xeon X3320)

SPECint®_rate2006 =   59.2

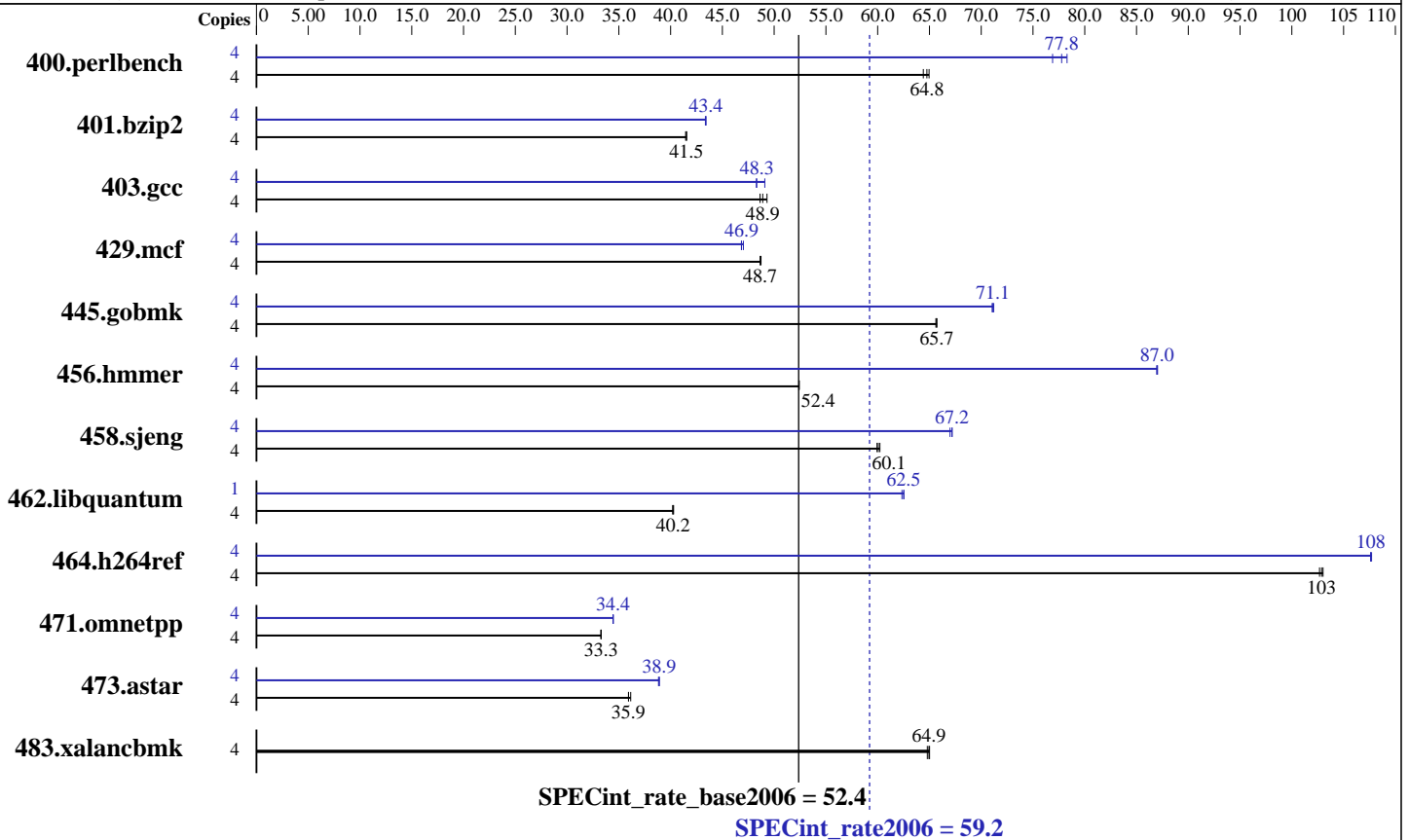SPECint_rate_base2006 = 52.4

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Apr-2008
Hardware Availability: Mar-2008
Software Availability: Nov-2007

Copies

Benchmark	Peak	Base
400.perlbench	77.8	64.8
401.bzip2	43.4	41.5
403.gcc	48.3	48.9
429.mcf	46.9	48.7
445.gobmk	71.1	65.7
456.hmmer	87.0	52.4
458.sjeng	67.2	60.1
462.libquantum	62.5	40.2
464.h264ref	108	103
471.omnetpp	34.4	33.3
473.astar	38.9	35.9
483.xalancbmk		64.9

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Hardware

CPU Name: Intel Xeon X3320
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2500
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)
Disk Subsystem: 1 x 160 GB 7200RPM SATA HDD
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 SP1 (x86_64)
Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler 10.1 for Linux
Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1
Binutils 2.17.50.0.15

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	602	65.0	**604**	**64.8**	607	64.4	4	508	76.9	499	78.3	**503**	**77.8**
401.bzip2	4	929	41.5	931	41.5	**929**	**41.5**	4	**889**	**43.4**	889	43.4	890	43.4
403.gcc	4	**659**	**48.9**	662	48.6	653	49.3	4	667	48.3	**666**	**48.3**	656	49.1
429.mcf	4	750	48.7	**750**	**48.7**	749	48.7	4	776	47.0	**779**	**46.9**	779	46.8
445.gobmk	4	639	65.7	**639**	**65.7**	639	65.6	4	589	71.2	**590**	**71.1**	591	71.1
456.hmmer	4	**712**	**52.4**	712	52.4	712	52.4	4	429	87.0	429	87.0	**429**	**87.0**
458.sjeng	4	**806**	**60.1**	808	59.9	804	60.2	4	723	67.0	**720**	**67.2**	720	67.2
462.libquantum	4	2057	40.3	2062	40.2	**2060**	**40.2**	1	**331**	**62.5**	332	62.3	331	62.5
464.h264ref	4	859	103	**860**	**103**	862	103	4	**823**	**108**	822	108	823	108
471.omnetpp	4	751	33.3	**751**	**33.3**	751	33.3	4	725	34.5	726	34.4	**726**	**34.4**
473.astar	4	781	35.9	**781**	**35.9**	777	36.1	4	723	38.8	**722**	**38.9**	722	38.9
483.xalancbmk	4	**425**	**64.9**	426	64.8	425	65.0	4	**425**	**64.9**	426	64.8	425	65.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode.
All results but peak 462.libquantum used taskset to bind
processes to cores.
"ulimit -s unlimited" was set for stacksize unlimited
OMP_NUM_THREAD set to number to cores
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
           -L/opt/intel/cce/10.1.008/lib
           -I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
           -L/opt/intel/cce/10.1.008/lib
           -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias`
`-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo`
`-no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
`-opt-streaming-stores always -vec-guard-write`
`-opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs`
`-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs`
`-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html

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You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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