



# SPEC® CINT2006 Result

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Itautec

SPECint®\_rate2006 = 67.0

Servidor Itautec MX201 (Intel Xeon E5430)

SPECint\_rate\_base2006 = 58.3

CPU2006 license: 9001

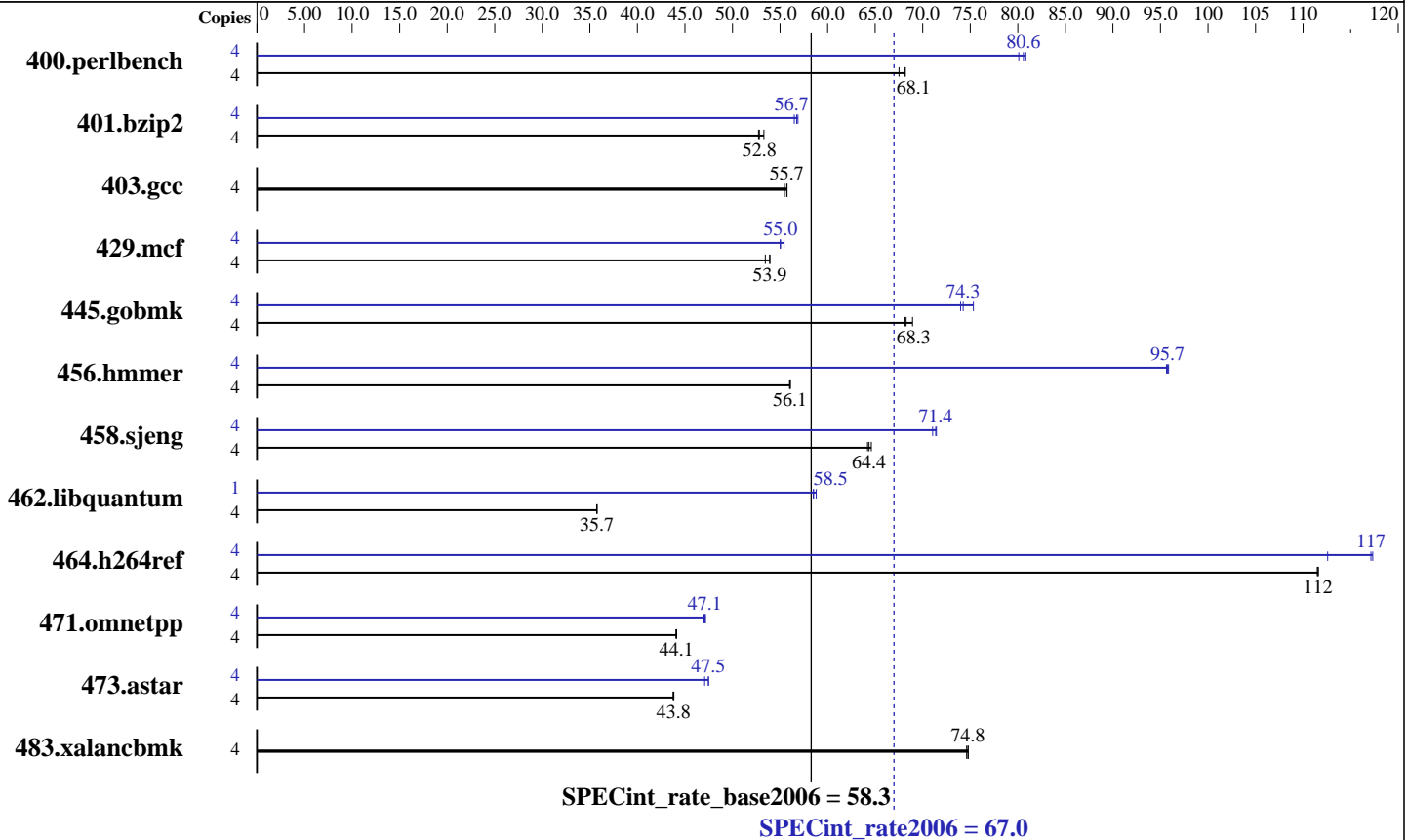
Test sponsor: Itautec

Tested by: Itautec

Test date: Apr-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



### Hardware

CPU Name: Intel Xeon E5430  
 CPU Characteristics:  
 CPU MHz: 2660  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>574</b>	<b>68.1</b>	573	68.2	579	67.5	4	483	80.8	488	80.1	<b>485</b>	<b>80.6</b>
401.bzip2	4	732	52.7	724	53.3	<b>731</b>	<b>52.8</b>	4	679	56.9	<b>680</b>	<b>56.7</b>	683	56.5
403.gcc	4	<b>578</b>	<b>55.7</b>	581	55.5	578	55.7	4	<b>578</b>	<b>55.7</b>	581	55.5	578	55.7
429.mcf	4	677	53.9	683	53.4	<b>677</b>	<b>53.9</b>	4	663	55.0	<b>663</b>	<b>55.0</b>	658	55.4
445.gobmk	4	616	68.1	609	68.9	<b>615</b>	<b>68.3</b>	4	557	75.3	567	74.0	<b>565</b>	<b>74.3</b>
456.hammer	4	<b>666</b>	<b>56.1</b>	666	56.0	665	56.1	4	390	95.6	<b>390</b>	<b>95.7</b>	389	95.9
458.sjeng	4	749	64.6	<b>752</b>	<b>64.4</b>	754	64.2	4	677	71.4	<b>678</b>	<b>71.4</b>	681	71.0
462.libquantum	4	2318	35.8	2320	35.7	<b>2319</b>	<b>35.7</b>	1	352	58.8	<b>354</b>	<b>58.5</b>	354	58.5
464.h264ref	4	794	111	<b>793</b>	<b>112</b>	793	112	4	786	113	<b>756</b>	<b>117</b>	755	117
471.omnetpp	4	<b>567</b>	<b>44.1</b>	567	44.1	568	44.0	4	532	47.0	<b>531</b>	<b>47.1</b>	530	47.2
473.astar	4	<b>641</b>	<b>43.8</b>	642	43.7	641	43.8	4	597	47.1	<b>592</b>	<b>47.5</b>	591	47.5
483.xalancbmk	4	369	74.8	370	74.6	<b>369</b>	<b>74.8</b>	4	369	74.8	370	74.6	<b>369</b>	<b>74.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Disabled

## General Notes

This result was measured on the Servidor Itautec MX201.  
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

456.hmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
403.gcc: basepeak = yes  
429.mcf: -fast -prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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