SPEC® CINT2006 Result

Itautec

Servidor Itautec LX211 (Intel Xeon E5430)

SPECint®_rate2006 = 120

SPECint_rate_base2006 = 93.2

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Apr-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Benchmark	Copies	Result
400.perlbench	8 / 8	159 / 134
401.bzip2	8 / 8	101 / 95.3
403.gcc	8 / 8	87.5 / 87.9
429.mcf	8 / 8	75.7 / 75.2
445.gobmk	8 / 8	142 / 131
456.hmmer	8 / 8	185 / 111
458.sjeng	8 / 8	138 / 125
462.libquantum	1 / 8	206 / 29.6
464.h264ref	8 / 8	233 / 223
471.omnetpp	8 / 8	57.7 / 56.8
473.astar	8 / 8	69.1 / 64.5
483.xalancbmk	8	116

SPECint_rate_base2006 = 93.2

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Hardware

CPU Name:	Intel Xeon E5430
CPU Characteristics:	
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	1 x SCSI, 73GB, 15000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run Level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.10.50 MicroQuill SmartHeap V8.1

SPEC CINT2006 Result
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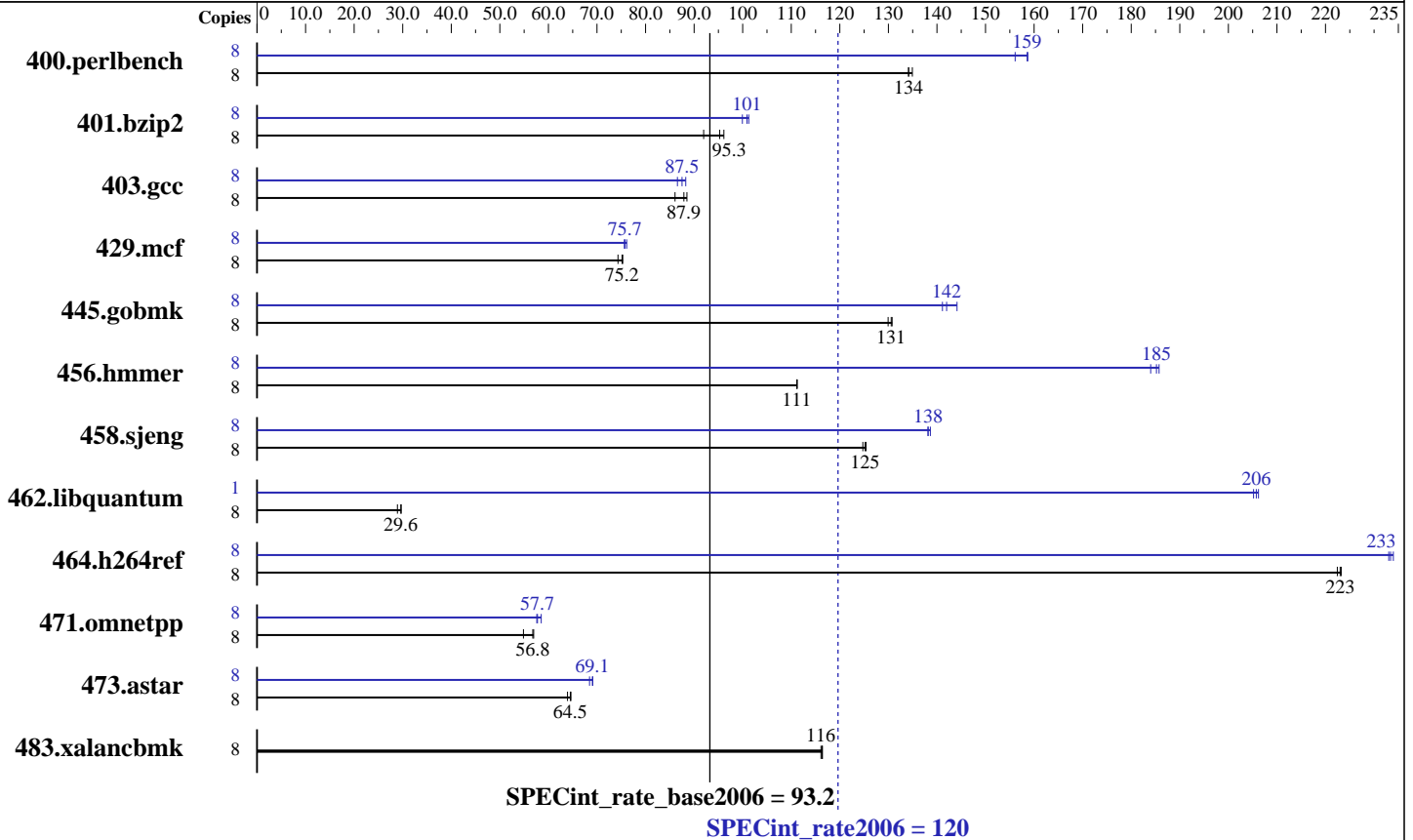
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	583	134	579	135	**583**	**134**	8	501	156	**493**	**159**	492	159
401.bzip2	8	839	92.0	**810**	**95.3**	803	96.1	8	773	99.9	**765**	**101**	762	101
403.gcc	8	748	86.1	**733**	**87.9**	728	88.5	8	730	88.3	744	86.6	**736**	**87.5**
429.mcf	8	982	74.3	**971**	**75.2**	969	75.3	8	959	76.1	**963**	**75.7**	965	75.6
445.gobmk	8	**643**	**131**	646	130	642	131	8	582	144	595	141	**591**	**142**
456.hmmer	8	672	111	671	111	**672**	**111**	8	402	186	**403**	**185**	406	184
458.sjeng	8	776	125	772	125	**773**	**125**	8	701	138	**701**	**138**	698	139
462.libquantum	8	5734	28.9	**5598**	**29.6**	5584	29.7	1	**101**	**206**	101	205	100	206
464.h264ref	8	796	222	793	223	**794**	**223**	8	760	233	**759**	**233**	757	234
471.omnetpp	8	911	54.9	**881**	**56.8**	878	57.0	8	854	58.5	**866**	**57.7**	868	57.6
473.astar	8	878	63.9	**870**	**64.5**	869	64.6	8	**813**	**69.1**	820	68.5	812	69.1
483.xalancbmk	8	475	116	**474**	**116**	474	116	8	475	116	**474**	**116**	474	116

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch Enabled
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:
```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.012/bin/icc
           -L/opt/intel/cce/10.1.012/lib
           -I/opt/intel/cce/10.1.012/include
```

```
456.hmmer: /opt/intel/cce/10.1.012/bin/icc
           -L/opt/intel/cce/10.1.012/lib
           -I/opt/intel/cce/10.1.012/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias`
`-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo`
`-no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
`-opt-streaming-stores always -vec-guard-write`
`-opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
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