



# SPEC® CINT2006 Result

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**Itautec**

**SPECint\_rate2006 = 62.2**

**Servidor Itautec LX211 (Intel Xeon E5430)**

**SPECint\_rate\_base2006 = 52.1**

**CPU2006 license:** 9001

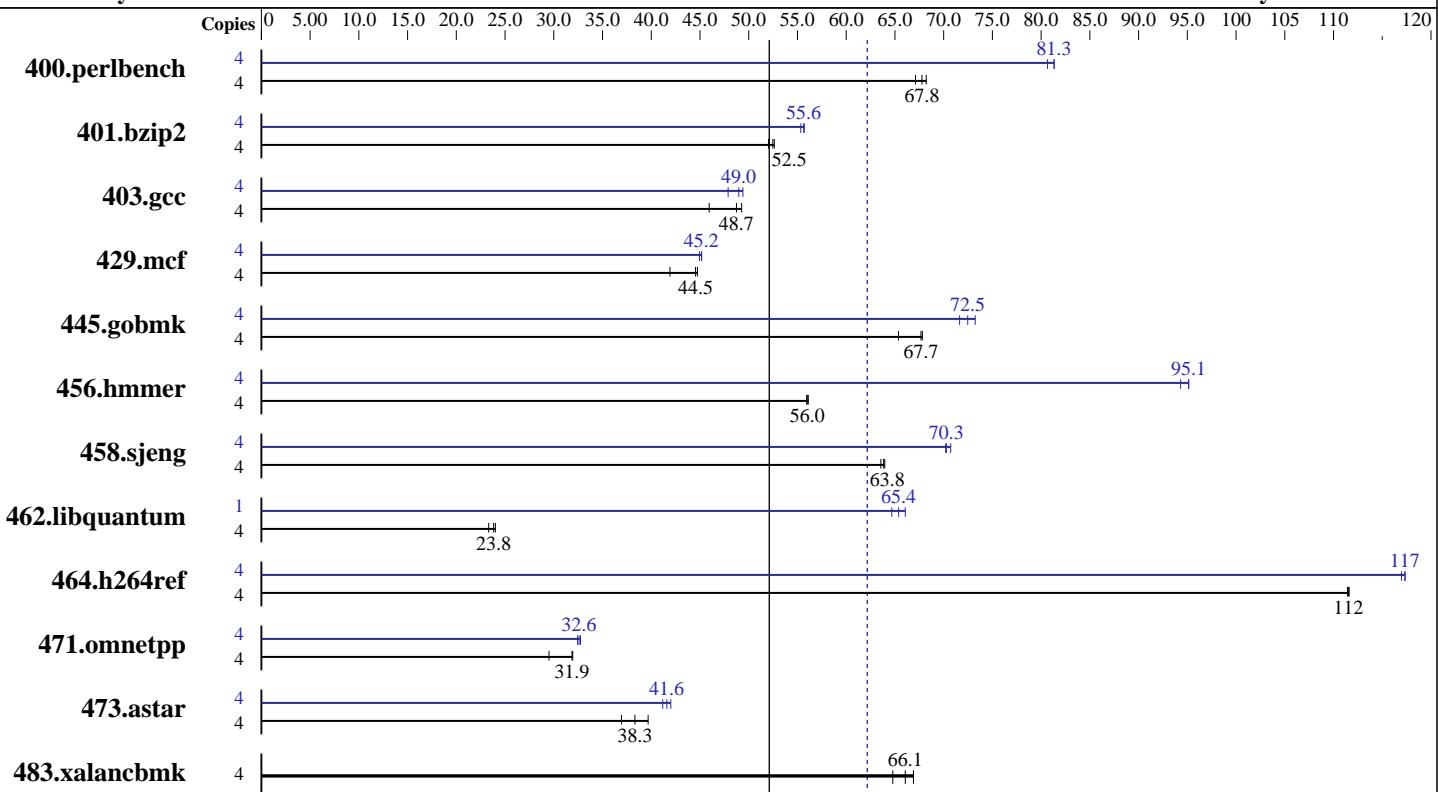
**Test date:** Apr-2008

**Test sponsor:** Itautec

**Hardware Availability:** Dec-2007

**Tested by:** Itautec

**Software Availability:** Jan-2008



**SPECint\_rate\_base2006 = 52.1**

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## Hardware

CPU Name:	Intel Xeon E5430
CPU Characteristics:	
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4 * 1 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	1 x SCSI, 73GB, 10000 RPM
Other Hardware:	None

## Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run Level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.10.50 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	582	67.1	<b>577</b>	<b>67.8</b>	573	68.2	4	485	80.7	480	81.3	<b>481</b>	<b>81.3</b>
401.bzip2	4	742	52.0	733	52.6	<b>736</b>	<b>52.5</b>	4	697	55.3	693	55.7	<b>694</b>	<b>55.6</b>
403.gcc	4	701	45.9	<b>661</b>	<b>48.7</b>	653	49.3	4	652	49.4	672	47.9	<b>657</b>	<b>49.0</b>
429.mcf	4	870	41.9	815	44.7	<b>819</b>	<b>44.5</b>	4	808	<b>45.2</b>	812	44.9	808	45.2
445.gobmk	4	642	65.4	619	67.8	<b>620</b>	<b>67.7</b>	4	579	<b>72.5</b>	573	73.2	586	71.6
456.hammer	4	667	55.9	<b>666</b>	<b>56.0</b>	665	56.1	4	396	94.3	392	95.1	<b>392</b>	<b>95.1</b>
458.sjeng	4	<b>758</b>	<b>63.8</b>	761	63.6	757	64.0	4	684	70.7	<b>689</b>	<b>70.3</b>	689	70.2
462.libquantum	4	3554	23.3	3452	24.0	<b>3478</b>	<b>23.8</b>	1	<b>317</b>	<b>65.4</b>	320	64.7	314	66.1
464.h264ref	4	<b>794</b>	<b>112</b>	794	111	793	112	4	754	117	<b>755</b>	<b>117</b>	757	117
471.omnetpp	4	847	29.5	783	31.9	<b>784</b>	<b>31.9</b>	4	764	32.7	771	32.4	<b>767</b>	<b>32.6</b>
473.astar	4	760	36.9	<b>733</b>	<b>38.3</b>	708	39.7	4	669	42.0	<b>675</b>	<b>41.6</b>	682	41.2
483.xalancbmk	4	426	64.8	<b>418</b>	<b>66.1</b>	413	66.9	4	426	64.8	<b>418</b>	<b>66.1</b>	413	66.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'./usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Enabled

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

456.hmmr: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmr: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll12 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll14 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.

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