



SPEC® CINT2006 Result

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Itaotec

SPECint®_rate2006 = 40.6

Servidor Itaotec LX211 (Intel Xeon E5310)

SPECint_rate_base2006 = 35.2

CPU2006 license: 9001

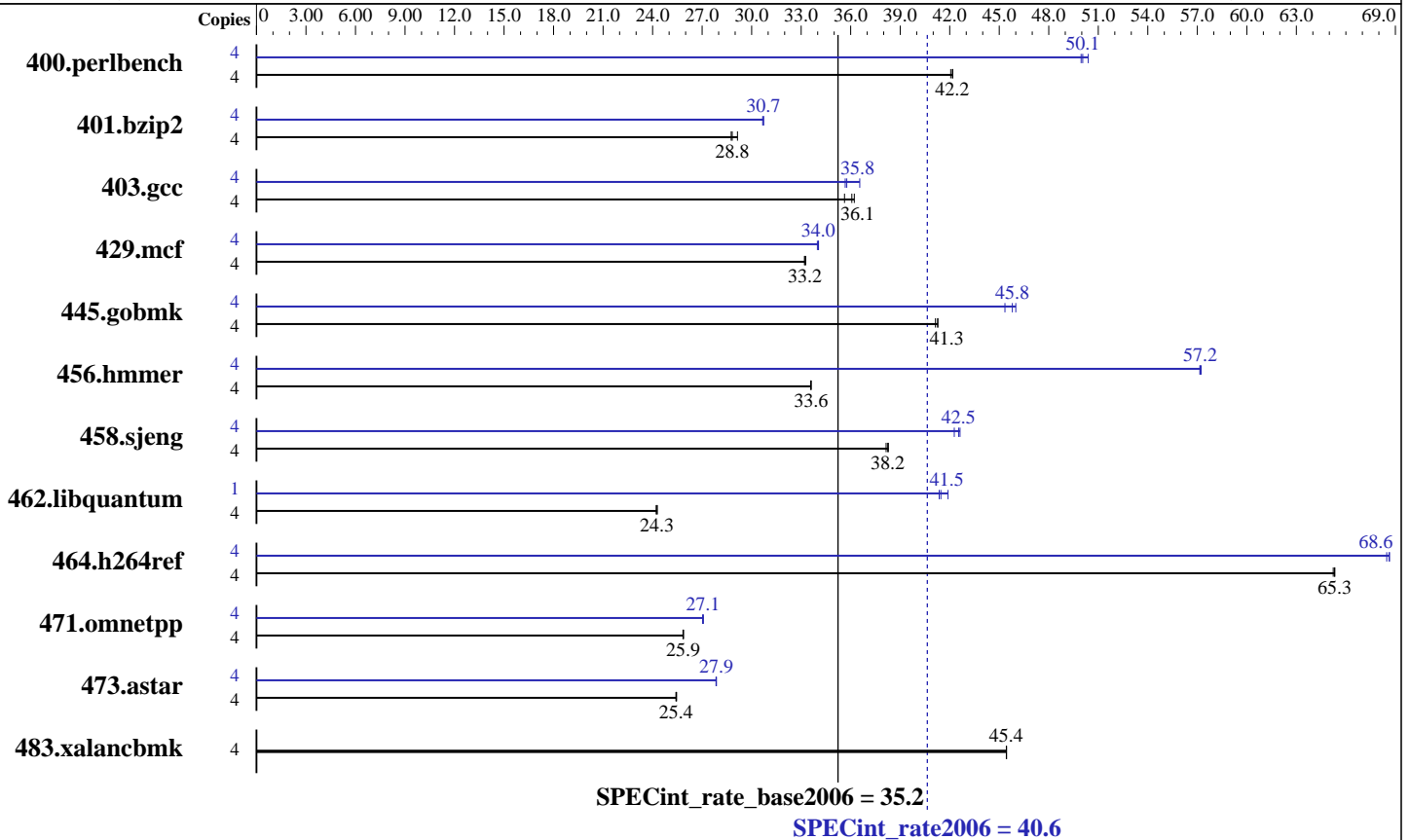
Test date: Apr-2008

Test sponsor: Itaotec

Hardware Availability: Dec-2007

Tested by: Itaotec

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5310
 CPU Characteristics:
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SCSI, 73GB, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>927</u>	<u>42.2</u>	929	42.1	926	42.2	4	775	50.4	782	50.0	<u>781</u>	<u>50.1</u>
401.bzip2	4	1325	29.1	<u>1339</u>	<u>28.8</u>	1342	28.8	4	1256	30.7	<u>1257</u>	<u>30.7</u>	1259	30.7
403.gcc	4	904	35.6	<u>893</u>	<u>36.1</u>	889	36.2	4	903	35.7	881	36.6	<u>900</u>	<u>35.8</u>
429.mcf	4	1099	33.2	<u>1098</u>	<u>33.2</u>	1096	33.3	4	1071	34.1	<u>1072</u>	<u>34.0</u>	1074	34.0
445.gobmk	4	1020	41.1	1017	41.3	<u>1017</u>	<u>41.3</u>	4	<u>916</u>	<u>45.8</u>	912	46.0	925	45.3
456.hammer	4	1111	33.6	<u>1111</u>	<u>33.6</u>	1111	33.6	4	652	57.2	653	57.2	<u>653</u>	<u>57.2</u>
458.sjeng	4	<u>1266</u>	<u>38.2</u>	1269	38.1	1264	38.3	4	1145	42.3	1136	42.6	<u>1138</u>	<u>42.5</u>
462.libquantum	4	3423	24.2	<u>3416</u>	<u>24.3</u>	3413	24.3	1	495	41.9	501	41.4	<u>500</u>	<u>41.5</u>
464.h264ref	4	1357	65.2	<u>1355</u>	<u>65.3</u>	1355	65.3	4	1290	68.6	<u>1290</u>	<u>68.6</u>	1293	68.5
471.omnetpp	4	966	25.9	<u>967</u>	<u>25.9</u>	968	25.8	4	923	27.1	<u>924</u>	<u>27.1</u>	925	27.0
473.astar	4	1103	25.5	<u>1104</u>	<u>25.4</u>	1105	25.4	4	1009	27.8	<u>1008</u>	<u>27.9</u>	1008	27.9
483.xalancbmk	4	608	45.4	<u>608</u>	<u>45.4</u>	607	45.4	4	608	45.4	<u>608</u>	<u>45.4</u>	607	45.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.

Platform Notes

BIOS configuration:
Hardware Prefetch Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

456.hmmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Itautec

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-INT-intel64-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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