SPEC® CINT2006 Result
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Itautec

SPECint®_rate2006 =　　54.3

Servidor Itautec LX201 (Intel Xeon E5410)
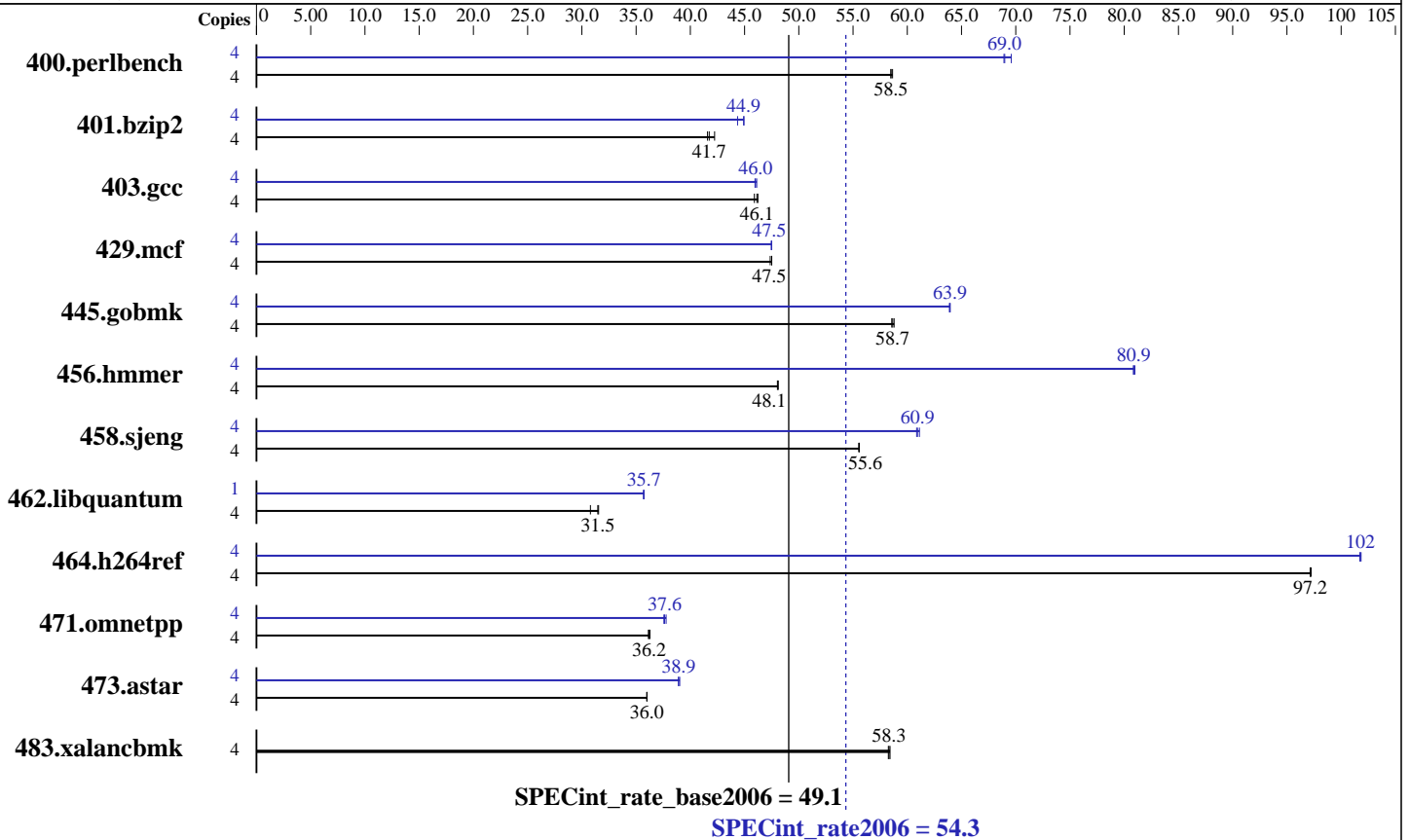
SPECint_rate_base2006 =　49.1

Benchmark	Copies	Result
400.perlbench	4	69.0
400.perlbench	4	58.5
401.bzip2	4	44.9
401.bzip2	4	41.7
403.gcc	4	46.0
403.gcc	4	46.1
429.mcf	4	47.5
429.mcf	4	47.5
445.gobmk	4	63.9
445.gobmk	4	58.7
456.hmmer	4	80.9
456.hmmer	4	48.1
458.sjeng	4	60.9
458.sjeng	4	55.6
462.libquantum	1	35.7
462.libquantum	4	31.5
464.h264ref	4	102
464.h264ref	4	97.2
471.omnetpp	4	37.6
471.omnetpp	4	36.2
473.astar	4	38.9
473.astar	4	36.0
483.xalancbmk	4	58.3

SPECint_rate_base2006 = 49.1

SPECint_rate2006 = 54.3

Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	
CPU MHz:	2330
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 * 1 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	1 x SATA-2, 160GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run Level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.10.50 MicroQuill SmartHeap V8.1

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CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Apr-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**668**	**58.5**	666	58.6	668	58.5	4	562	69.6	567	68.9	**566**	**69.0**
401.bzip2	4	914	42.2	928	41.6	**925**	**41.7**	4	859	45.0	870	44.4	**860**	**44.9**
403.gcc	4	702	45.9	**698**	**46.1**	697	46.2	4	698	46.1	701	46.0	**700**	**46.0**
429.mcf	4	**768**	**47.5**	768	47.5	770	47.4	4	768	47.5	**768**	**47.5**	769	47.5
445.gobmk	4	714	58.8	**715**	**58.7**	716	58.6	4	**656**	**63.9**	657	63.9	656	64.0
456.hmmer	4	**776**	**48.1**	777	48.0	776	48.1	4	461	81.0	**461**	**80.9**	462	80.8
458.sjeng	4	**871**	**55.6**	872	55.5	871	55.6	4	**794**	**60.9**	792	61.1	795	60.9
462.libquantum	4	2692	30.8	**2634**	**31.5**	2628	31.5	1	581	35.7	580	35.7	**580**	**35.7**
464.h264ref	4	**911**	**97.2**	910	97.2	911	97.1	4	870	102	**870**	**102**	869	102
471.omnetpp	4	**691**	**36.2**	689	36.3	692	36.1	4	666	37.6	662	37.8	**664**	**37.6**
473.astar	4	780	36.0	780	36.0	**780**	**36.0**	4	**722**	**38.9**	720	39.0	722	38.9
483.xalancbmk	4	474	58.3	**473**	**58.3**	473	58.4	4	474	58.3	**473**	**58.3**	473	58.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.012/bin/icc
         -L/opt/intel/cce/10.1.012/lib
         -I/opt/intel/cce/10.1.012/include
```

```
456.hmmer: /opt/intel/cce/10.1.012/bin/icc
         -L/opt/intel/cce/10.1.012/lib
         -I/opt/intel/cce/10.1.012/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
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