SPEC® CINT2006 Result

IBM Corporation

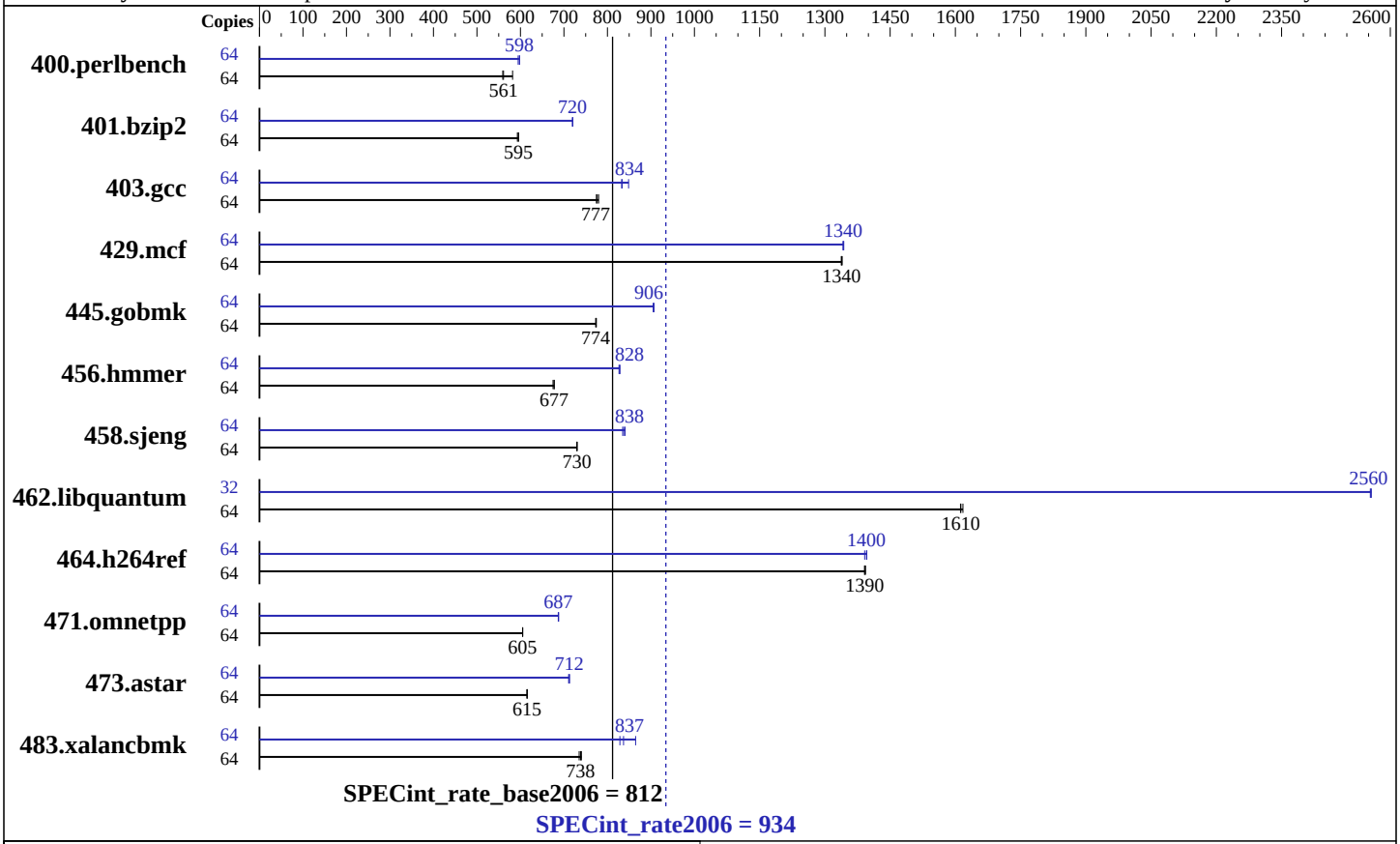
IBM Power 575 (4.7 GHz, 32 core)

SPECint®_rate2006 = 934

SPECint_rate_base2006 = 812

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Mar-2008
Hardware Availability: May-2008
Software Availability: May-2008



Hardware

CPU Name:	POWER6
CPU Characteristics:	
CPU MHz:	4700
FPU:	Integrated
CPU(s) enabled:	32 cores, 16 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	32 cores
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per core
L3 Cache:	32 MB I+D off chip per chip
Other Cache:	None
Memory:	128 GB (64x2 GB) DDR2 533 MHz
Disk Subsystem:	2x146 GB SFF SAS 10K RPM
Other Hardware:	None

Software

Operating System:	IBM AIX V5.3 with the 5300-08 Technology Level
Compiler:	XL C/C++ Enterprise Edition V9 for AIX Updated with the Oct2007 PTF.
Auto Parallel:	No
File System:	AIX/JFS2
System State:	Multi-user
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	--

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	1074	582	**1114**	**561**	1118	559	64	1046	598	1051	595	**1046**	**598**
401.bzip2	64	**1038**	**595**	1042	593	1037	595	64	857	720	858	719	**858**	**720**
403.gcc	64	**663**	**777**	665	775	661	780	64	619	833	**618**	**834**	607	849
429.mcf	64	436	1340	436	1340	**436**	**1340**	64	434	1340	435	1340	**435**	**1340**
445.gobmk	64	**867**	**774**	869	773	867	774	64	**741**	**906**	742	905	740	907
456.hmmer	64	885	675	881	678	**881**	**677**	64	**721**	**828**	722	827	721	829
458.sjeng	64	1061	730	**1061**	**730**	1061	730	64	927	835	921	841	**925**	**838**
462.libquantum	64	823	1610	820	1620	**822**	**1610**	32	259	2560	260	2550	**259**	**2560**
464.h264ref	64	**1018**	**1390**	1016	1390	1018	1390	64	**1015**	**1400**	1014	1400	1018	1390
471.omnetpp	64	661	605	661	605	**661**	**605**	64	582	687	582	688	**582**	**687**
473.astar	64	730	615	730	615	**730**	**615**	64	**631**	**712**	630	713	632	711
483.xalancbmk	64	597	740	601	735	**598**	**738**	64	511	865	533	829	**528**	**837**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
See flags file of details on following settings.
all ulimits set to unlimited.
Envionment variables set before executing benchmarks:
  MALLOCOPTIONS=pool
  MEMORY_AFFINITY=MCM
  XLFRTEOPTS=intrinthds=1
System set to "Enhanced" mode when defining partition on HMC.
System set to "Chip affinity" mode using the HMC command
  chsyscfg ... -i "addr_broadcast_perf_policy=chip_affinity"
bindprocessor command used on submit to bind each copy to a
  unique processor.
6400 16M large pages defined with vmo command
Remote console disabled in /etc/inittab.
fdpr binary optimization tool used for:
  400.perlbench 401.bzip2 403.gcc 429.mcf 456.hmmer
  458.sjent 462.libquantum 464.h264ref 473.astar
```

Base Compiler Invocation

C benchmarks:

```
/usr/vac/bin/xlc -qlanglvl=extc99
```

C++ benchmarks:

```
/usr/vacpp/bin/xlC
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_AIX
462.libquantum: -DSPEC_CPU_AIX
464.h264ref: -DSPEC_CPU_AIX -qchars=signed
483.xalancbmk: -DSPEC_CPU_AIX

Base Optimization Flags

C benchmarks:
-bmaxdata:0x50000000 -O5 -qlargepage -D_ILS_MACROS -qalias=noansi -qalloca -blpdata

C++ benchmarks:
-bmaxdata:0x20000000 -O5 -qlargepage -D_ILS_MACROS -qrtti=all -blpdata

Base Other Flags

C benchmarks:
-qipa=noobject -qipa=threads -qsuppress=1500-036

C++ benchmarks:
-qipa=noobject -qipa=threads -qsuppress=1500-036

Peak Compiler Invocation

C benchmarks:
/usr/vac/bin/xlc -qlanglvl=extc99

C++ benchmarks:
/usr/vacpp/bin/xlC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_AIX
403.gcc: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_AIX
464.h264ref: -DSPEC_CPU_AIX -qchars=signed
483.xalancbmk: -DSPEC_CPU_AIX

Peak Optimization Flags

C benchmarks:

400.perlbench: -bmaxdata:0x50000000 -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qlargepage -qenablevmx -qvecnvol -D_ILS_MACROS -qalias=noansi -qfdpr -blpdata

401.bzip2: -bmaxdata:0x4fffffc -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -qenablevmx -qvecnvol -D_ILS_MACROS -qfdpr -blpdata

403.gcc: -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qlargepage -D_ILS_MACROS -qalloca -qfdpr -q64 -blpdata

429.mcf: -bmaxdata:0x50000000 -O5 -qlargepage -qenablevmx -qvecnvol -D_ILS_MACROS -qfdpr -blpdata

445.gobmk: -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qlargepage -qenablevmx -qvecnvol -D_ILS_MACROS -blpdata

456.hmmer: -O5 -qlargepage -D_ILS_MACROS -qfdpr -blpdata

458.sjeng: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -qenablevmx -qvecnvol -D_ILS_MACROS -qfdpr -blpdata

462.libquantum: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -qenablevmx -qvecnvol -D_ILS_MACROS -q64 -qfdpr -blpdata

464.h264ref: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -q64 -D_ILS_MACROS -qenablevmx -qvecnvol -qfdpr -bdatapsize:64K -bstackpsize:64K -btextpsize:64K

C++ benchmarks:

471.omnetpp: -bmaxdata:0x20000000 -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -qenablevmx -qvecnvol -D_ILS_MACROS -qalign=natural -qrtti=all -qinlglue -blpdata

473.astar: -bmaxdata:0x20000000 -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -D_ILS_MACROS -qfdpr -qinlglue -qalign=natural -blpdata

483.xalancbmk: -bmaxdata:0x20000000 -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -D_ILS_MACROS -qinlglue -D__IBM_FAST_VECTOR -blpdata

Peak Other Flags

C benchmarks:
```
-qipa=noobject -qipa=threads -qsuppress=1500-036
```

C++ benchmarks:
```
-qipa=noobject -qipa=threads -qsuppress=1500-036
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/IBM-AIX-XL.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/IBM-AIX-XL.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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