



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®_rate2006 = 81.5

PowerEdge M600 (Intel Xeon X5260, 3.33 GHz)

SPECint_rate_base2006 = 69.1

CPU2006 license: 55

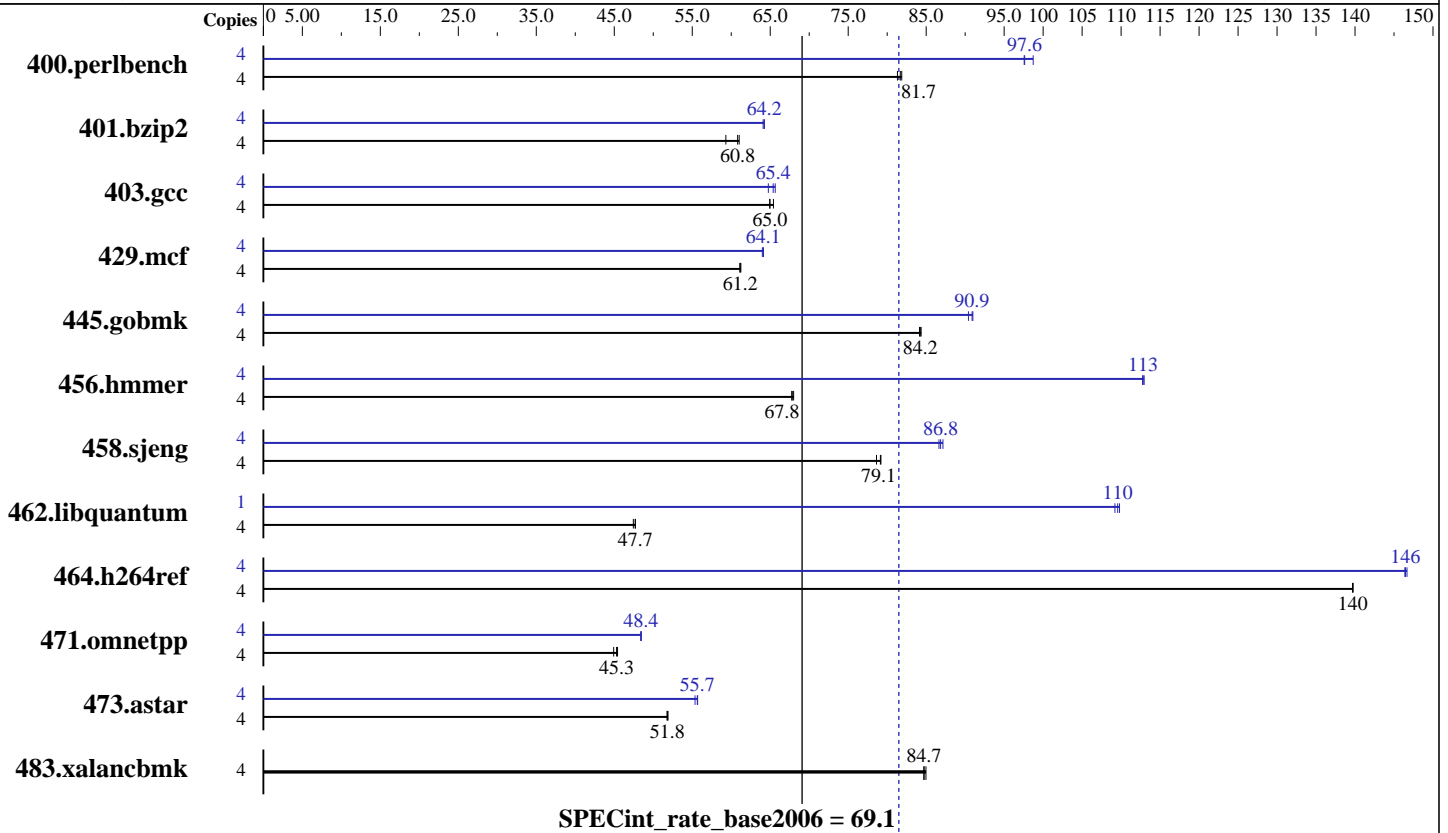
Test date: Feb-2008

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 667 MHz ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 80 GB 7200 RPM SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap library V8.1



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|-------------|-------------|-------------|------------|-------------|--------|------------|-------------|------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | 478 | 81.7 | 477 | 81.9 | 481 | 81.3 | 4 | 400 | 97.6 | 396 | 98.7 | 401 | 97.6 |
| 401.bzip2 | 4 | 633 | 61.0 | 635 | 60.8 | 651 | 59.3 | 4 | 601 | 64.2 | 601 | 64.2 | 602 | 64.1 |
| 403.gcc | 4 | 492 | 65.4 | 496 | 65.0 | 496 | 64.9 | 4 | 491 | 65.6 | 497 | 64.8 | 492 | 65.4 |
| 429.mcf | 4 | 597 | 61.1 | 596 | 61.2 | 597 | 61.2 | 4 | 570 | 64.0 | 569 | 64.1 | 569 | 64.1 |
| 445.gobmk | 4 | 499 | 84.1 | 497 | 84.4 | 498 | 84.2 | 4 | 464 | 90.4 | 461 | 91.0 | 462 | 90.9 |
| 456.hammer | 4 | 549 | 68.0 | 551 | 67.8 | 550 | 67.8 | 4 | 331 | 113 | 331 | 113 | 330 | 113 |
| 458.sjeng | 4 | 616 | 78.6 | 611 | 79.2 | 612 | 79.1 | 4 | 555 | 87.2 | 559 | 86.6 | 557 | 86.8 |
| 462.libquantum | 4 | 1747 | 47.4 | 1738 | 47.7 | 1737 | 47.7 | 1 | 189 | 110 | 189 | 110 | 190 | 109 |
| 464.h264ref | 4 | 634 | 140 | 634 | 140 | 634 | 140 | 4 | 604 | 147 | 605 | 146 | 604 | 146 |
| 471.omnetpp | 4 | 552 | 45.3 | 556 | 44.9 | 550 | 45.4 | 4 | 516 | 48.5 | 516 | 48.4 | 517 | 48.4 |
| 473.astar | 4 | 542 | 51.8 | 541 | 51.9 | 542 | 51.8 | 4 | 505 | 55.7 | 504 | 55.7 | 507 | 55.4 |
| 483.xalancbmk | 4 | 326 | 84.7 | 326 | 84.7 | 325 | 85.0 | 4 | 326 | 84.7 | 326 | 84.7 | 325 | 85.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.03.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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