



SPEC[®] CINT2006 Result

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Bull SAS

SPECint[®]_rate2006 = 362

Bull Escala PL1660 (3.5 GHz, 16 cores)

SPECint_rate_base2006 = 318

CPU2006 license: 20

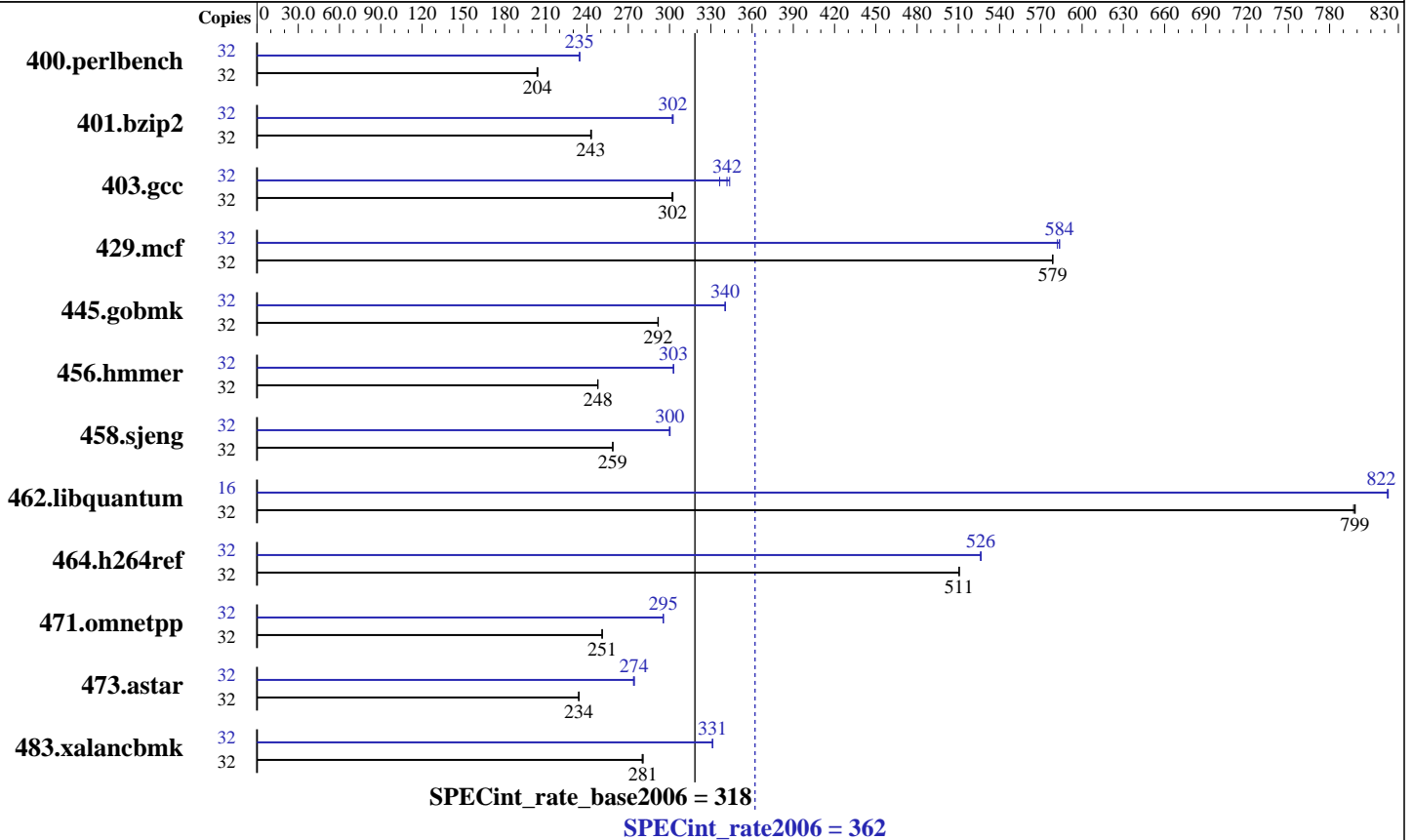
Test date: Mar-2008

Test sponsor: Bull SAS

Hardware Availability: Oct-2007

Tested by: Bull SAS

Software Availability: Oct-2007



Hardware

CPU Name: POWER6
 CPU Characteristics:
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 16 cores, 8 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 4,8,12,16 cores
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per core
 L3 Cache: 32 MB I+D off chip per chip
 Other Cache: None
 Memory: 128 GB (64x2 GB) DDR2 667 MHz
 Disk Subsystem: 2x73 GB SAS 15K RPM
 Other Hardware: None

Software

Operating System: IBM AIX 5L V5.3 updated with the 5300-07 Technology Level
 Compiler: XL C/C++ Enterprise Edition V9 for AIX Updated with the Oct2007 PTF.
 Auto Parallel: No
 File System: AIX/JFS2
 System State: Multi-user
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: --



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	<u>1533</u>	<u>204</u>	1535	204	1531	204	32	<u>1333</u>	<u>235</u>	1333	235	1332	235
401.bzip2	32	<u>1272</u>	<u>243</u>	1269	243	1273	243	32	1023	302	1021	303	<u>1021</u>	<u>302</u>
403.gcc	32	<u>852</u>	<u>302</u>	852	302	853	302	32	766	336	<u>754</u>	<u>342</u>	749	344
429.mcf	32	504	578	504	579	<u>504</u>	<u>579</u>	32	<u>500</u>	<u>584</u>	501	582	500	584
445.gobmk	32	1150	292	1151	292	<u>1151</u>	<u>292</u>	32	<u>986</u>	<u>340</u>	986	340	986	341
456.hammer	32	1204	248	1205	248	<u>1204</u>	<u>248</u>	32	<u>986</u>	<u>303</u>	985	303	986	303
458.sjeng	32	1497	259	1496	259	<u>1497</u>	<u>259</u>	32	1291	300	1289	300	<u>1290</u>	<u>300</u>
462.libquantum	32	831	797	830	799	<u>830</u>	<u>799</u>	16	403	822	403	823	<u>403</u>	<u>822</u>
464.h264ref	32	1387	511	1387	511	<u>1387</u>	<u>511</u>	32	1346	526	<u>1345</u>	<u>526</u>	1345	527
471.omnetpp	32	797	251	<u>797</u>	<u>251</u>	797	251	32	<u>677</u>	<u>295</u>	677	295	677	296
473.astar	32	959	234	960	234	<u>960</u>	<u>234</u>	32	819	274	<u>819</u>	<u>274</u>	820	274
483.xalancbmk	32	786	281	<u>787</u>	<u>281</u>	788	280	32	666	331	667	331	<u>667</u>	<u>331</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

See flags file of details on following settings.
all ulimits set to unlimited.
Environment variables set before executing benchmarks:
MALLOCOPTIONS=pool
MEMORY_AFFINITY=MCM
XLFRTOPTS=intrinthds=1
System set to "Enhanced" mode when defining partition on HMC.
bindprocessor command used on submit to bind each copy to a unique processor.
Remote console disabled in /etc/inittab.
fdpr binary optimization tool used for:
400.perlbench 401.bzip2 403.gcc 429.mcf 456.hammer
458.sjeng 462.libquantum 464.h264ref 473.astar
4000 16M large pages defined with vmo command

Base Compiler Invocation

C benchmarks:
/usr/vac/bin/xlc -qlanglvl=extc99

C++ benchmarks:
/usr/vacpp/bin/xlC



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Base Portability Flags

400.perlbench: -DSPEC_CPU_AIX
462.libquantum: -DSPEC_CPU_AIX
464.h264ref: -DSPEC_CPU_AIX -qchars=signed
483.xalancbmk: -DSPEC_CPU_AIX

Base Optimization Flags

C benchmarks:
-bmaxdata:0x50000000 -O5 -qlargepage -D_ILS_MACROS -qalias=noansi
-qalloca -blpdata

C++ benchmarks:
-bmaxdata:0x20000000 -O5 -qlargepage -D_ILS_MACROS -qrtti=all
-blpdata

Base Other Flags

C benchmarks:
-qipa=noobject -qipa=threads -qsuppress=1500-036

C++ benchmarks:
-qipa=noobject -qipa=threads -qsuppress=1500-036

Peak Compiler Invocation

C benchmarks:
/usr/vac/bin/xlc -qlanglvl=extc99

C++ benchmarks:
/usr/vacpp/bin/xlC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_AIX
403.gcc: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_AIX
464.h264ref: -DSPEC_CPU_AIX -qchars=signed
483.xalancbmk: -DSPEC_CPU_AIX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -bmaxdata:0x50000000 -qpdf1(pass 1) -qpdf2(pass 2) -O4
-qlargepage -qenablevmx -qvecnvml -D_ILS_MACROS
-qalias=noansi -qfdpr -blpdata

401.bzip2: -bmaxdata:0x4ffffffc -qpdf1(pass 1) -qpdf2(pass 2) -O5
-qlargepage -qenablevmx -qvecnvml -D_ILS_MACROS -qfdpr
-blpdata

403.gcc: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage
-D_ILS_MACROS -qalloca -qfdpr -q64 -blpdata

429.mcf: -bmaxdata:0x50000000 -O5 -qlargepage -qenablevmx
-qvecnvml -D_ILS_MACROS -qfdpr -blpdata

445.gobmk: -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qlargepage -qenablevmx
-qvecnvml -D_ILS_MACROS -blpdata

456.hmmr: -O5 -qlargepage -D_ILS_MACROS -qfdpr -blpdata

458.sjeng: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -qenablevmx
-qvecnvml -D_ILS_MACROS -qfdpr -blpdata

462.libquantum: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -qenablevmx
-qvecnvml -D_ILS_MACROS -q64 -qfdpr -blpdata

464.h264ref: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -q64 -D_ILS_MACROS
-qenablevmx -qvecnvml -qfdpr -bdatapsize:64K
-bstacksize:64K -btextpsize:64K

C++ benchmarks:

471.omnetpp: -bmaxdata:0x20000000 -qpdf1(pass 1) -qpdf2(pass 2) -O5
-qlargepage -qenablevmx -qvecnvml -D_ILS_MACROS
-qalign=natural -qrtti=all -qinlglue -blpdata

473.astar: -bmaxdata:0x20000000 -qpdf1(pass 1) -qpdf2(pass 2) -O5
-qlargepage -D_ILS_MACROS -qfdpr -qinlglue
-qalign=natural -blpdata

483.xalancbmk: -bmaxdata:0x20000000 -qpdf1(pass 1) -qpdf2(pass 2) -O5
-qlargepage -D_ILS_MACROS -qinlglue -D__IBM_FAST_VECTOR
-blpdata



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Peak Other Flags

C benchmarks:

`-qipa=noobject -qipa=threads -qsuppress=1500-036`

C++ benchmarks:

`-qipa=noobject -qipa=threads -qsuppress=1500-036`

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.06.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.06.xml

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