



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®_rate2006 = Not Run

IBM System x3500 (Intel Xeon E5420)

SPECfp_rate_base2006 = 63.1

CPU2006 license: 11

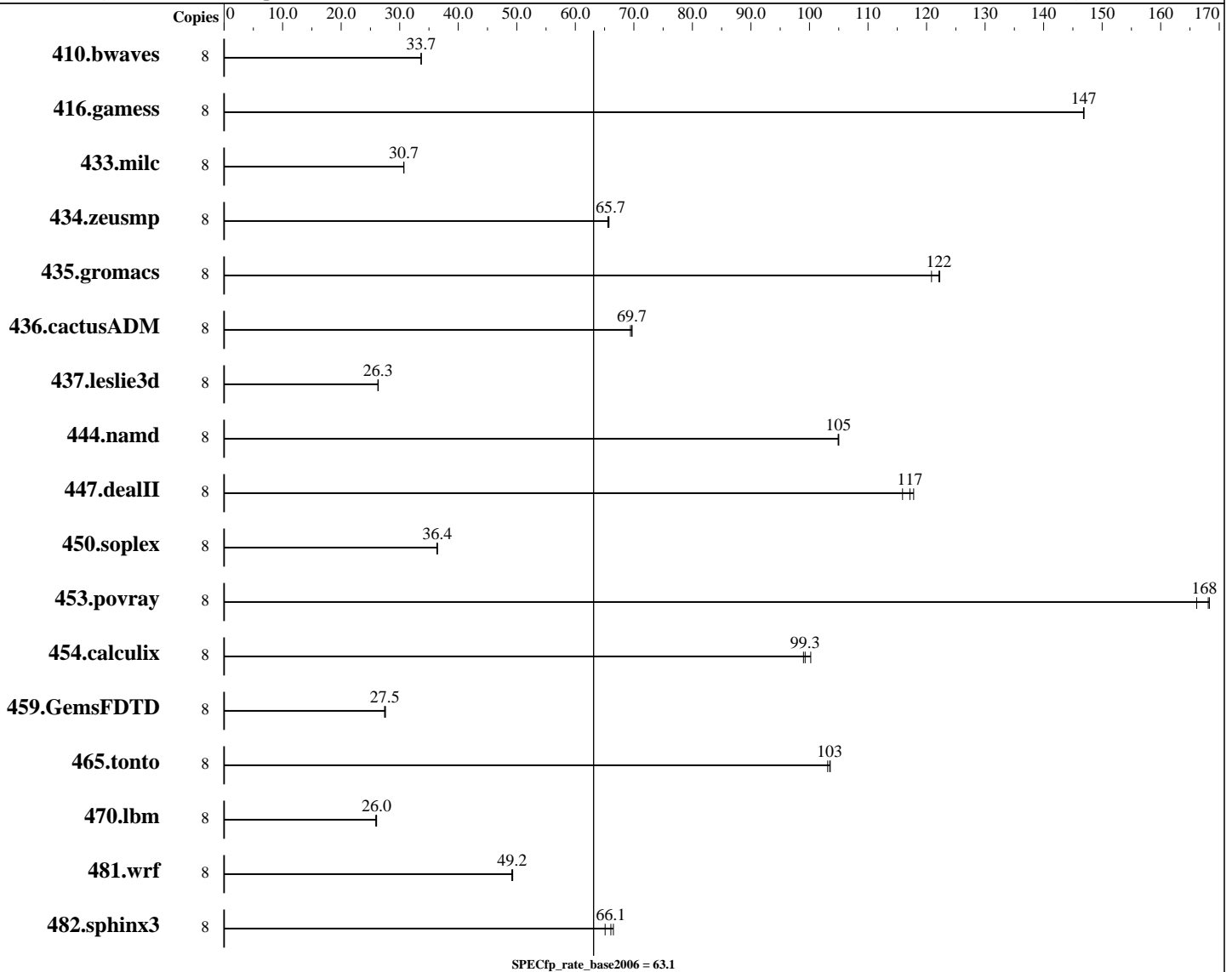
Test date: Mar-2008

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5420
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3228	33.7	<u>3229</u>	<u>33.7</u>	3230	33.7							
416.gamess	8	<u>1067</u>	<u>147</u>	1067	147	1066	147							
433.milc	8	<u>2393</u>	<u>30.7</u>	2391	30.7	2393	30.7							
434.zeusmp	8	1111	65.6	<u>1109</u>	<u>65.7</u>	1107	65.8							
435.gromacs	8	467	122	<u>468</u>	<u>122</u>	473	121							
436.cactusADM	8	1372	69.7	<u>1372</u>	<u>69.7</u>	1377	69.4							
437.leslie3d	8	2855	26.3	2862	26.3	<u>2857</u>	<u>26.3</u>							
444.namd	8	<u>611</u>	<u>105</u>	611	105	612	105							
447.dealII	8	790	116	777	118	<u>781</u>	<u>117</u>							
450.soplex	8	<u>1832</u>	<u>36.4</u>	1830	36.4	1835	36.4							
453.povray	8	256	166	<u>253</u>	<u>168</u>	253	168							
454.calculix	8	659	100	<u>665</u>	<u>99.3</u>	667	99.0							
459.GemsFDTD	8	3095	27.4	<u>3090</u>	<u>27.5</u>	3079	27.6							
465.tonto	8	<u>761</u>	<u>103</u>	763	103	760	104							
470.lbm	8	<u>4221</u>	<u>26.0</u>	4243	25.9	4219	26.1							
481.wrf	8	<u>1815</u>	<u>49.2</u>	1818	49.2	1812	49.3							
482.sphinx3	8	2344	66.5	<u>2360</u>	<u>66.1</u>	2395	65.1							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
taskset utility used to bind CPU(s) to processes
This result is measured on an IBM System x3500 Server. Note
that the IBM System x3500 and IBM System x3400 are electrically
equivalent.

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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