SPEC® CINT2006 Result
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IBM Corporation

IBM System x3500 (Intel Xeon E5410)

$SPECint^®2006 =$ 21.5
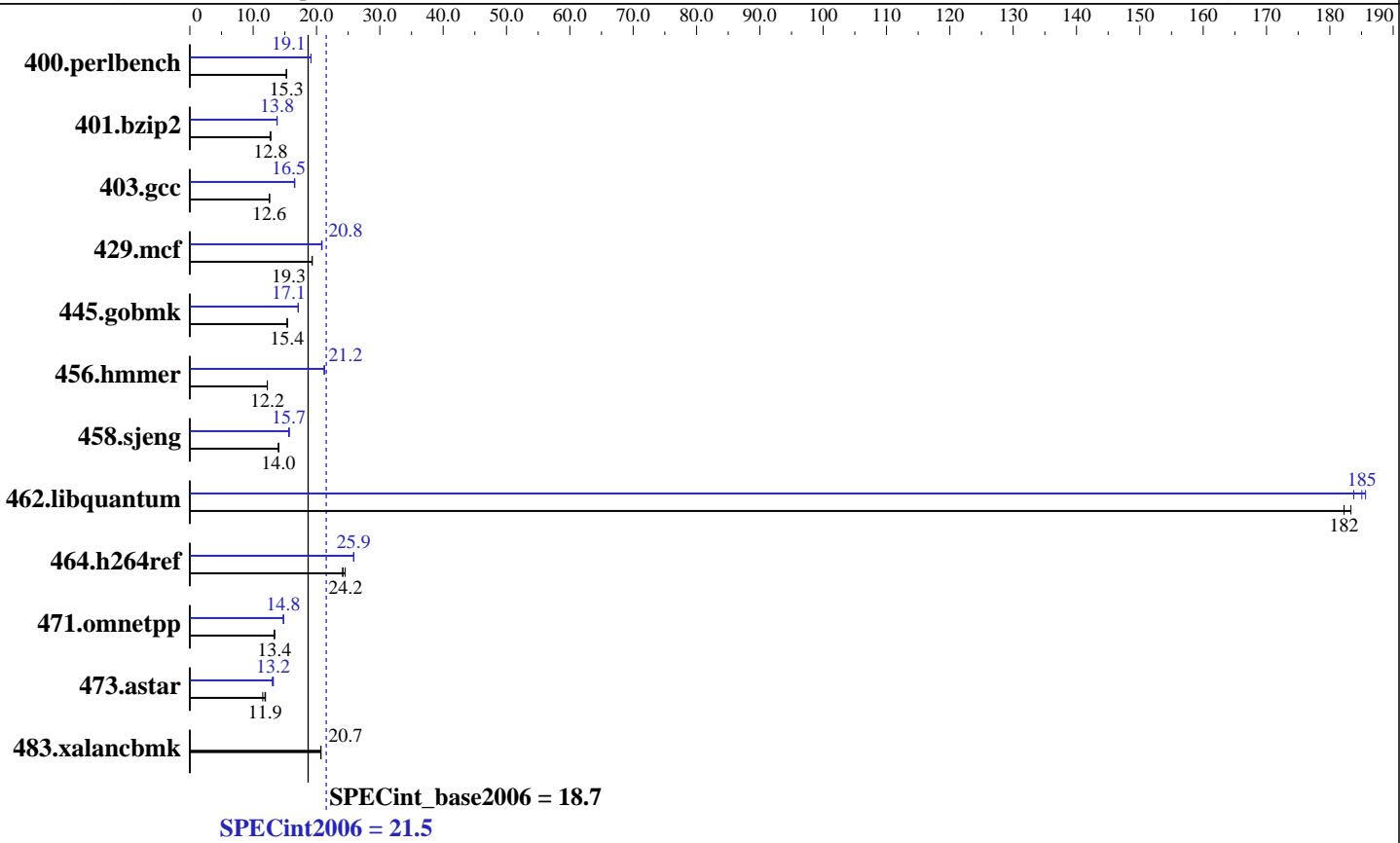
$SPECint\_base2006 =$ 18.7

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Mar-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

400.perlbench	19.1 / 15.3
401.bzip2	13.8 / 12.8
403.gcc	16.5 / 12.6
429.mcf	20.8 / 19.3
445.gobmk	17.1 / 15.4
456.hmmer	21.2 / 12.2
458.sjeng	15.7 / 14.0
462.libquantum	185 / 182
464.h264ref	25.9 / 24.2
471.omnetpp	14.8 / 13.4
473.astar	13.2 / 11.9
483.xalancbmk	20.7

SPECint_base2006 = 18.7

SPECint2006 = 21.5

Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	1333MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem:	1 x 80 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**640**	**15.3**	643	15.2	639	15.3	**511**	**19.1**	511	19.1	510	19.1
401.bzip2	761	12.7	756	12.8	**756**	**12.8**	700	13.8	**701**	**13.8**	701	13.8
403.gcc	641	12.6	640	12.6	**640**	**12.6**	488	16.5	**486**	**16.5**	486	16.6
429.mcf	473	19.3	**472**	**19.3**	472	19.3	437	20.9	**438**	**20.8**	438	20.8
445.gobmk	**682**	**15.4**	683	15.4	682	15.4	613	17.1	613	17.1	**613**	**17.1**
456.hmmer	762	12.2	763	12.2	**763**	**12.2**	**439**	**21.2**	440	21.2	439	21.3
458.sjeng	**864**	**14.0**	865	14.0	863	14.0	775	15.6	**772**	**15.7**	771	15.7
462.libquantum	113	183	114	182	**114**	**182**	**112**	**185**	112	186	113	184
464.h264ref	918	24.1	**915**	**24.2**	903	24.5	854	25.9	856	25.9	**855**	**25.9**
471.omnetpp	469	13.3	**468**	**13.4**	467	13.4	423	14.8	422	14.8	**423**	**14.8**
473.astar	588	11.9	**592**	**11.9**	610	11.5	539	13.0	533	13.2	**533**	**13.2**
483.xalancbmk	334	20.7	**333**	**20.7**	333	20.7	334	20.7	**333**	**20.7**	333	20.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Enabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null
This result is measured on an IBM System x3500 Server. Note
that the IBM System x3500 and IBM System x3400 are electrically
equivalent.
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-fast -vec-guard-write -parallel -par-runtime-control
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib  -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
           -L/opt/intel/cce/10.1.008/lib
           -I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
           -L/opt/intel/cce/10.1.008/lib
           -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Continued on next page

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Peak Optimization Flags (Continued)

```
     400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
                    -prefetch

        401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
                    -auto-ilp32

          403.gcc: -fast -inline-calloc -opt-malloc-options=3

          429.mcf: -fast -prefetch

        445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
                    -no-prec-div -ansi-alias

        456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
                    -auto-ilp32

        458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

    462.libquantum: -fast -unroll4 -Ob0 -prefetch
                    -opt-streaming-stores always -vec-guard-write
                    -opt-malloc-options=3 -parallel -par-runtime-control

        464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
                    -ansi-alias
```

C++ benchmarks:

```
       471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
                    -no-prec-div -ansi-alias -opt-ra-region-strategy=block
                    -Wl,-z,muldefs
                    -L/spec/users/rahul/cpu2006.1.0/lib  -lsmartheap

         473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
                    -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
                    -Wl,-z,muldefs
                    -L/spec/users/rahul/cpu2006.1.0/lib  -lsmartheap
```

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

```
          403.gcc: -Dalloca=_alloca
```

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The flags file that was used to format this result can be browsed at

`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.html`

You can also download the XML flags source by saving the following link:

`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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