



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Itautec

## SPECint<sup>®</sup>\_rate2006 = 129

### Servidor Itautec MX201 (Intel Xeon E5440)

## SPECint\_rate\_base2006 = 105

CPU2006 license: 9001

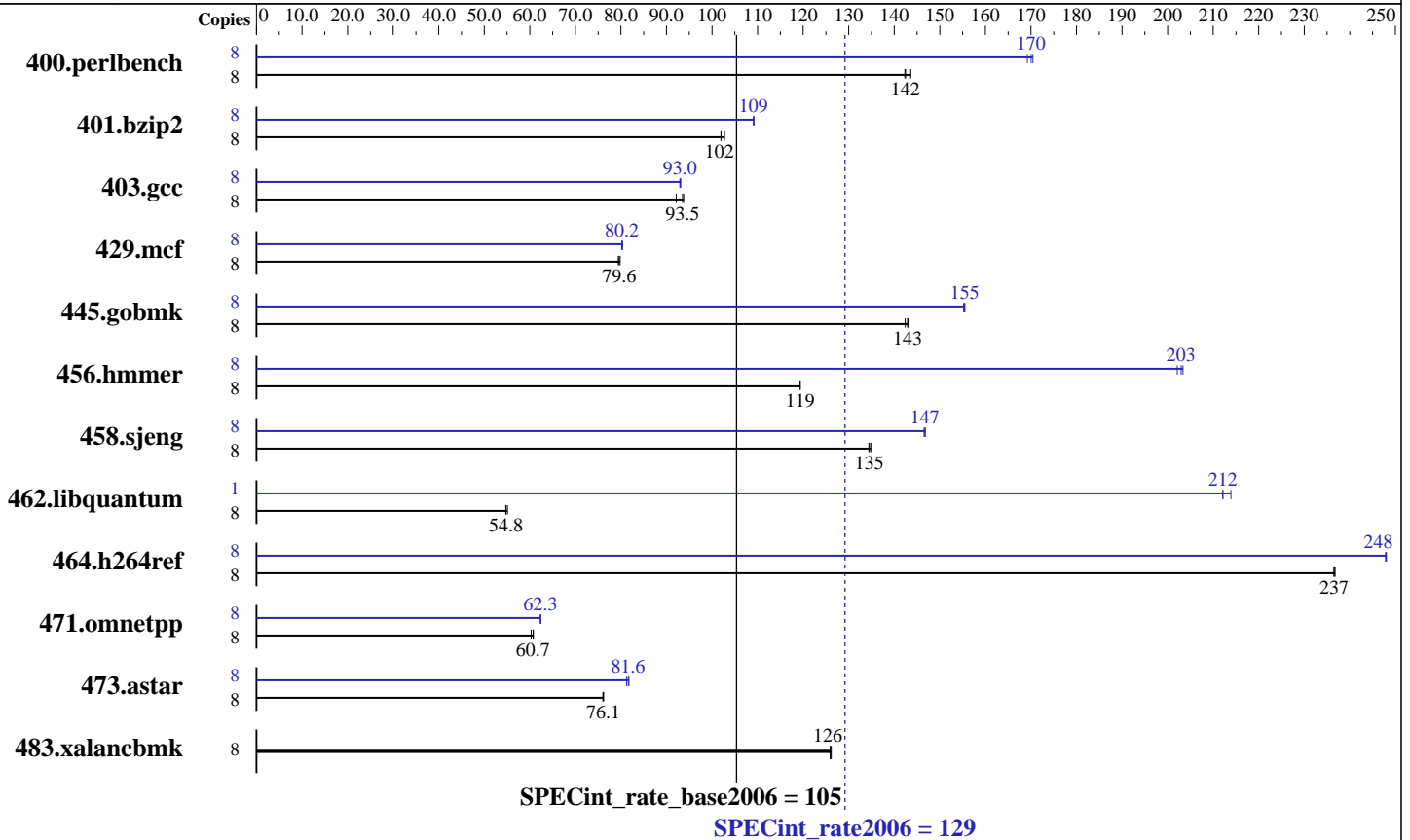
Test date: Mar-2008

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Jan-2008



### Hardware

CPU Name: Intel Xeon E5440  
 CPU Characteristics:  
 CPU MHz: 2830  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 129

Servidor Itautec MX201 (Intel Xeon E5440)

SPECint\_rate\_base2006 = 105

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Mar-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>549</b>	<b>142</b>	544	144	549	142	8	459	170	462	169	<b>460</b>	<b>170</b>
401.bzip2	8	751	103	<b>757</b>	<b>102</b>	757	102	8	707	109	707	109	<b>707</b>	<b>109</b>
403.gcc	8	687	93.8	<b>689</b>	<b>93.5</b>	699	92.1	8	<b>693</b>	<b>93.0</b>	693	93.0	691	93.1
429.mcf	8	914	79.8	919	79.4	<b>916</b>	<b>79.6</b>	8	910	80.2	908	80.4	<b>909</b>	<b>80.2</b>
445.gobmk	8	<b>587</b>	<b>143</b>	589	142	587	143	8	541	155	540	155	<b>540</b>	<b>155</b>
456.hammer	8	<b>625</b>	<b>119</b>	625	119	626	119	8	367	203	<b>368</b>	<b>203</b>	369	202
458.sjeng	8	718	135	<b>720</b>	<b>135</b>	720	134	8	<b>660</b>	<b>147</b>	661	147	659	147
462.libquantum	8	3009	55.1	3029	54.7	<b>3026</b>	<b>54.8</b>	1	97.7	212	96.9	214	<b>97.7</b>	<b>212</b>
464.h264ref	8	748	237	<b>748</b>	<b>237</b>	749	237	8	714	248	714	248	<b>714</b>	<b>248</b>
471.omnetpp	8	<b>823</b>	<b>60.7</b>	823	60.8	829	60.3	8	801	62.4	802	62.3	<b>802</b>	<b>62.3</b>
473.astar	8	737	76.2	739	76.0	<b>737</b>	<b>76.1</b>	8	<b>688</b>	<b>81.6</b>	691	81.2	687	81.8
483.xalancbmk	8	437	126	<b>438</b>	<b>126</b>	438	126	8	437	126	<b>438</b>	<b>126</b>	438	126

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Enabled

## General Notes

This result was measured on the Servidor Itautec MX201.  
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 129

Servidor Itaotec MX201 (Intel Xeon E5440)

SPECint\_rate\_base2006 = 105

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Mar-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
456.hmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include  
C++ benchmarks:  
icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 129

Servidor Itautec MX201 (Intel Xeon E5440)

SPECint\_rate\_base2006 = 105

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Mar-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
403.gcc: -fast -inline-calloc -opt-malloc-options=3  
429.mcf: -fast -prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
483.xalancbmk: basepeak = yes



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 129

Servidor Itautec MX201 (Intel Xeon E5440)

SPECint\_rate\_base2006 = 105

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Mar-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.  
Report generated on Tue Jul 22 18:24:49 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 15 April 2008.