



SPEC® CINT2006 Result

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Bull SAS

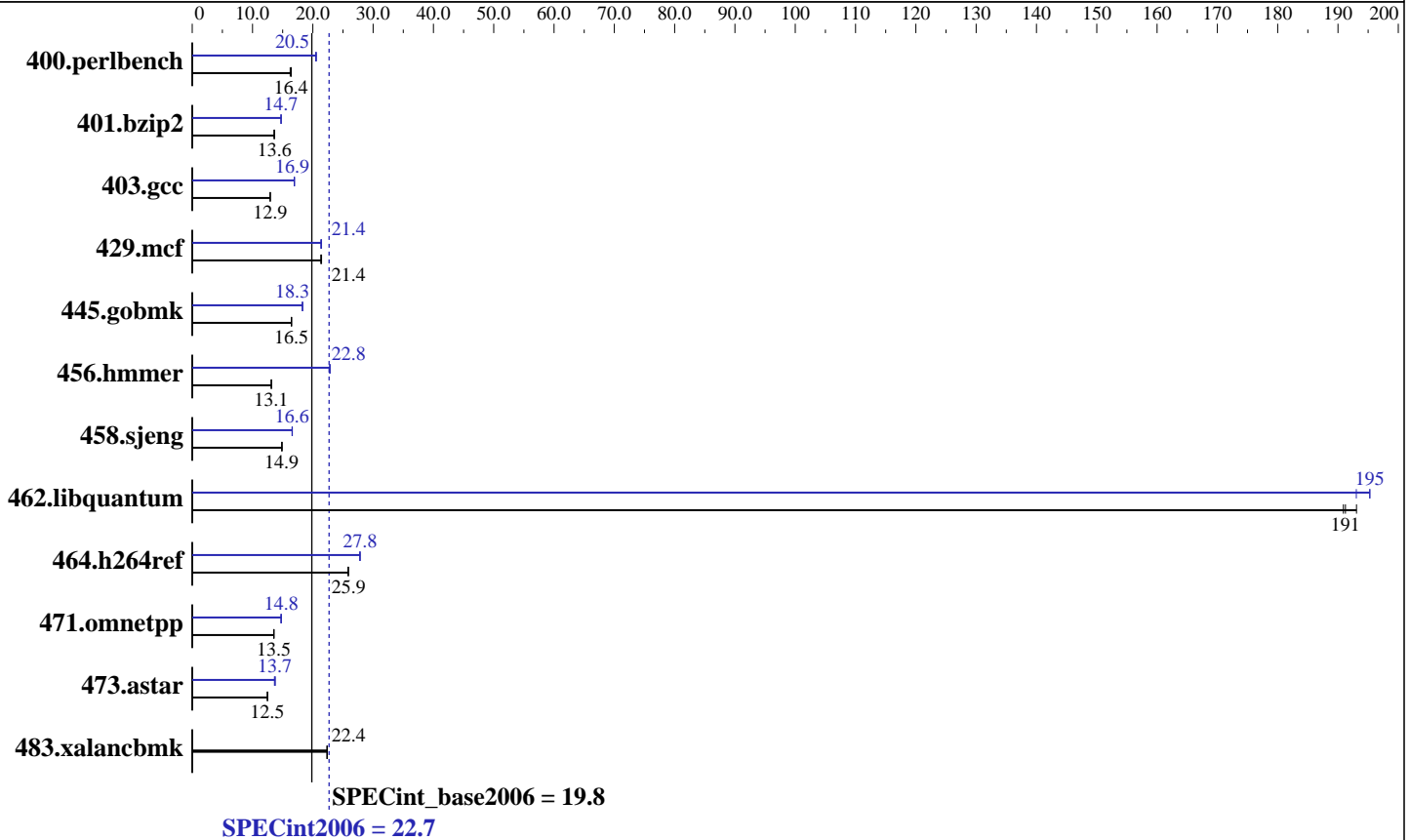
NovaScale T860 E1
(Intel Xeon E5420, 2.50GHz)

SPECint®2006 = 22.7

SPECint_base2006 = 19.8

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: NEC Corporation

Test date: Mar-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5420
 CPU Characteristics: 2.50 GHz, 2x6 MB L2 shared, 1333 MHz bus
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 15000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	595	16.4	602	16.2	<u>595</u>	<u>16.4</u>	477	20.5	<u>476</u>	<u>20.5</u>	475	20.6
401.bzip2	707	13.6	<u>708</u>	<u>13.6</u>	712	13.6	654	14.8	<u>655</u>	<u>14.7</u>	656	14.7
403.gcc	623	12.9	<u>622</u>	<u>12.9</u>	622	12.9	<u>476</u>	<u>16.9</u>	474	17.0	476	16.9
429.mcf	<u>427</u>	<u>21.4</u>	425	21.4	427	21.3	425	21.4	<u>426</u>	<u>21.4</u>	428	21.3
445.gobmk	<u>637</u>	<u>16.5</u>	636	16.5	638	16.4	575	18.3	<u>574</u>	<u>18.3</u>	573	18.3
456.hmmmer	711	13.1	712	13.1	<u>711</u>	<u>13.1</u>	<u>409</u>	<u>22.8</u>	409	22.8	409	22.8
458.sjeng	811	14.9	<u>815</u>	<u>14.9</u>	815	14.8	731	16.6	<u>730</u>	<u>16.6</u>	729	16.6
462.libquantum	107	193	<u>108</u>	<u>191</u>	109	191	107	193	106	195	<u>106</u>	<u>195</u>
464.h264ref	<u>855</u>	<u>25.9</u>	856	25.8	854	25.9	795	27.8	<u>795</u>	<u>27.8</u>	797	27.8
471.omnetpp	461	13.6	<u>462</u>	<u>13.5</u>	463	13.5	<u>423</u>	<u>14.8</u>	423	14.8	425	14.7
473.astar	<u>563</u>	<u>12.5</u>	564	12.5	562	12.5	<u>512</u>	<u>13.7</u>	515	13.6	510	13.8
483.xalancbmk	308	22.4	<u>308</u>	<u>22.4</u>	309	22.3	308	22.4	<u>308</u>	<u>22.4</u>	309	22.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Intel SpeedStep Technology: Disabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode

The NEC Express5800/120Lj(Intel Xeon E5420) and the Bull NovaScale T860 E1(Intel Xeon E5420,2.50GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/120Lj(Intel Xeon E5420) model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 18:13:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 April 2008.