



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

CELSIUS M460, Intel Core 2 Quad Q9550

SPECfp®_rate2006 = 45.8

CELSIUS M460, Intel Core 2 Quad Q9550

SPECfp_rate_base2006 = 43.3

CPU2006 license: 22

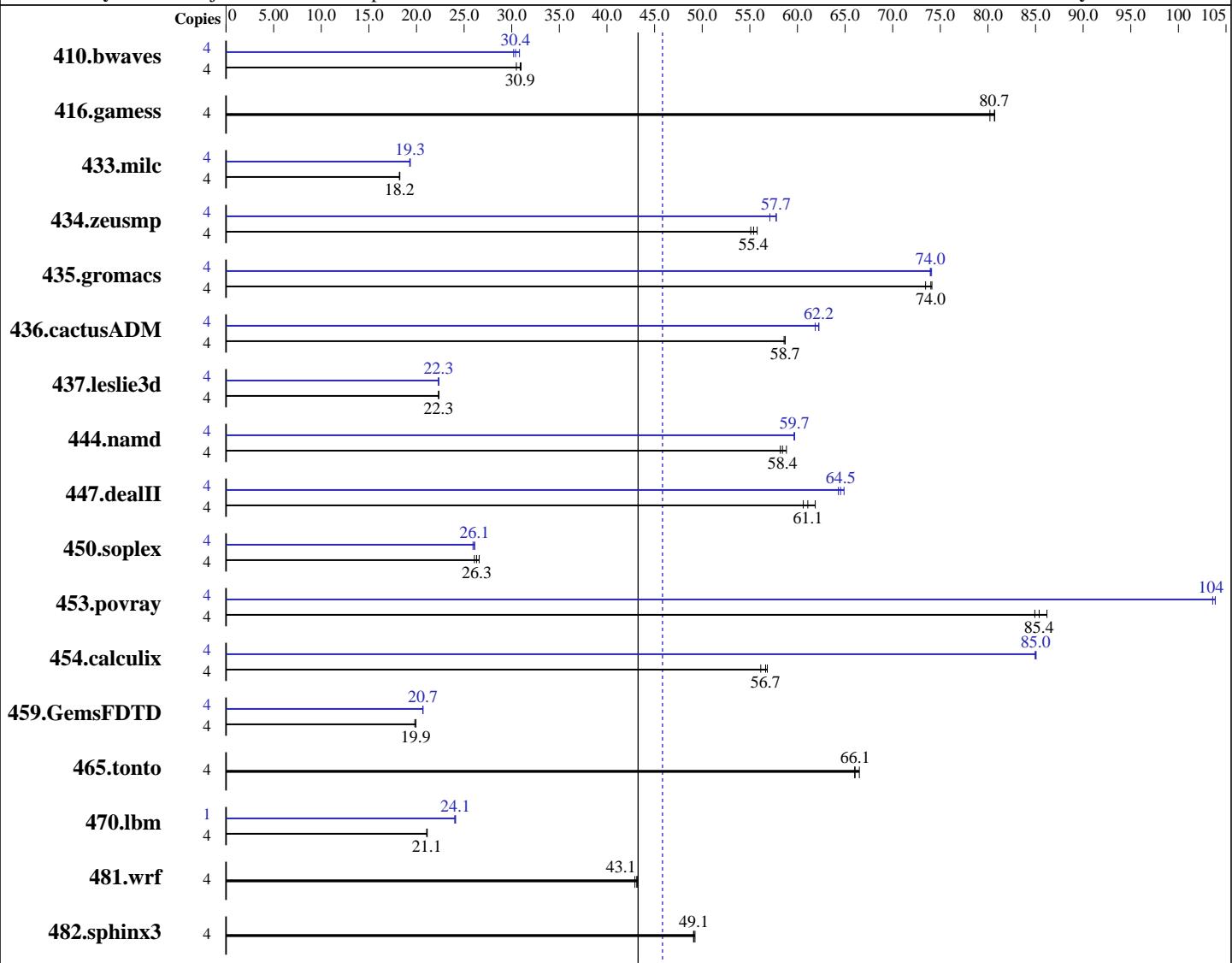
Test date: Mar-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Mar-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



CPU Name: Intel Core 2 Quad Q9550
CPU Characteristics:
CPU MHz: 2833
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Hardware

Operating System: Windows Vista Ultimate, 64 bit Version

Software

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L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
 Disk Subsystem: 1 x 400 GB SATA 7200 RPM
 Other Hardware: None

Compiler: Intel C++ Compiler
 for applications running on Intel 64,
 Version 10.1, Build 20070913
 Intel Visual Fortran Compiler
 for applications running on Intel 64,
 Version 10.1, Build 20070913
 Microsoft Visual Studio 2005 with SP1
 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other Software: SmartHeap Library Version 8.0 (64 bit) from
<http://www.microquill.com/>

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 4 | 1760 | 30.9 | 1755 | 31.0 | 1783 | 30.5 | 4 | 1764 | 30.8 | 1788 | 30.4 | 1800 | 30.2 |
| 416.gamess | 4 | 970 | 80.7 | 971 | 80.7 | 976 | 80.2 | 4 | 970 | 80.7 | 971 | 80.7 | 976 | 80.2 |
| 433.milc | 4 | 2012 | 18.3 | 2014 | 18.2 | 2017 | 18.2 | 4 | 1904 | 19.3 | 1899 | 19.3 | 1900 | 19.3 |
| 434.zeusmp | 4 | 657 | 55.4 | 661 | 55.1 | 653 | 55.8 | 4 | 637 | 57.1 | 630 | 57.7 | 630 | 57.8 |
| 435.gromacs | 4 | 385 | 74.1 | 386 | 74.0 | 389 | 73.5 | 4 | 386 | 74.0 | 386 | 74.0 | 385 | 74.1 |
| 436.cactusADM | 4 | 814 | 58.7 | 814 | 58.7 | 816 | 58.6 | 4 | 773 | 61.9 | 768 | 62.2 | 768 | 62.3 |
| 437.leslie3d | 4 | 1684 | 22.3 | 1684 | 22.3 | 1684 | 22.3 | 4 | 1683 | 22.3 | 1687 | 22.3 | 1685 | 22.3 |
| 444.namd | 4 | 545 | 58.8 | 549 | 58.4 | 551 | 58.2 | 4 | 537 | 59.7 | 537 | 59.7 | 538 | 59.6 |
| 447.dealII | 4 | 749 | 61.1 | 755 | 60.6 | 740 | 61.9 | 4 | 709 | 64.5 | 712 | 64.3 | 705 | 64.9 |
| 450.soplex | 4 | 1279 | 26.1 | 1255 | 26.6 | 1268 | 26.3 | 4 | 1279 | 26.1 | 1287 | 25.9 | 1278 | 26.1 |
| 453.povray | 4 | 247 | 86.2 | 251 | 84.9 | 249 | 85.4 | 4 | 205 | 104 | 205 | 104 | 205 | 104 |
| 454.calculix | 4 | 582 | 56.7 | 588 | 56.1 | 581 | 56.8 | 4 | 388 | 85.1 | 388 | 85.0 | 388 | 85.0 |
| 459.GemsFDTD | 4 | 2128 | 19.9 | 2141 | 19.8 | 2130 | 19.9 | 4 | 2054 | 20.7 | 2054 | 20.7 | 2051 | 20.7 |
| 465.tonto | 4 | 596 | 66.1 | 596 | 66.0 | 592 | 66.5 | 4 | 596 | 66.1 | 596 | 66.0 | 592 | 66.5 |
| 470.lbm | 4 | 2601 | 21.1 | 2608 | 21.1 | 2606 | 21.1 | 1 | 572 | 24.0 | 570 | 24.1 | 570 | 24.1 |
| 481.wrf | 4 | 1041 | 42.9 | 1036 | 43.1 | 1035 | 43.2 | 4 | 1041 | 42.9 | 1036 | 43.1 | 1035 | 43.2 |
| 482.sphinx3 | 4 | 1583 | 49.2 | 1587 | 49.1 | 1588 | 49.1 | 4 | 1583 | 49.2 | 1587 | 49.1 | 1588 | 49.1 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used.



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General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com/>

Base Compiler Invocation

C benchmarks:

`icl -Qvc8 -Qc99`

C++ benchmarks:

`icl -Qvc8`

Fortran benchmarks:

`ifort`

Benchmarks using both Fortran and C:

`icl -Qvc8 -Qc99 ifort`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_P64`
416.gamess: `-DSPEC_CPU_P64`
 433.milc: `-DSPEC_CPU_P64`
434.zeusmp: `-DSPEC_CPU_P64`
435.gromacs: `-DSPEC_CPU_P64`
436.cactusADM: `-DSPEC_CPU_P64` `-Qlowercase -assume:underscore`
 437.leslie3d: `-DSPEC_CPU_P64`
 444.namd: `-DSPEC_CPU_P64` `-TP`
 447.dealII: `-DSPEC_CPU_P64` `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG`
 450.soplex: `-DSPEC_CPU_P64`
 453.povray: `-DSPEC_CPU_P64` `-DSPEC_CPU_WINDOWS_ICL`
 454.calculix: `-DSPEC_CPU_P64` `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
459.GemsFDTD: `-DSPEC_CPU_P64`
 465.tonto: `-DSPEC_CPU_P64`
 470.lbm: `-DSPEC_CPU_P64`
 481.wrf: `-DSPEC_CPU_P64` `-DSPEC_CPU_WINDOWS_ICL`
482.sphinx3: `-DSPEC_CPU_P64`

Base Optimization Flags

C benchmarks:

`-fast -F9500000000 shlw64M.lib`

C++ benchmarks:

`-fast -Qcxx-features -F9500000000 shlw64M.lib`

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Base Optimization Flags (Continued)

Fortran benchmarks:

-fast -F950000000

Benchmarks using both Fortran and C:

-fast -F950000000

Peak Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc8 -Qc99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

470.lbm: -fast -Qunroll12 -Qscalar-rep- -Qprefetch -F950000000

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -fast -Oa -F950000000 shlw64M.lib

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F950000000

450.soplex: Same as 447.dealII

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Peak Optimization Flags (Continued)

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

416.gamess: basepeak = yes

434.zeusmp: -QxT -O2 -Qprec-div- -Qunroll10 -Qscalar-rep- -F950000000

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: -fast -Qunroll12 -Ob0 -Qprefetch -F950000000

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

436.cactusADM: -fast -Qunroll12 -Qprefetch -F950000000

454.calculix: -fast -Qunroll-aggressive -F950000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.xml

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For other inquiries, please contact webmaster@spec.org.

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