



# SPEC® CINT2006 Result

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## NEC Corporation

Express5800/i110Rc-1h  
(Intel Core 2 Duo T7400)

SPECint®\_rate2006 = 25.2

SPECint\_rate\_base2006 = 22.0

CPU2006 license: 9006

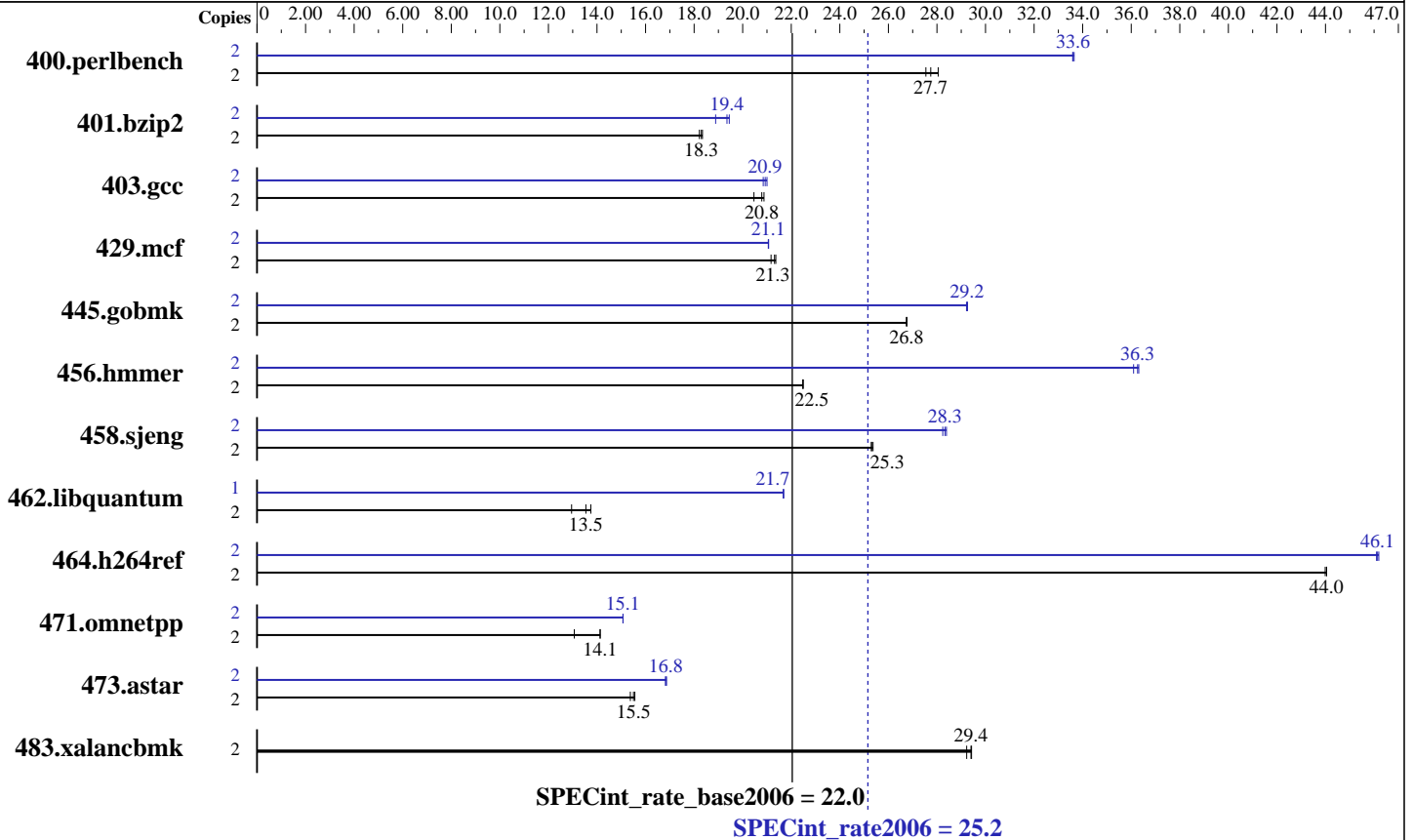
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Mar-2008

Hardware Availability: Oct-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Core 2 Duo T7400  
 CPU Characteristics: 2.16 GHz, 4 MB L2, 667 MHz bus  
 CPU MHz: 2167  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 4 GB (2x2 GB PC2-3200R, 2 rank, CL3-3-3, ECC)  
 Disk Subsystem: 1x73.2 GB SAS, 10000RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: L\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	697	28.1	<b>704</b>	<b>27.7</b>	709	27.5	2	581	33.6	582	33.6	<b>581</b>	<b>33.6</b>
401.bzip2	2	1059	18.2	<b>1055</b>	<b>18.3</b>	1052	18.3	2	1022	18.9	<b>997</b>	<b>19.4</b>	992	19.4
403.gcc	2	787	20.5	<b>775</b>	<b>20.8</b>	771	20.9	2	<b>769</b>	<b>20.9</b>	767	21.0	772	20.8
429.mcf	2	862	21.2	854	21.4	<b>856</b>	<b>21.3</b>	2	866	21.1	866	21.1	<b>866</b>	<b>21.1</b>
445.gobmk	2	784	26.7	784	26.8	<b>784</b>	<b>26.8</b>	2	718	29.2	<b>717</b>	<b>29.2</b>	717	29.3
456.hmmer	2	830	22.5	<b>830</b>	<b>22.5</b>	830	22.5	2	<b>515</b>	<b>36.3</b>	517	36.1	514	36.3
458.sjeng	2	<b>956</b>	<b>25.3</b>	954	25.4	957	25.3	2	<b>854</b>	<b>28.3</b>	852	28.4	857	28.2
462.libquantum	2	3199	13.0	<b>3060</b>	<b>13.5</b>	3015	13.7	1	955	21.7	956	21.7	<b>956</b>	<b>21.7</b>
464.h264ref	2	1006	44.0	<b>1005</b>	<b>44.0</b>	1005	44.0	2	<b>959</b>	<b>46.1</b>	960	46.1	958	46.2
471.omnetpp	2	957	13.1	<b>885</b>	<b>14.1</b>	884	14.1	2	829	15.1	<b>829</b>	<b>15.1</b>	830	15.1
473.astar	2	913	15.4	902	15.6	<b>906</b>	<b>15.5</b>	2	<b>834</b>	<b>16.8</b>	835	16.8	832	16.9
483.xalanbmk	2	472	29.2	<b>469</b>	<b>29.4</b>	469	29.4	2	472	29.2	<b>469</b>	<b>29.4</b>	469	29.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2,  
456.hmmer, for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap_8.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

`401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include`

`456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include`

C++ benchmarks:

`icpc`

## Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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