



# SPEC® CINT2006 Result

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**Itautec**

**SPECint\_rate2006 = Not Run**

**Servidor Itautec MX201 (Intel Xeon X5460)**

**SPECint\_rate\_base2006 = 65.2**

**CPU2006 license:** 9001

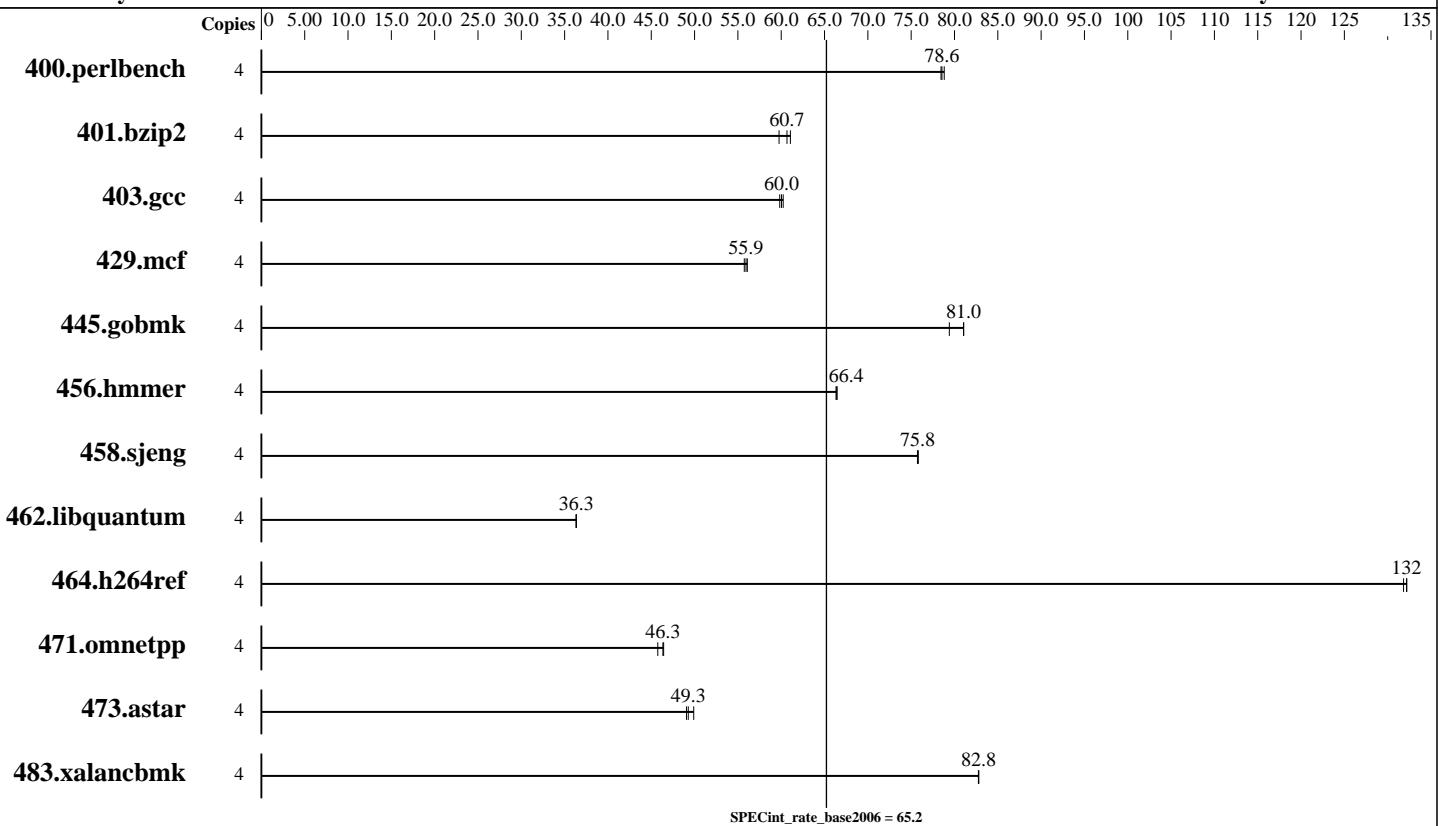
**Test date:** Mar-2008

**Test sponsor:** Itautec

**Hardware Availability:** Dec-2007

**Tested by:** Itautec

**Software Availability:** Jan-2008



## Hardware

CPU Name:	Intel Xeon X5460
CPU Characteristics:	
CPU MHz:	3160
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	1 x SCSI, 73GB, 15000 RPM
Other Hardware:	None

## Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Binutils 2.17.10.50 MicroQuill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	496	78.8	<b>498</b>	<b>78.6</b>	498	78.4							
401.bzip2	4	632	61.1	<b>636</b>	<b>60.7</b>	646	59.8							
403.gcc	4	<b>536</b>	<b>60.0</b>	535	60.2	538	59.8							
429.mcf	4	650	56.1	654	55.7	<b>652</b>	<b>55.9</b>							
445.gobmk	4	<b>518</b>	<b>81.0</b>	528	79.4	518	81.1							
456.hmmer	4	563	66.3	<b>562</b>	<b>66.4</b>	562	66.4							
458.sjeng	4	<b>639</b>	<b>75.8</b>	639	75.7	638	75.8							
462.libquantum	4	<b>2281</b>	<b>36.3</b>	2281	36.3	2280	36.3							
464.h264ref	4	<b>670</b>	<b>132</b>	670	132	671	132							
471.omnetpp	4	546	45.8	538	46.4	<b>539</b>	<b>46.3</b>							
473.astar	4	572	49.1	563	49.9	<b>570</b>	<b>49.3</b>							
483.xalancbmk	4	333	82.8	334	82.8	<b>333</b>	<b>82.8</b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
 '/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS configuration:  
 Hardware Prefetch Disabled

## General Notes

This result was measured on the Servidor Itautec MX201.  
 The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
 icc

C++ benchmarks:  
 icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.

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