



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 133

ProLiant BL260c G5
(3.0 GHz, Intel Xeon E5450)

SPECint_rate_base2006 = 106

CPU2006 license: 3

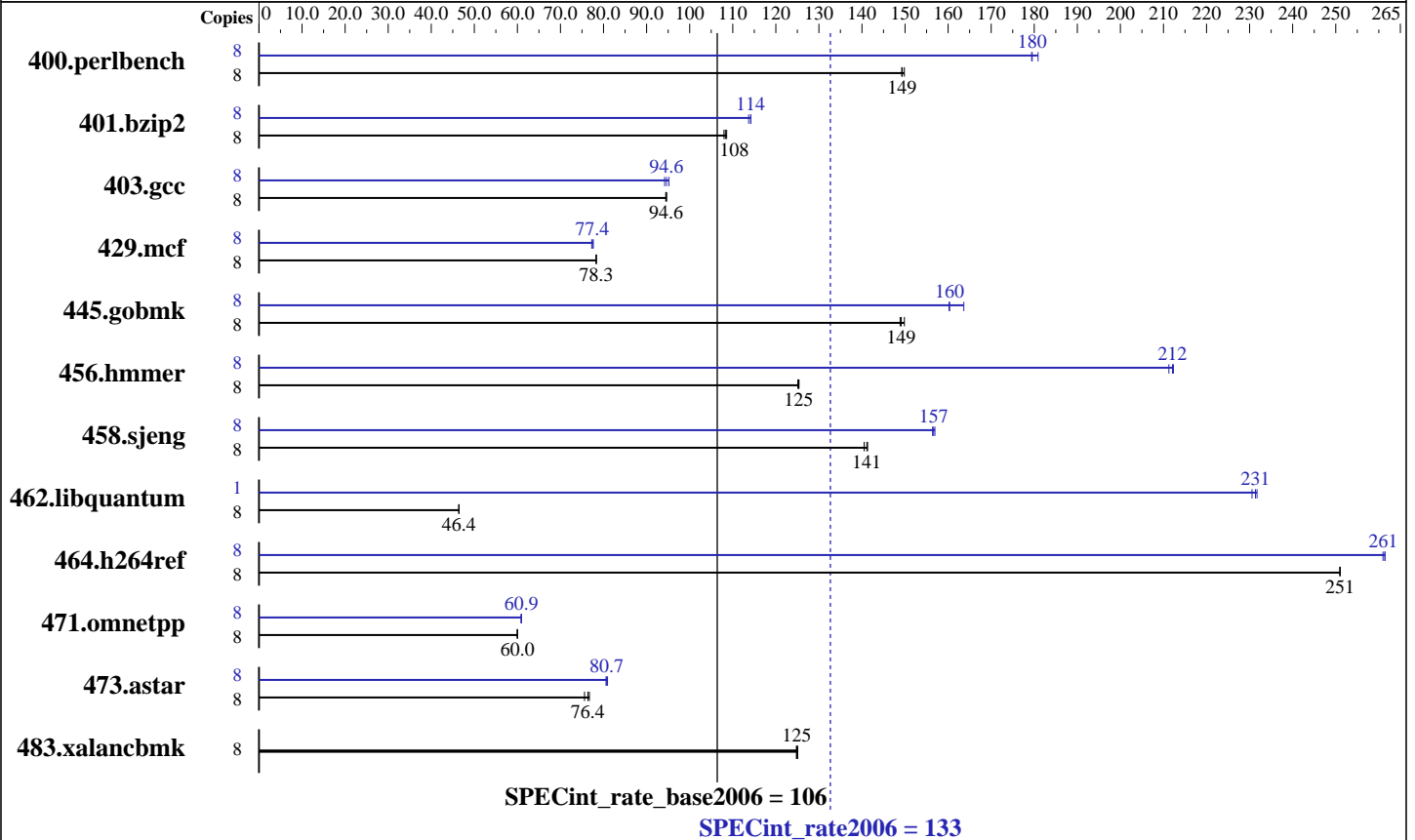
Test date: Mar-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5450
 CPU Characteristics: 3.0 GHz, 2x6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4 GB PC2-5300P CL5)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	522	150	<u>523</u>	<u>149</u>	524	149	8	436	179	<u>435</u>	<u>180</u>	432	181
401.bzip2	8	715	108	711	109	<u>713</u>	<u>108</u>	8	679	114	676	114	<u>676</u>	<u>114</u>
403.gcc	8	682	94.4	<u>681</u>	<u>94.6</u>	680	94.6	8	677	95.2	684	94.2	<u>681</u>	<u>94.6</u>
429.mcf	8	932	78.3	<u>932</u>	<u>78.3</u>	932	78.3	8	940	77.6	<u>942</u>	<u>77.4</u>	944	77.3
445.gobmk	8	560	150	564	149	<u>563</u>	<u>149</u>	8	513	164	524	160	<u>523</u>	<u>160</u>
456.hammer	8	597	125	595	125	<u>596</u>	<u>125</u>	8	352	212	353	211	<u>352</u>	<u>212</u>
458.sjeng	8	685	141	<u>686</u>	<u>141</u>	689	141	8	619	156	<u>618</u>	<u>157</u>	617	157
462.libquantum	8	3570	46.4	<u>3571</u>	<u>46.4</u>	3572	46.4	1	89.4	232	<u>89.6</u>	<u>231</u>	89.9	231
464.h264ref	8	705	251	706	251	<u>705</u>	<u>251</u>	8	678	261	<u>677</u>	<u>261</u>	677	261
471.omnetpp	8	834	60.0	<u>833</u>	<u>60.0</u>	833	60.0	8	<u>820</u>	<u>60.9</u>	822	60.8	820	61.0
473.astar	8	<u>735</u>	<u>76.4</u>	732	76.7	743	75.6	8	697	80.6	694	80.9	<u>696</u>	<u>80.7</u>
483.xalancbmk	8	441	125	442	125	<u>442</u>	<u>125</u>	8	441	125	442	125	<u>442</u>	<u>125</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
```

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

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