



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = Not Run

PRIMERGY TX300 S4, Intel Xeon E5440, 2.83 GHz

SPECint_rate_base2006 = 105

CPU2006 license: 22

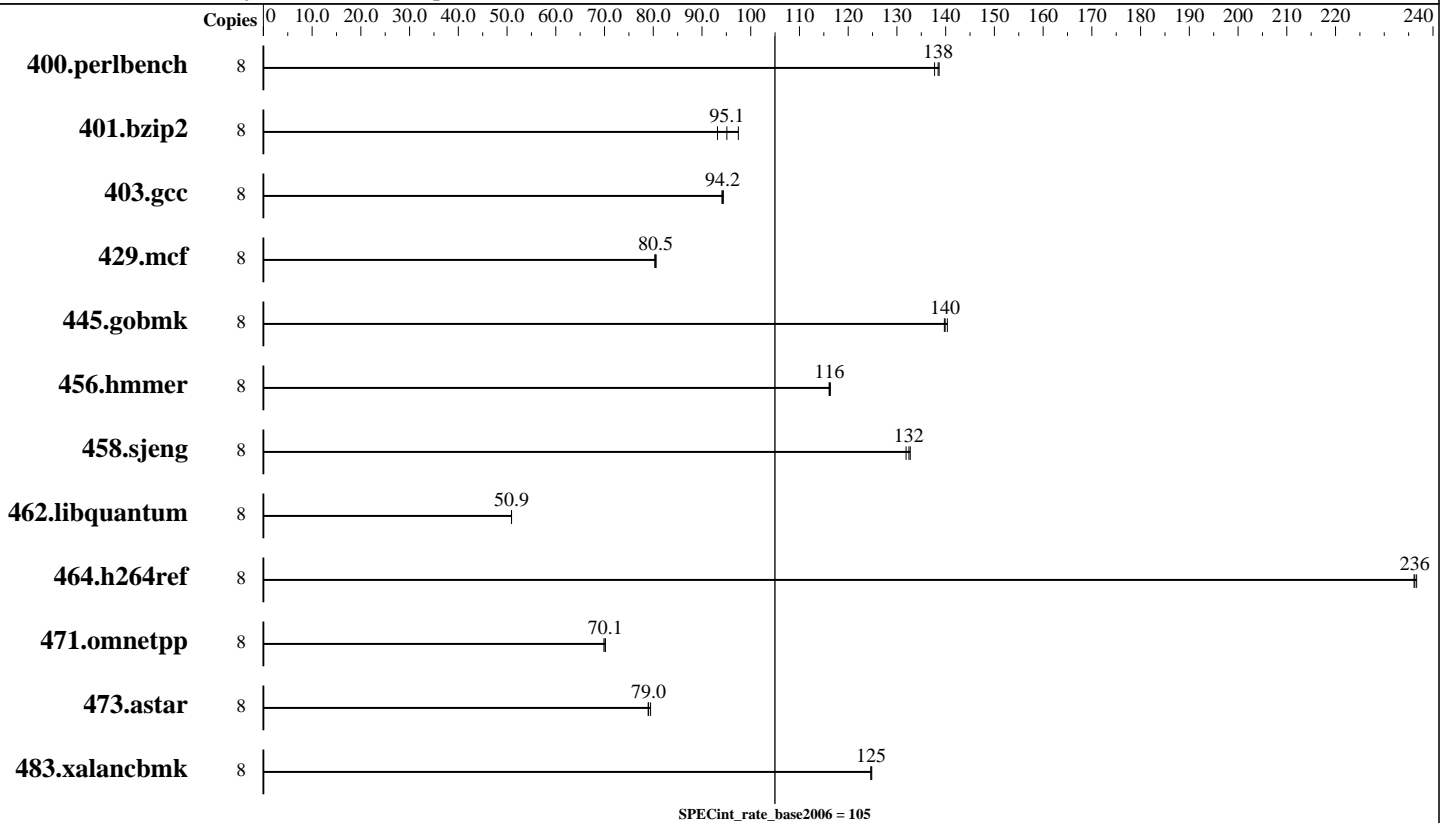
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Mar-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5440
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
 Disk Subsystem: 1x SAS, 73 GB, 15000 rpm
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
 Auto Parallel: No
 File System: ext2
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	565	138	564	139	568	138							
401.bzip2	8	828	93.2	812	95.1	792	97.5							
403.gcc	8	684	94.1	682	94.4	683	94.2							
429.mcf	8	907	80.5	905	80.6	909	80.3							
445.gobmk	8	598	140	601	140	600	140							
456.hammer	8	642	116	643	116	642	116							
458.sjeng	8	734	132	731	132	729	133							
462.libquantum	8	3255	50.9	3255	50.9	3256	50.9							
464.h264ref	8	749	236	750	236	748	237							
471.omnetpp	8	716	69.9	713	70.1	713	70.2							
473.astar	8	707	79.5	711	79.0	710	79.0							
483.xalancbmk	8	443	125	443	125	442	125							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmarheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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