SPEC® CINT2006 Result

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Intel Corporation

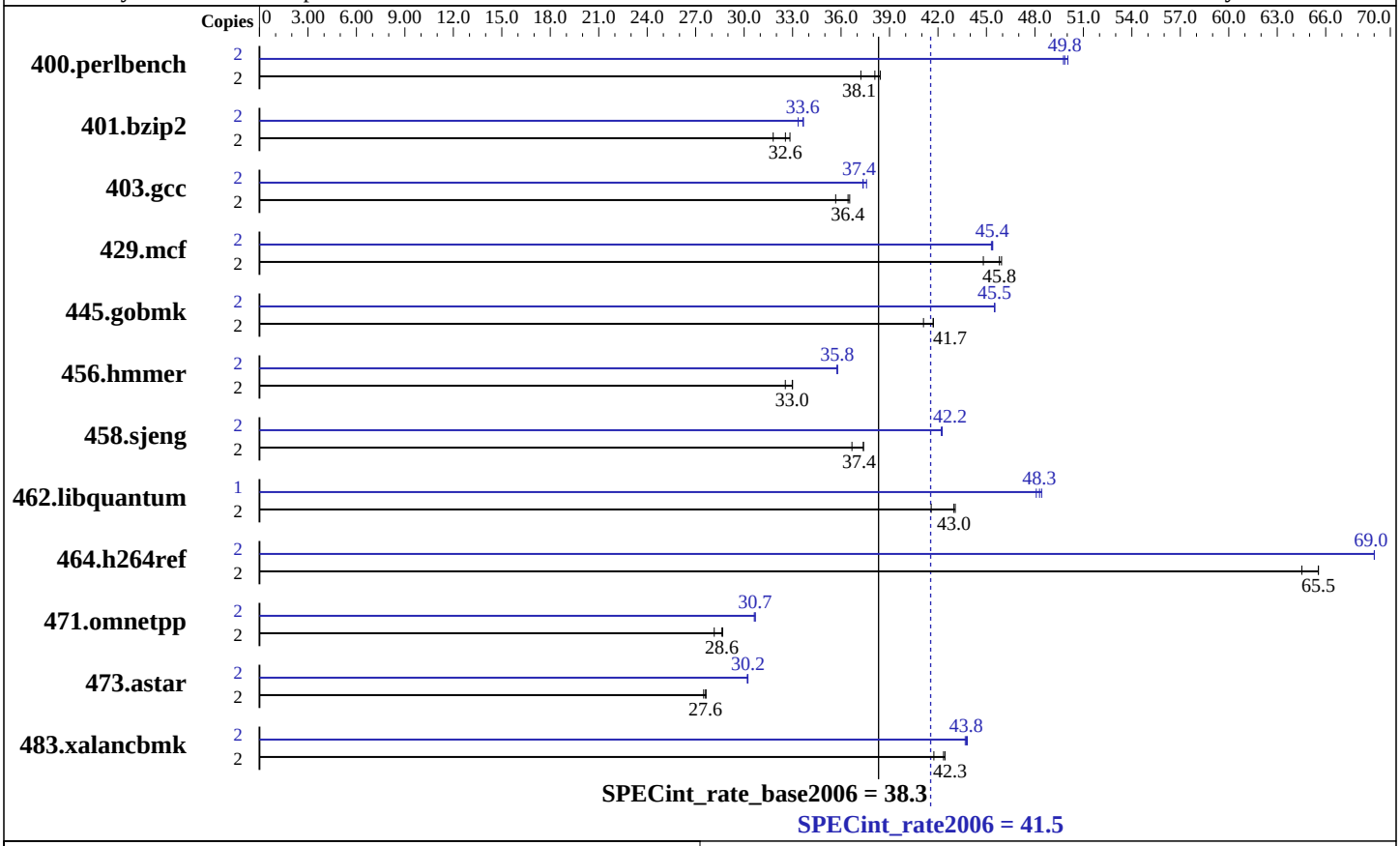
Intel Desktop Board DQ35JO (Intel Core 2 Duo E8500)

SPECint®_rate2006 = 41.5

SPECint_rate_base2006 = 38.3

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Feb-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo E8500
CPU Characteristics:	3.16 GHz, 1333 FSB
CPU MHz:	3167
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Micron DDR2-800 CL5)
Disk Subsystem:	Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	508	38.4	525	37.2	**513**	**38.1**	2	393	49.8	**392**	**49.8**	390	50.0
401.bzip2	2	**593**	**32.6**	607	31.8	588	32.8	2	573	33.7	**574**	**33.6**	579	33.3
403.gcc	2	441	36.5	451	35.7	**442**	**36.4**	2	**431**	**37.4**	431	37.4	428	37.6
429.mcf	2	**398**	**45.8**	407	44.8	397	45.9	2	403	45.3	**402**	**45.4**	402	45.4
445.gobmk	2	**503**	**41.7**	510	41.1	503	41.7	2	**461**	**45.5**	461	45.5	461	45.5
456.hmmer	2	565	33.0	573	32.6	**566**	**33.0**	2	**522**	**35.8**	522	35.8	522	35.8
458.sjeng	2	**647**	**37.4**	660	36.7	647	37.4	2	573	42.3	573	42.2	**573**	**42.2**
462.libquantum	2	997	41.6	962	43.1	**964**	**43.0**	1	**429**	**48.3**	431	48.1	428	48.4
464.h264ref	2	686	64.5	675	65.5	**675**	**65.5**	2	641	69.0	641	69.0	**641**	**69.0**
471.omnetpp	2	444	28.1	**437**	**28.6**	436	28.7	2	408	30.6	**407**	**30.7**	407	30.7
473.astar	2	511	27.5	**508**	**27.6**	508	27.6	2	464	30.2	**464**	**30.2**	465	30.2
483.xalancbmk	2	331	41.7	325	42.4	**326**	**42.3**	2	316	43.7	315	43.8	**315**	**43.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 03/2008:
http://www.intel.com/products/motherboard/DQ35JO/index.htm
The system bus runs at 1333 MHz
System was configured with Asus EN8800GTX discrete graphics card
Binaries were built on Windows Vista Ultimate (32-bit)
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
OMP_NUM_THREADS was set to number of logical processors as seen by the OS
KMP_AFFINITY was set to physical,0
submit was disabled for 462.libquantum at peak.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
`-fast -Qvec-guard-write /F512000000`

C++ benchmarks:
`-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc8 -Qc99`

C++ benchmarks:
`icl -Qvc8`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000

429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:28:18 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 March 2008.