



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

## SPECint®\_rate2006 = Not Run

ProLiant BL460c  
(3.00 GHz, Intel Xeon E5450)

## SPECint\_rate\_base2006 = 110

CPU2006 license: 3

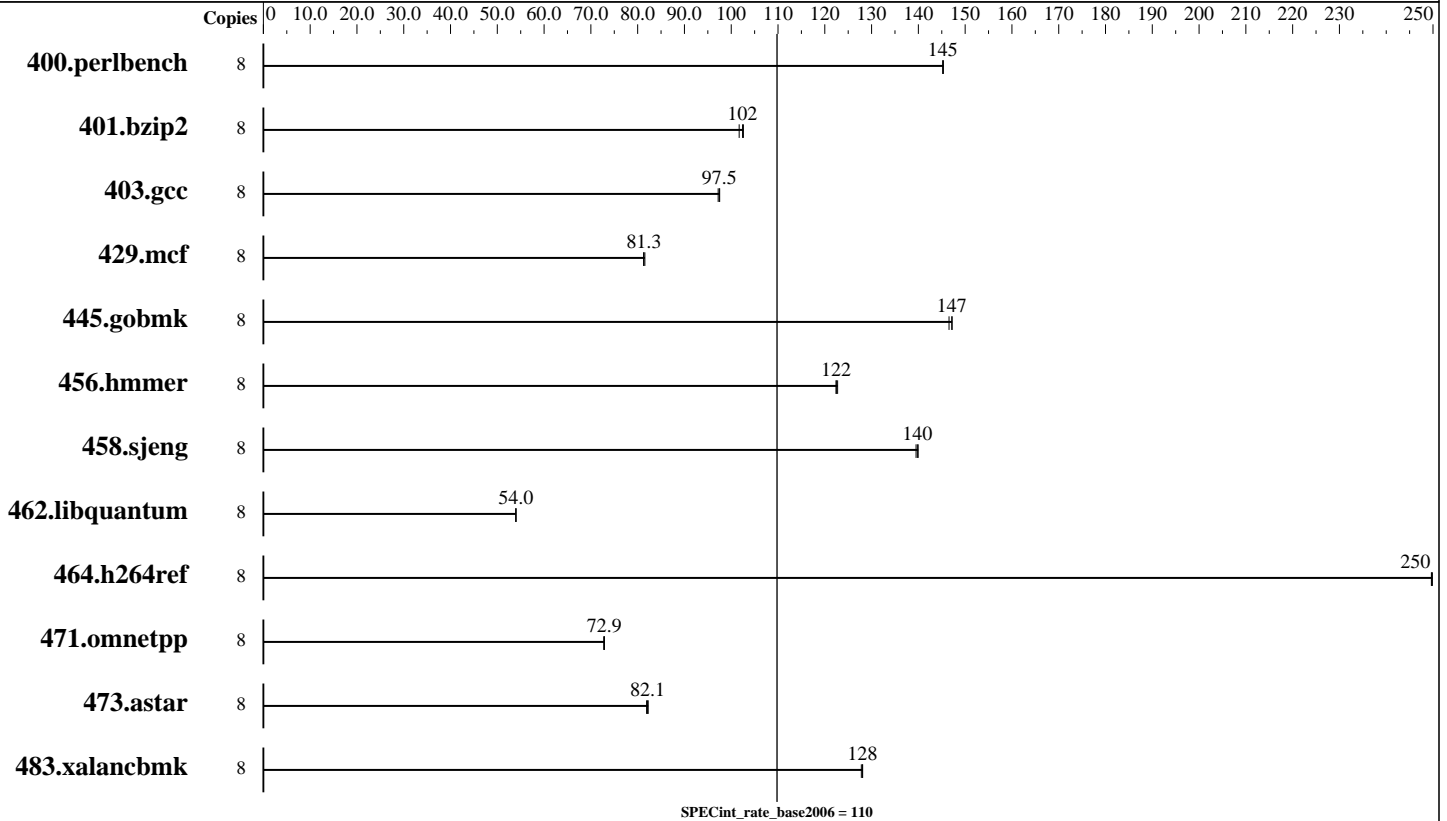
Test date: Feb-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5450  
 CPU Characteristics: 3.00 GHz, 2x6 MB L2 shared, 1333 MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F CL5)  
 Disk Subsystem: 1x72 GB 15 K SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: No  
 File System: ext2  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	538	145	<b><u>538</u></b>	<b><u>145</u></b>	538	145							
401.bzip2	8	<b><u>754</u></b>	<b><u>102</u></b>	752	103	759	102							
403.gcc	8	<b><u>661</u></b>	<b><u>97.5</u></b>	660	97.5	663	97.2							
429.mcf	8	<b><u>897</u></b>	<b><u>81.3</u></b>	895	81.5	898	81.3							
445.gobmk	8	570	147	<b><u>570</u></b>	<b><u>147</u></b>	573	147							
456.hammer	8	610	122	608	123	<b><u>609</u></b>	<b><u>122</u></b>							
458.sjeng	8	692	140	694	140	<b><u>692</u></b>	<b><u>140</u></b>							
462.libquantum	8	3070	54.0	<b><u>3071</u></b>	<b><u>54.0</u></b>	3072	54.0							
464.h264ref	8	709	250	<b><u>709</u></b>	<b><u>250</u></b>	709	250							
471.omnetpp	8	687	72.8	686	72.9	<b><u>686</u></b>	<b><u>72.9</u></b>							
473.astar	8	<b><u>684</u></b>	<b><u>82.1</u></b>	686	81.9	683	82.2							
483.xalanbmk	8	431	128	432	128	<b><u>431</u></b>	<b><u>128</u></b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch Disabled  
Hardware Prefetcher Disabled

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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