



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®_rate2006 = 62.0

PowerEdge M600 (Intel Xeon E5410, 2.33 GHz)

SPECfp_rate_base2006 = 56.3

CPU2006 license: 55

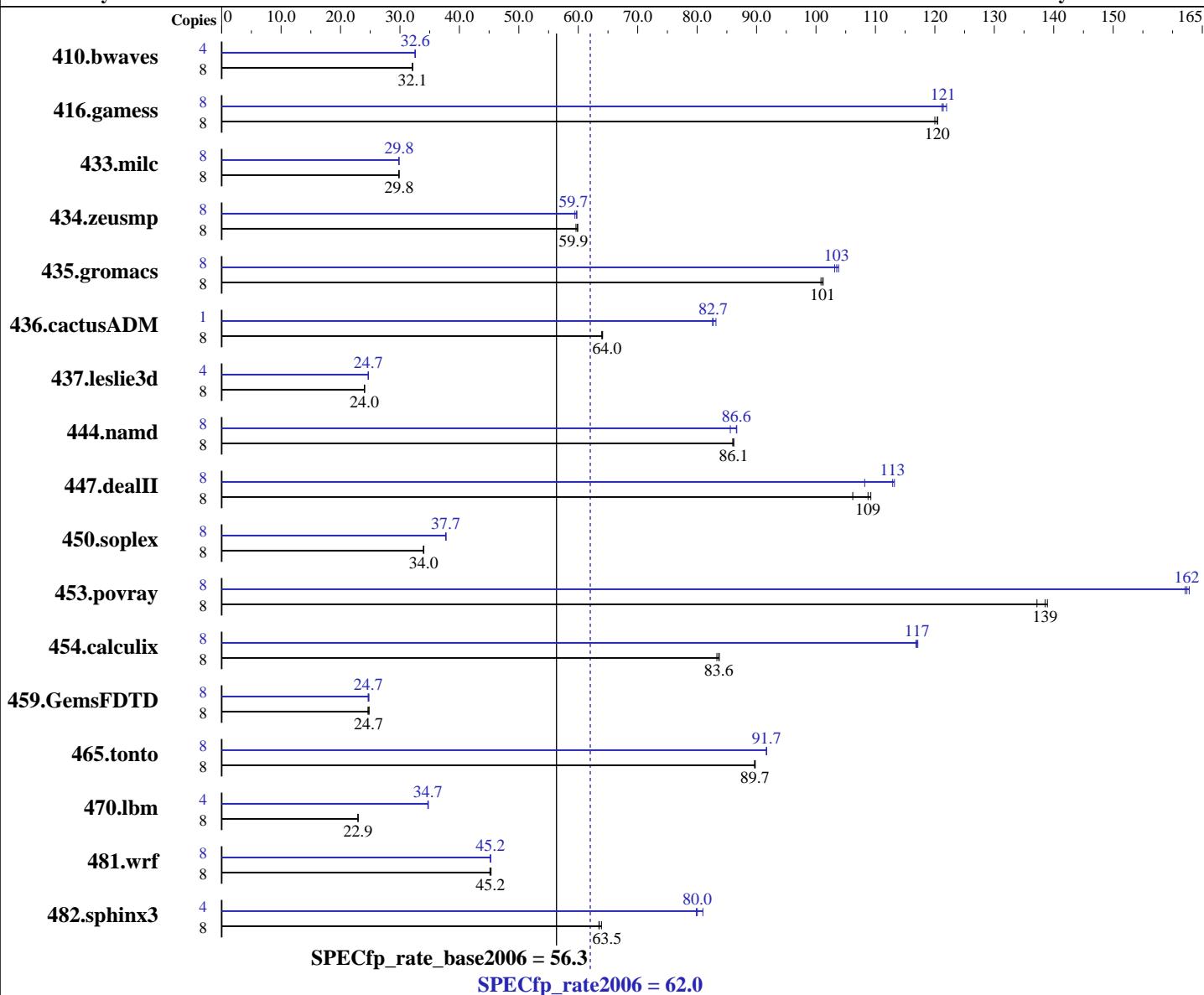
Test date: Feb-2008

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5410
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2333
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: 1_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4 GB 667 MHz ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 80 GB 7.2k SATA
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3387	32.1	<u>3385</u>	<u>32.1</u>	3385	32.1	4	1672	32.5	1667	32.6	<u>1668</u>	<u>32.6</u>
416.gamess	8	1300	120	1305	120	<u>1301</u>	<u>120</u>	8	1292	121	<u>1290</u>	<u>121</u>	1284	122
433.milc	8	2464	29.8	<u>2461</u>	<u>29.8</u>	2459	29.9	8	2461	29.8	<u>2462</u>	<u>29.8</u>	2463	29.8
434.zeusmp	8	1215	59.9	<u>1216</u>	<u>59.9</u>	1221	59.6	8	1218	59.8	<u>1219</u>	<u>59.7</u>	1225	59.4
435.gromacs	8	566	101	<u>565</u>	<u>101</u>	564	101	8	<u>552</u>	<u>103</u>	554	103	550	104
436.cactusADM	8	<u>1493</u>	<u>64.0</u>	1491	64.1	1495	64.0	1	144	83.2	<u>144</u>	<u>82.7</u>	145	82.6
437.leslie3d	8	3124	24.1	<u>3130</u>	<u>24.0</u>	3133	24.0	4	1526	24.6	1522	24.7	<u>1524</u>	<u>24.7</u>
444.namd	8	<u>745</u>	<u>86.1</u>	746	86.0	745	86.1	8	750	85.6	<u>741</u>	<u>86.6</u>	740	86.7
447.dealII	8	838	109	862	106	<u>842</u>	<u>109</u>	8	846	108	808	113	<u>811</u>	<u>113</u>
450.soplex	8	<u>1964</u>	<u>34.0</u>	1961	34.0	1966	33.9	8	1771	37.7	<u>1769</u>	<u>37.7</u>	1767	37.8
453.povray	8	306	139	<u>307</u>	<u>139</u>	310	137	8	263	162	<u>262</u>	<u>162</u>	261	163
454.calculix	8	792	83.3	<u>789</u>	<u>83.6</u>	788	83.7	8	563	117	565	117	<u>564</u>	<u>117</u>
459.GemsFDTD	8	3449	24.6	3422	24.8	<u>3431</u>	<u>24.7</u>	8	3445	24.6	3425	24.8	<u>3437</u>	<u>24.7</u>
465.tonto	8	878	89.6	<u>877</u>	<u>89.7</u>	877	89.8	8	858	91.7	859	91.6	<u>858</u>	<u>91.7</u>
470.lbm	8	4792	22.9	<u>4791</u>	<u>22.9</u>	4790	22.9	4	1580	34.8	<u>1583</u>	<u>34.7</u>	1584	34.7
481.wrf	8	<u>1976</u>	<u>45.2</u>	1971	45.3	1980	45.1	8	1976	45.2	<u>1976</u>	<u>45.2</u>	1974	45.3
482.sphinx3	8	<u>2454</u>	<u>63.5</u>	2456	63.5	2440	63.9	4	976	79.9	963	81.0	<u>974</u>	<u>80.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
 KMP_STACK_SIZE set to 64M
 KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 '/usr/bin/taskset' used to bind processes to CPUs



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Platform Notes

BIOS Settings:

Adjacent Cache Line Prefetch = Disabled (default Enabled)

Hardware Prefetcher = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

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Base Optimization Flags (Continued)

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```

433.milc: icc

C++ benchmarks (except as noted below):
icpc

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks (except as noted below):
ifort

```
437.leslie3d: /opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib  
-I/opt/intel/fc/10.1.008/include
```

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -O0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -O0
-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.03.xml>

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For other inquiries, please contact webmaster@spec.org.

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