



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 58.6

PowerEdge 2950 III (Intel Xeon 5148 LV, 2.33 GHz)

SPECint\_rate\_base2006 = 51.1

CPU2006 license: 55

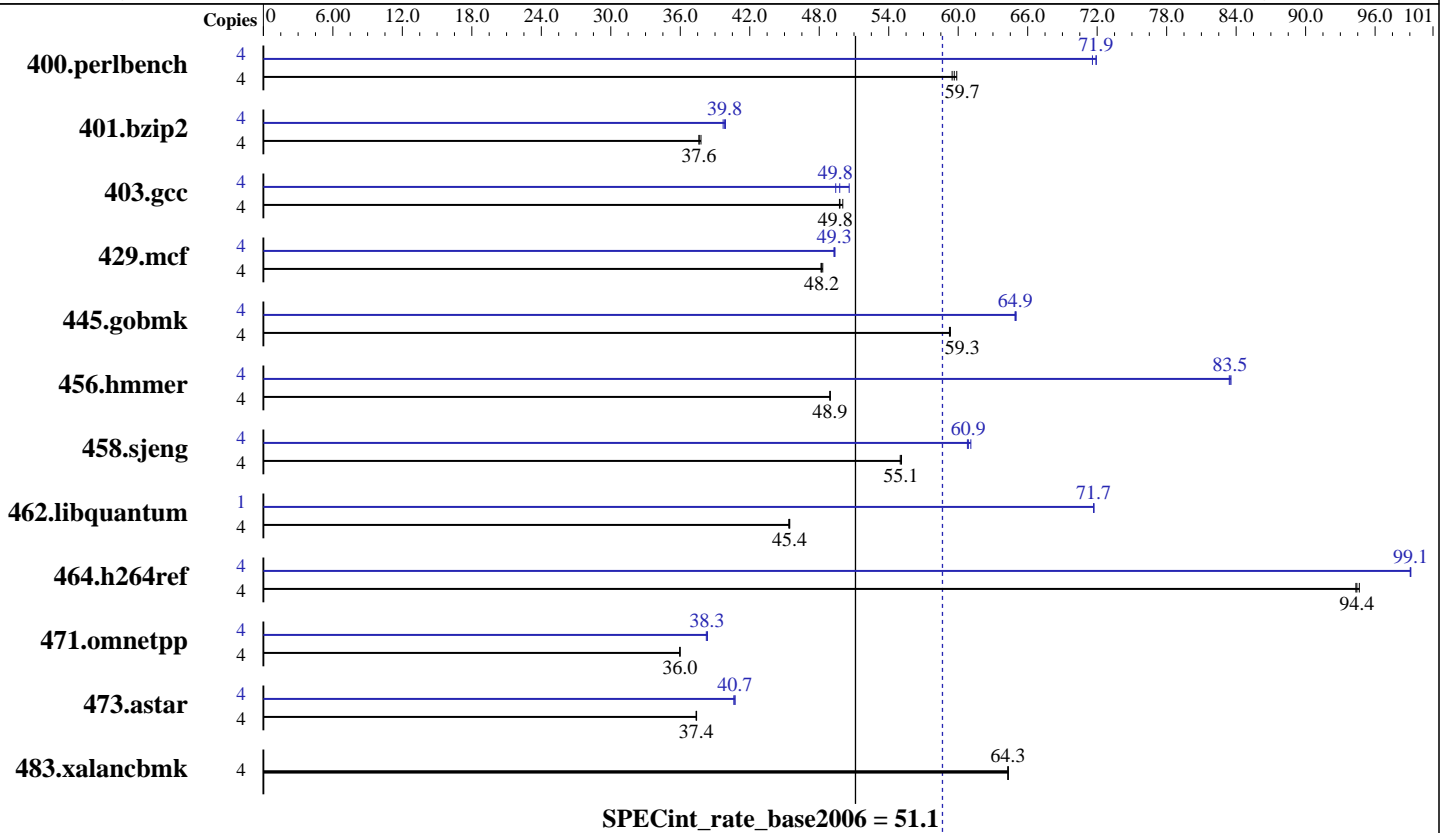
Test date: Feb-2008

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Nov-2007



## Hardware

CPU Name: Intel Xeon 5148 LV  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB 667 MHz ECC CL5 FB-DIMM)  
 Disk Subsystem: 1 x 73 GB SAS 15k RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap library V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>655</b>	<b>59.7</b>	653	59.9	657	59.5	4	546	71.6	<b>544</b>	<b>71.9</b>	543	71.9
401.bzip2	4	1026	37.6	<b>1026</b>	<b>37.6</b>	1022	37.8	4	972	39.7	967	39.9	<b>969</b>	<b>39.8</b>
403.gcc	4	<b>647</b>	<b>49.8</b>	647	49.7	644	50.0	4	<b>647</b>	<b>49.8</b>	637	50.6	651	49.4
429.mcf	4	755	48.3	<b>757</b>	<b>48.2</b>	758	48.2	4	<b>740</b>	<b>49.3</b>	740	49.3	739	49.4
445.gobmk	4	707	59.3	<b>708</b>	<b>59.3</b>	708	59.2	4	<b>646</b>	<b>64.9</b>	645	65.0	646	64.9
456.hammer	4	<b>763</b>	<b>48.9</b>	762	49.0	763	48.9	4	<b>447</b>	<b>83.5</b>	447	83.4	447	83.6
458.sjeng	4	880	55.0	<b>879</b>	<b>55.1</b>	878	55.1	4	792	61.1	<b>795</b>	<b>60.9</b>	796	60.8
462.libquantum	4	1827	45.4	<b>1824</b>	<b>45.4</b>	1823	45.5	1	289	71.7	289	71.7	<b>289</b>	<b>71.7</b>
464.h264ref	4	935	94.6	<b>937</b>	<b>94.4</b>	938	94.3	4	893	99.1	<b>894</b>	<b>99.1</b>	894	99.0
471.omnetpp	4	<b>695</b>	<b>36.0</b>	695	36.0	695	36.0	4	<b>652</b>	<b>38.3</b>	654	38.2	652	38.3
473.astar	4	<b>751</b>	<b>37.4</b>	751	37.4	751	37.4	4	690	40.7	691	40.6	<b>690</b>	<b>40.7</b>
483.xalancbmk	4	429	64.3	429	64.3	<b>429</b>	<b>64.3</b>	4	429	64.3	429	64.3	<b>429</b>	<b>64.3</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## Platform Notes

BIOS Settings:  
Adjacent Cache Line Prefetch = Disabled (default Enabled)

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
  
401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include  
  
456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include  
  
C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.03.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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