SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY TX150 S6, Intel Xeon X3360, 2.83 GHz

$SPECint^{®}\_rate2006 =$ 74.5

$SPECint\_rate\_base2006 =$ 64.0

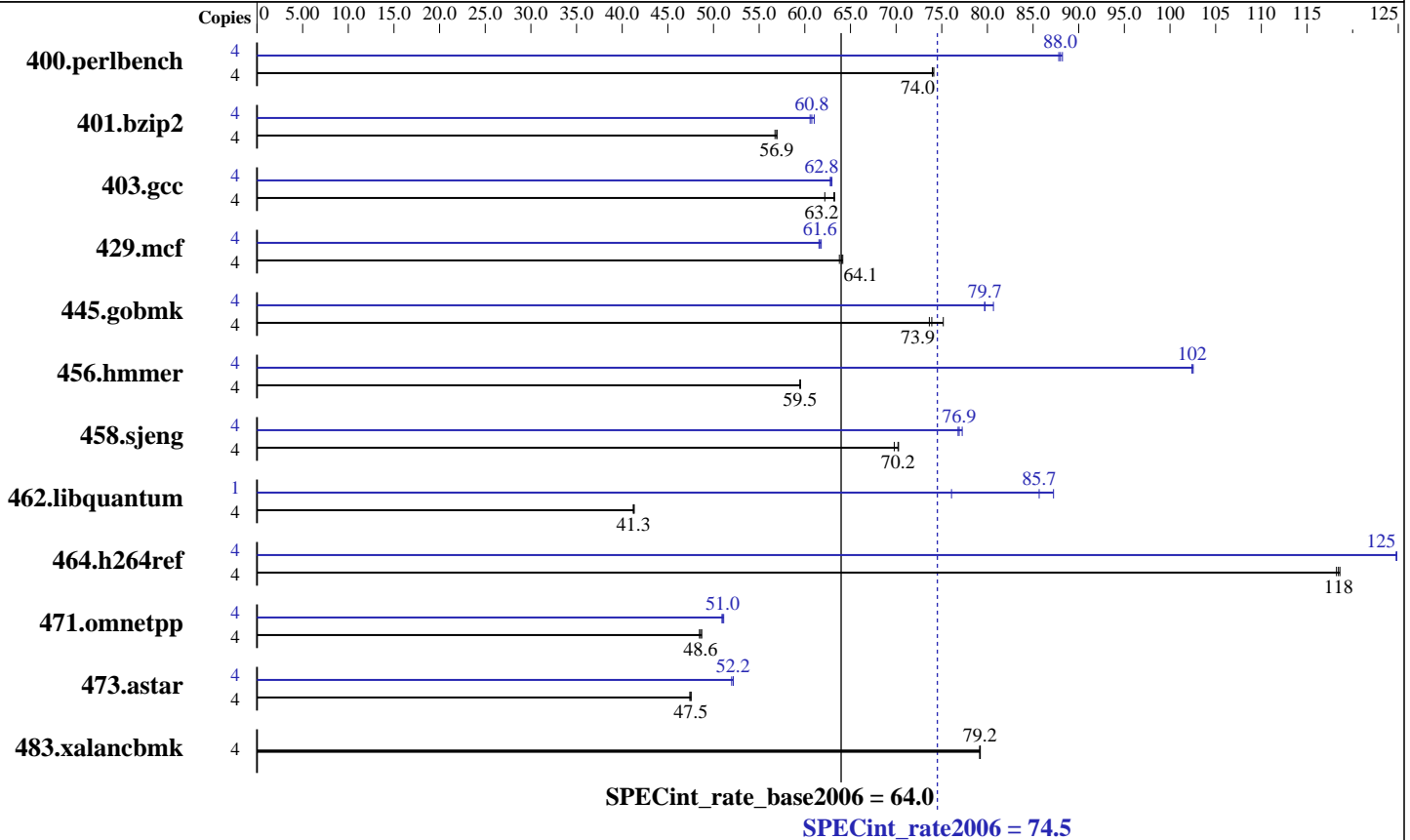**CPU2006 license:** 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Feb-2008
Hardware Availability: Mar-2008
Software Availability: Nov-2007

Benchmark	Copies	Result
400.perlbench	4	88.0
400.perlbench	4	74.0
401.bzip2	4	60.8
401.bzip2	4	56.9
403.gcc	4	62.8
403.gcc	4	63.2
429.mcf	4	61.6
429.mcf	4	64.1
445.gobmk	4	79.7
445.gobmk	4	73.9
456.hmmer	4	102
456.hmmer	4	59.5
458.sjeng	4	76.9
458.sjeng	4	70.2
462.libquantum	1	85.7
462.libquantum	4	41.3
464.h264ref	4	125
464.h264ref	4	118
471.omnetpp	4	51.0
471.omnetpp	4	48.6
473.astar	4	52.2
473.astar	4	47.5
483.xalancbmk	4	79.2

SPECint_rate_base2006 = 64.0

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Hardware

CPU Name: Intel Xeon X3360
CPU Characteristics: 1333 MHz system bus
CPU MHz: 2833
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CAS 6-6-6, with ECC)
Disk Subsystem: Western Digital WD5000AAKS (SATA, 500GB, 7200rpm)
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel: Yes
File System: ext2
System State: Multiuser, Runlevel 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	528	74.0	**528**	**74.0**	527	74.1	4	**444**	**88.0**	443	88.2	445	87.8
401.bzip2	4	678	56.9	680	56.7	**678**	**56.9**	4	632	61.0	637	60.6	**635**	**60.8**
403.gcc	4	509	63.3	**510**	**63.2**	518	62.2	4	511	63.0	**512**	**62.8**	513	62.8
429.mcf	4	572	63.8	**570**	**64.1**	569	64.2	4	593	61.6	**592**	**61.6**	590	61.8
445.gobmk	4	570	73.6	558	75.2	**568**	**73.9**	4	520	80.7	527	79.7	**526**	**79.7**
456.hmmer	4	628	59.5	627	59.5	**627**	**59.5**	4	364	102	364	103	**364**	**102**
458.sjeng	4	693	69.8	689	70.3	**689**	**70.2**	4	627	77.2	**629**	**76.9**	630	76.8
462.libquantum	4	**2009**	**41.3**	2011	41.2	2006	41.3	1	238	87.2	**242**	**85.7**	272	76.1
464.h264ref	4	**747**	**118**	746	119	749	118	4	709	125	709	125	**709**	**125**
471.omnetpp	4	**515**	**48.6**	513	48.7	516	48.5	4	489	51.1	491	50.9	**490**	**51.0**
473.astar	4	**591**	**47.5**	592	47.4	591	47.5	4	538	52.2	**538**	**52.2**	540	52.0
483.xalancbmk	4	**349**	**79.2**	348	79.2	349	79.1	4	**349**	**79.2**	348	79.2	349	79.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'OMP_NUM_THREADS' set to number of cores (default)
```

General Notes

```
This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

    -fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

    -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

    icc

    401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
        -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
        -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

    456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
        -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
        -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

    icpc

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias`
`-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo`
`-no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
`-opt-streaming-stores always -vec-guard-write`
`-opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.04.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.04.xml`

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