



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant ML350 G5
(3.16 GHz, Intel Xeon X5460)

SPECint®_rate2006 = **NC**

SPECint_rate_base2006 = **NC**

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jan-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

	Copies
400.perlbench	
401.bzip2	
403.gcc	
429.mcf	
445.gobmk	
456.hmmer	
458.sjeng	
462.libquantum	
464.h264ref	
471.omnetpp	
473.astar	
483	

Non-Compliant

Hardware

CPU Name: Intel Xeon X5460
CPU Characteristics: 3.16 GHz, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz: 3166
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for applications running on IA-32 and Intel 64, Version 10.1 Build 20070913 Package ID: 1_cc_p_10.1.008
Auto Parallel: Yes
File System: ext2
System State: Multi-user run level 3

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Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 1x72 GB 15 K SAS
Other Hardware: None

Base Processors: 32-bit
Link Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 8.1
binutils-2.17.50

Result Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
401.bzip2	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
403.gcc	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
429.mcf	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
445.gobmk	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
456.hmmer	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
458.sjeng	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
462.libquantum	4	NC	NC	NC	NC	NC	NC	1	NC	NC	NC	NC	NC	NC		
464.h264ref	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
471.omnetpp	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
473.astar	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		
483.xs	4	NC	NC	NC	NC	NC	NC	4	NC	NC	NC	NC	NC	NC		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline -llo -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.quantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

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Peak Optimization Flags (Continued)

```

429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
         -no-prec-div -ansi-alias
456.hmmer: -fast -unroll -ansi-alias -opt-multi-version-aggressive
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll -O0 -prefetch
               -opt-streaming-primitives -always -vec-guard-write
               -opt-malloc-options-3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
             -ansi-alias
C++ benchmarks:
471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
            -no-prec-div -ansi-alias -opt-ra-region-strategy=block
            -Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
          -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
          -Wl,-z,muldefs -L/cpu2006/SmartHeap_8.1/lib -lsmartheap
483.xalancbmk: basepeak = yes

```

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html>

You can also download the XML file source by using the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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