SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY RX600 S4, Intel Xeon E7330, 2.40 GHz

SPECint®_rate2006 =   177

SPECint_rate_base2006 = 151
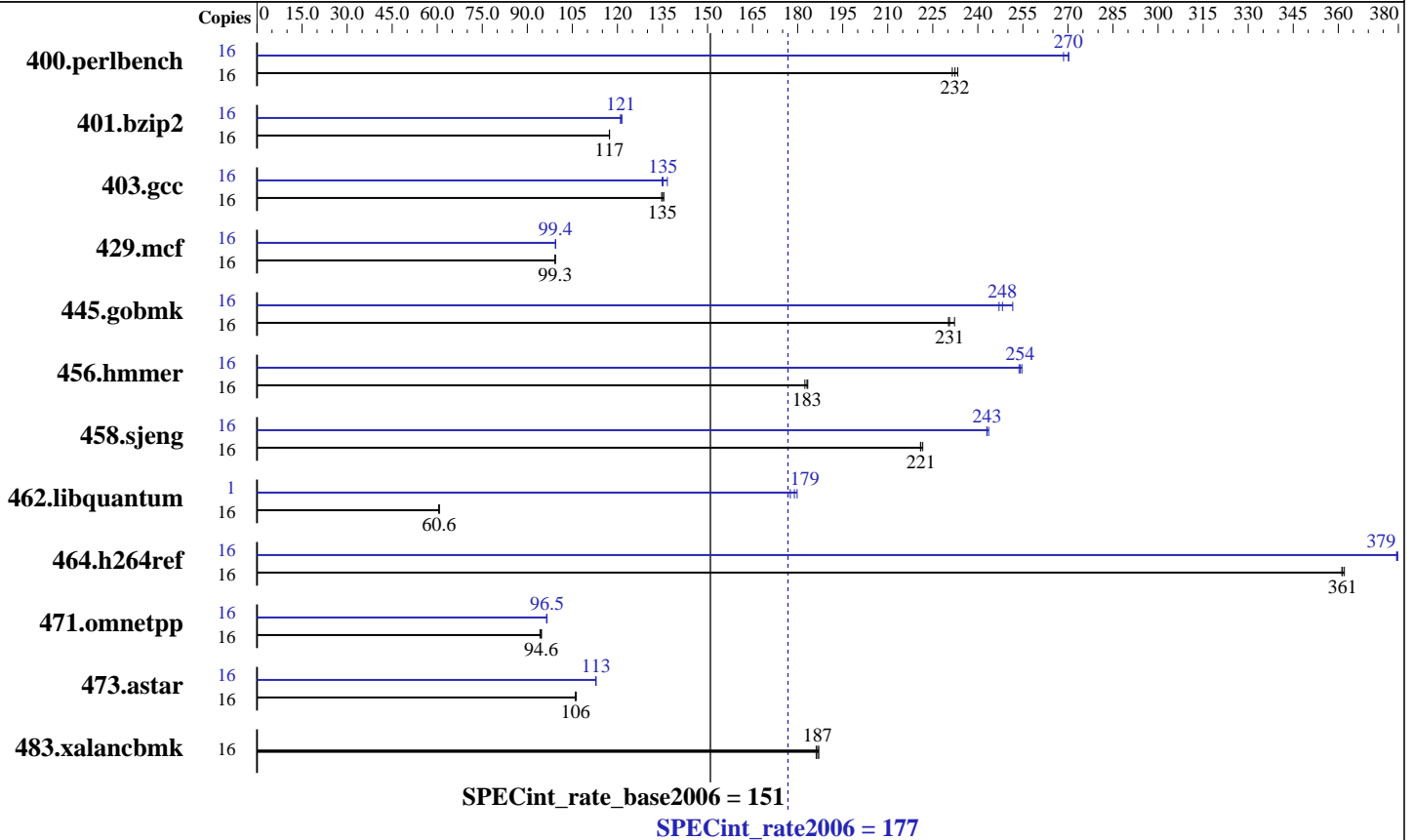
CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jan-2008
Hardware Availability: Dec-2007
Software Availability: Nov-2007

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Hardware

CPU Name:	Intel Xeon E7330
CPU Characteristics:	1067 MHz system bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	1,2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip, 3 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	64 GB (16x4 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel:	Yes
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**673**	**232**	675	231	670	233	16	**579**	**270**	578	270	582	269
401.bzip2	16	1316	117	**1316**	**117**	1316	117	16	**1274**	**121**	1271	121	1276	121
403.gcc	16	956	135	**954**	**135**	951	135	16	955	135	943	137	**953**	**135**
429.mcf	16	**1470**	**99.3**	1469	99.3	1470	99.2	16	1468	99.4	**1468**	**99.4**	1469	99.4
445.gobmk	16	723	232	729	230	**728**	**231**	16	667	252	679	247	**676**	**248**
456.hmmer	16	818	182	814	183	**815**	**183**	16	**587**	**254**	588	254	586	255
458.sjeng	16	876	221	**876**	**221**	873	222	16	795	244	797	243	**797**	**243**
462.libquantum	16	5478	60.5	**5467**	**60.6**	5464	60.7	1	**116**	**179**	117	178	115	180
464.h264ref	16	978	362	**980**	**361**	980	361	16	933	379	932	380	**933**	**379**
471.omnetpp	16	1062	94.2	**1057**	**94.6**	1056	94.7	16	1036	96.6	1037	96.4	**1036**	**96.5**
473.astar	16	1060	106	1057	106	**1059**	**106**	16	995	113	**995**	**113**	996	113
483.xalancbmk	16	590	187	**591**	**187**	593	186	16	590	187	**591**	**187**	593	186

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'OMP_NUM_THREADS' set to number of cores (default)

General Notes

This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

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Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
    -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc
```

```
    401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
              -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
              -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

    456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
              -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
              -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
    icpc
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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