



SPEC® CINT2006 Result

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Acer Incorporated

SPECint_rate2006 = 113

Acer Altos G540 (Intel Xeon X5365)

SPECint_rate_base2006 = 95.2

CPU2006 license: 97

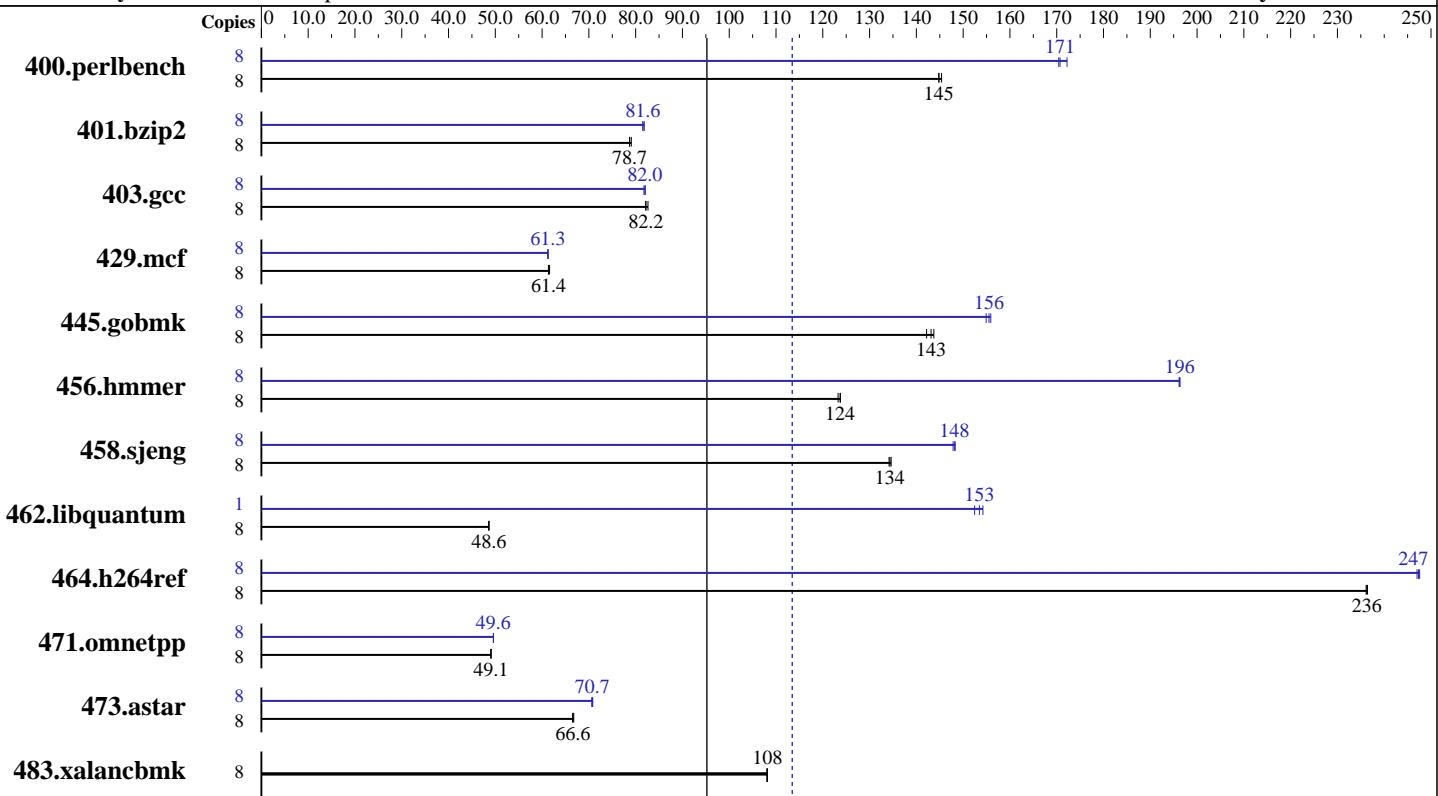
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2007

Hardware Availability: Nov-2006

Software Availability: Nov-2007



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Hardware

CPU Name: Intel Xeon X5365
CPU Characteristics: 1333MHz system bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB L1 + 32 KB D on chip per core
Secondary Cache: 8 MB L2+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2048 MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73 GB 10000RPM SAS HDD
Other Hardware: None

Software

Operating System: SUSE Enterprise Linux Server 10 (x86_64)
Compiler: Kernel 2.6.16.21-0.8-smp
Intel C++ Compiler 10.1 for Linux
Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1
Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	538	145	540	145	540	145	8	458	171	459	170	454	172
401.bzip2	8	981	78.7	976	79.1	981	78.7	8	947	81.5	943	81.8	946	81.6
403.gcc	8	784	82.1	779	82.6	783	82.2	8	785	82.1	786	82.0	787	81.8
429.mcf	8	1184	61.6	1188	61.4	1189	61.4	8	1191	61.3	1191	61.3	1192	61.2
445.gobmk	8	586	143	584	144	590	142	8	542	155	538	156	540	156
456.hammer	8	606	123	603	124	603	124	8	380	196	380	196	380	196
458.sjeng	8	720	134	719	135	722	134	8	653	148	653	148	655	148
462.libquantum	8	3409	48.6	3409	48.6	3409	48.6	1	136	152	135	153	134	154
464.h264ref	8	749	236	750	236	749	236	8	715	248	717	247	716	247
471.omnetpp	8	1018	49.1	1019	49.1	1019	49.1	8	1008	49.6	1008	49.6	1008	49.6
473.astar	8	841	66.8	843	66.6	844	66.5	8	794	70.7	793	70.8	795	70.6
483.xalancbmk	8	510	108	511	108	510	108	8	510	108	511	108	510	108

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode

All results used taskset to bind processes to cores

"ulimit -s unlimited" was set for stacksize unlimited

OMP_NUM_THREADS set to number of cores

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:

```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll12 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll14 -O0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.11.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.11.xml>

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Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 15:53:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 19 February 2008.