



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 134

Acer Altos R720 (Intel Xeon X5460)

SPECint_rate_base2006 = 108

CPU2006 license: 97

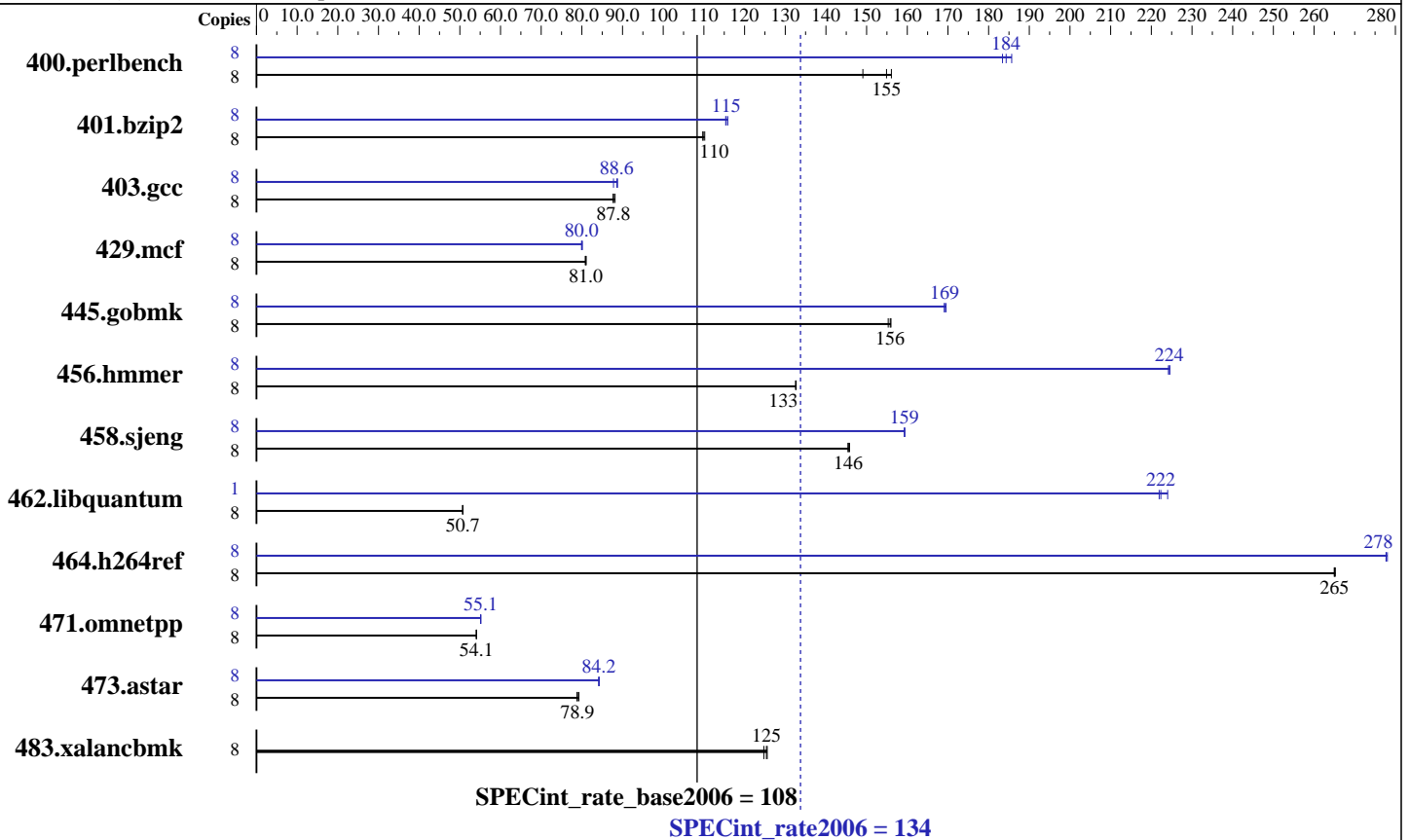
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3166
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
 Disk Subsystem: 1 x 73GB 10000PRM SAS HDD
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 (x86_64)
 Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	524	149	<u>505</u>	<u>155</u>	501	156	8	<u>424</u>	<u>184</u>	426	183	421	186
401.bzip2	8	703	110	700	110	<u>703</u>	<u>110</u>	8	666	116	<u>668</u>	<u>115</u>	669	115
403.gcc	8	735	87.7	730	88.2	<u>734</u>	<u>87.8</u>	8	<u>727</u>	<u>88.6</u>	725	88.8	734	87.8
429.mcf	8	903	80.8	<u>901</u>	<u>81.0</u>	900	81.1	8	911	80.1	913	79.9	<u>912</u>	<u>80.0</u>
445.gobmk	8	538	156	<u>539</u>	<u>156</u>	540	155	8	495	170	496	169	<u>496</u>	<u>169</u>
456.hmmer	8	<u>563</u>	<u>133</u>	563	133	563	133	8	<u>333</u>	<u>224</u>	332	225	333	224
458.sjeng	8	666	145	<u>665</u>	<u>146</u>	664	146	8	607	159	608	159	<u>608</u>	<u>159</u>
462.libquantum	8	3269	50.7	<u>3269</u>	<u>50.7</u>	3269	50.7	1	<u>93.2</u>	<u>222</u>	92.5	224	93.4	222
464.h264ref	8	<u>668</u>	<u>265</u>	668	265	667	265	8	637	278	<u>637</u>	<u>278</u>	638	278
471.omnetpp	8	925	54.1	<u>925</u>	<u>54.1</u>	924	54.1	8	906	55.2	907	55.1	<u>907</u>	<u>55.1</u>
473.astar	8	<u>712</u>	<u>78.9</u>	713	78.8	708	79.3	8	<u>667</u>	<u>84.2</u>	668	84.1	667	84.3
483.xalancbmk	8	443	125	<u>440</u>	<u>125</u>	440	126	8	443	125	<u>440</u>	<u>125</u>	440	126

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Acer Altos R520.
The Altos R720 and Altos R520 are electronically equivalent.

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

BIOS Settings

Hardware Prefetcher set to ON

Adjacent Cache Line Prefetch set to OFF

All results used taskset to bind processes to cores.
"ulimit -s unlimited" was set for stacksize unlimited
OMP_NUM_THREADS set to number of cores

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.08.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.08.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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