



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 128

PowerEdge M600 (Intel Xeon X5460, 3.16 GHz)

SPECint_rate_base2006 = 114

CPU2006 license: 55

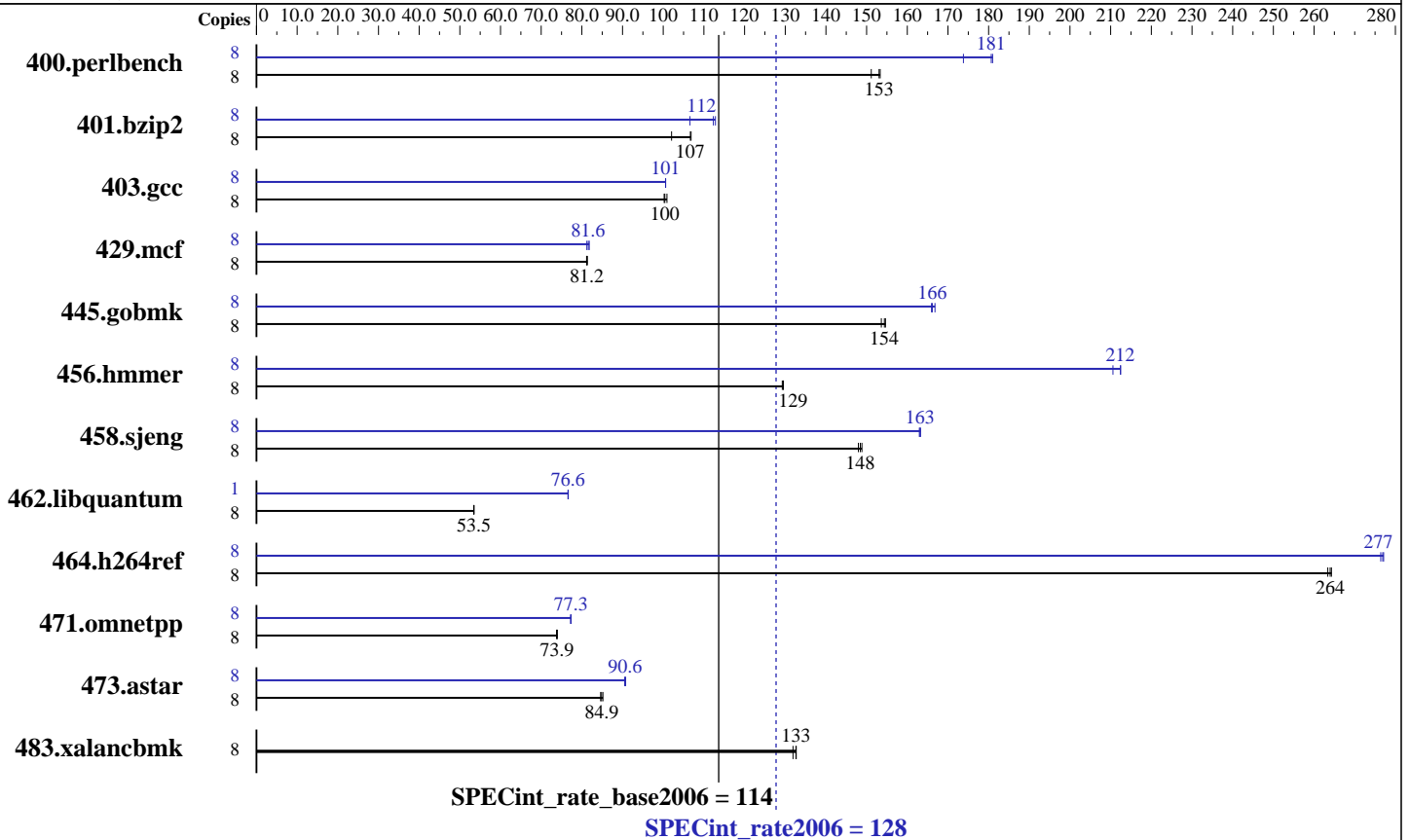
Test date: Dec-2007

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3166
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB DDR2-667 ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 80 GB 7.2k SATA
 Other Hardware: PowerEdge M1000e blade chassis

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	510	153	517	151	<u>511</u>	<u>153</u>	8	450	174	432	181	<u>433</u>	<u>181</u>
401.bzip2	8	756	102	<u>724</u>	<u>107</u>	723	107	8	724	107	<u>687</u>	<u>112</u>	684	113
403.gcc	8	638	101	643	100	<u>641</u>	<u>100</u>	8	640	101	<u>640</u>	<u>101</u>	640	101
429.mcf	8	897	81.3	899	81.2	<u>898</u>	<u>81.2</u>	8	<u>894</u>	<u>81.6</u>	892	81.8	898	81.2
445.gobmk	8	543	155	546	154	<u>544</u>	<u>154</u>	8	<u>505</u>	<u>166</u>	506	166	503	167
456.hammer	8	<u>576</u>	<u>129</u>	576	130	577	129	8	354	211	351	212	<u>351</u>	<u>212</u>
458.sjeng	8	650	149	654	148	<u>652</u>	<u>148</u>	8	<u>593</u>	<u>163</u>	594	163	593	163
462.libquantum	8	<u>3101</u>	<u>53.5</u>	3101	53.5	3101	53.4	1	270	76.7	<u>270</u>	<u>76.6</u>	271	76.6
464.h264ref	8	672	263	670	264	<u>671</u>	<u>264</u>	8	641	276	<u>639</u>	<u>277</u>	639	277
471.omnetpp	8	<u>677</u>	<u>73.9</u>	676	74.0	678	73.8	8	647	77.3	647	77.2	<u>647</u>	<u>77.3</u>
473.astar	8	<u>662</u>	<u>84.9</u>	659	85.2	664	84.6	8	<u>620</u>	<u>90.6</u>	620	90.5	619	90.8
483.xalancbmk	8	418	132	<u>416</u>	<u>133</u>	416	133	8	418	132	<u>416</u>	<u>133</u>	416	133

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)
Hardware Prefetcher = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.12.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.12.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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