



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 19.2

Lenovo ThinkPad T61 (Intel Core 2 Duo T7800)

SPECint_base2006 = 17.5

CPU2006 license: 13

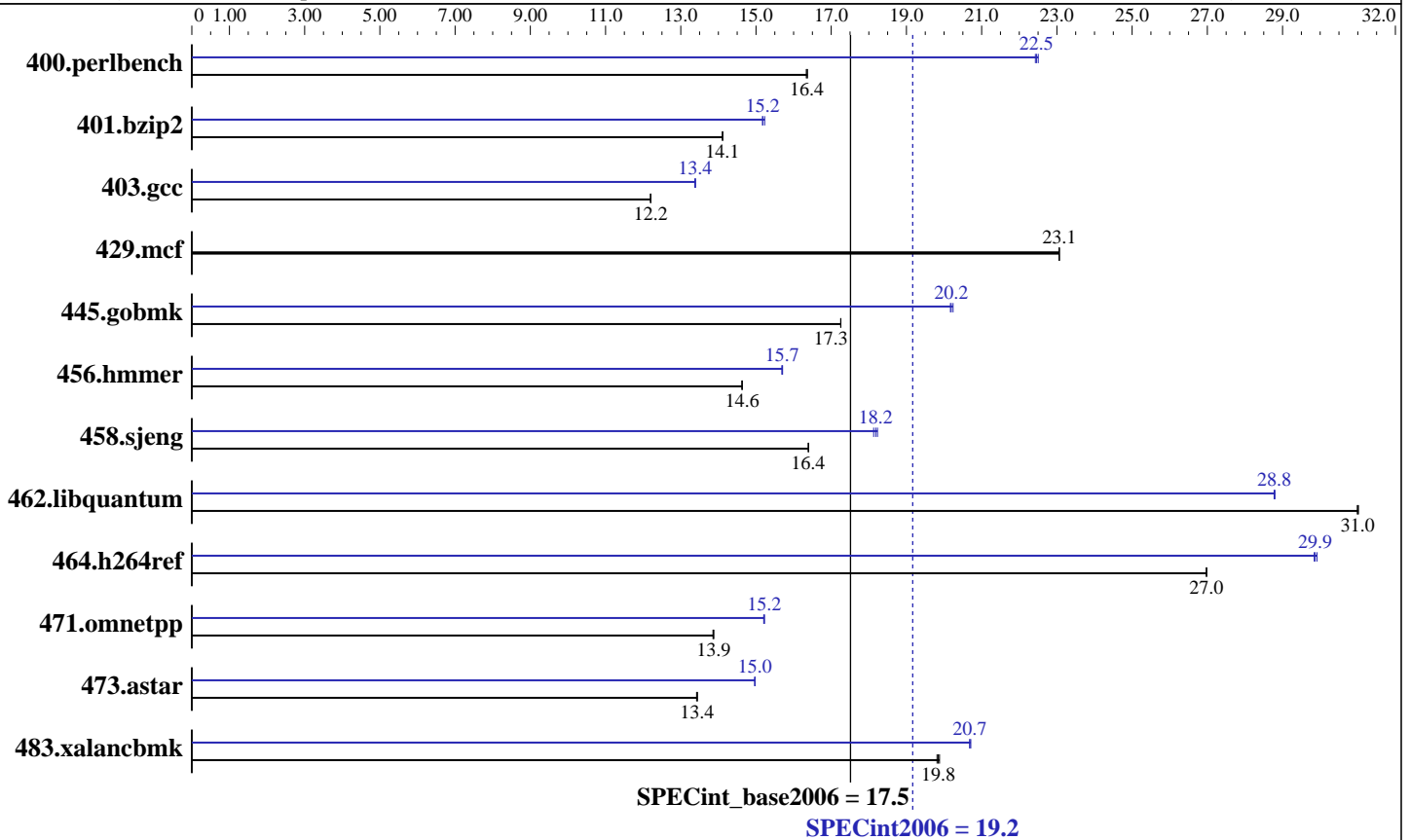
Test date: Dec-2007

Test sponsor: Intel Corporation

Hardware Availability: Jan-2008

Tested by: Intel Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo T7800
 CPU Characteristics:
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Hynix DDR2-667 CL5)
 Disk Subsystem: Hitachi 100 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: None
 SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	598	16.3	<u>597</u>	<u>16.4</u>	597	16.4	434	22.5	436	22.4	<u>435</u>	<u>22.5</u>
401.bzip2	<u>684</u>	<u>14.1</u>	684	14.1	684	14.1	634	15.2	<u>636</u>	<u>15.2</u>	636	15.2
403.gcc	660	12.2	660	12.2	<u>660</u>	<u>12.2</u>	601	13.4	<u>601</u>	<u>13.4</u>	602	13.4
429.mcf	396	23.1	395	23.1	<u>395</u>	<u>23.1</u>	396	23.1	395	23.1	<u>395</u>	<u>23.1</u>
445.gobmk	608	17.3	<u>608</u>	<u>17.3</u>	608	17.3	520	20.2	518	20.2	<u>520</u>	<u>20.2</u>
456.hammer	638	14.6	638	14.6	<u>638</u>	<u>14.6</u>	594	15.7	595	15.7	<u>594</u>	<u>15.7</u>
458.sjeng	<u>738</u>	<u>16.4</u>	738	16.4	738	16.4	667	18.1	664	18.2	<u>665</u>	<u>18.2</u>
462.libquantum	669	31.0	668	31.0	<u>668</u>	<u>31.0</u>	720	28.8	<u>720</u>	<u>28.8</u>	720	28.8
464.h264ref	820	27.0	<u>820</u>	<u>27.0</u>	820	27.0	<u>741</u>	<u>29.9</u>	740	29.9	742	29.8
471.omnetpp	<u>451</u>	<u>13.9</u>	450	13.9	451	13.9	411	15.2	411	15.2	<u>411</u>	<u>15.2</u>
473.astar	523	13.4	522	13.4	<u>522</u>	<u>13.4</u>	469	15.0	<u>469</u>	<u>15.0</u>	469	15.0
483.xalanbmk	348	19.8	<u>348</u>	<u>19.8</u>	347	19.9	334	20.7	<u>334</u>	<u>20.7</u>	333	20.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The system bus runs at 800 MHz
 Binaries were built on Windows Vista (32-bit)
 The following VS 2005 SP1 updates were applied: KB926601 and KB932232
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to physical,0

Base Compiler Invocation

C benchmarks:
 icl -Qvc8 -Qc99

C++ benchmarks:
 icl -Qvc8

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml>



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